

# Lifting to Parity Decision Trees via Stifling

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Proof Complexity



Parity Decision Trees

## Proof Complexity

every assignment falsifies a clause

Given an unsatisfiable CNF over  $n$  variables,  
prove that there is no satisfying assignment.

Should require large "proofs" assuming  $NP \neq \text{co}NP$ .

For what defn of "proof" can we show this?

False Clause  $\mathcal{C}_e \subseteq \{0,1\}^n \times [m]$   
↑ assignment  $x$       ↑ index of clause in  $\mathcal{C} = \bigwedge_{i \in [m]} C_i$

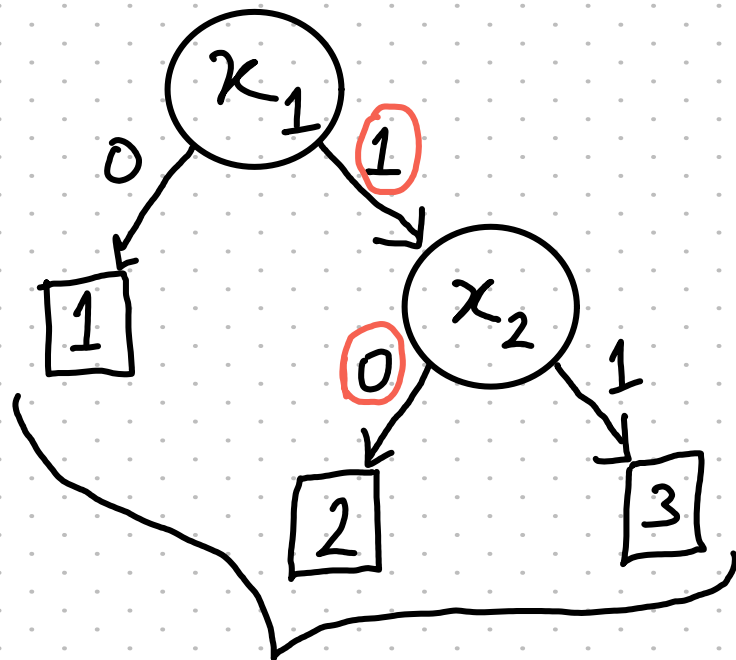
$(x, i) \in FC_{\mathcal{C}_e}$  if  $C_i(x) = \text{False}$

To prove:  $FC_{\mathcal{C}_e}$  is total

$(\forall x \exists i (x, i) \in FC_{\mathcal{C}_e})$

# Decision Trees are Proofs

$$T: \{0,1\}^2 \rightarrow [3]$$



$$\forall x \quad T(x) \in [3]$$

$$x_1 \wedge (\neg x_1 \vee x_2) \wedge \neg x_2$$

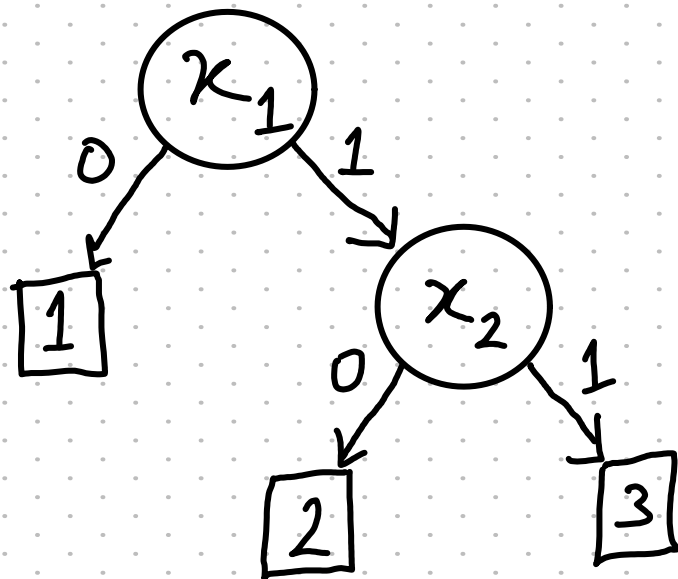
Need to verify:

For each leaf  $l$ , the inputs that reach  $l$  falsify the clause output at  $l$ .

DT of size  $s$  is a proof of size  $s$ .

# Decision Trees are Tree-like Resolution Proofs

$$T: \{0,1\}^2 \rightarrow [3]$$

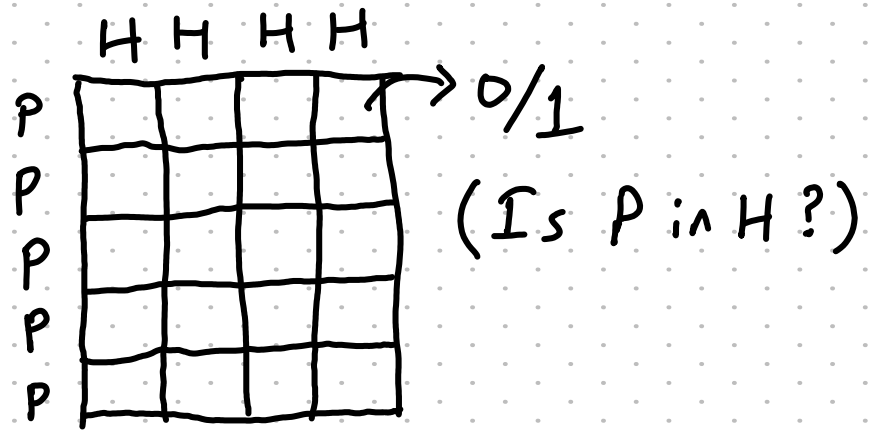


$$x_1 \wedge (\neg x_1 \vee x_2) \wedge \neg x_2$$

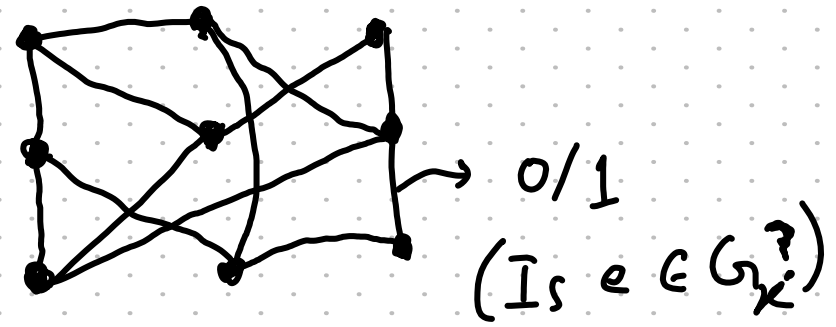
$$\frac{x_1 \quad \frac{\neg x_1 \vee x_2 \quad \neg x_2}{\quad}}{\perp}$$

# PigeonHole Principle

- Each Pigeon is in a Hole
- No Hole has  $>1$  Pigeon



Tseitin on a const. degree graph  $G$  with an odd number of vertices

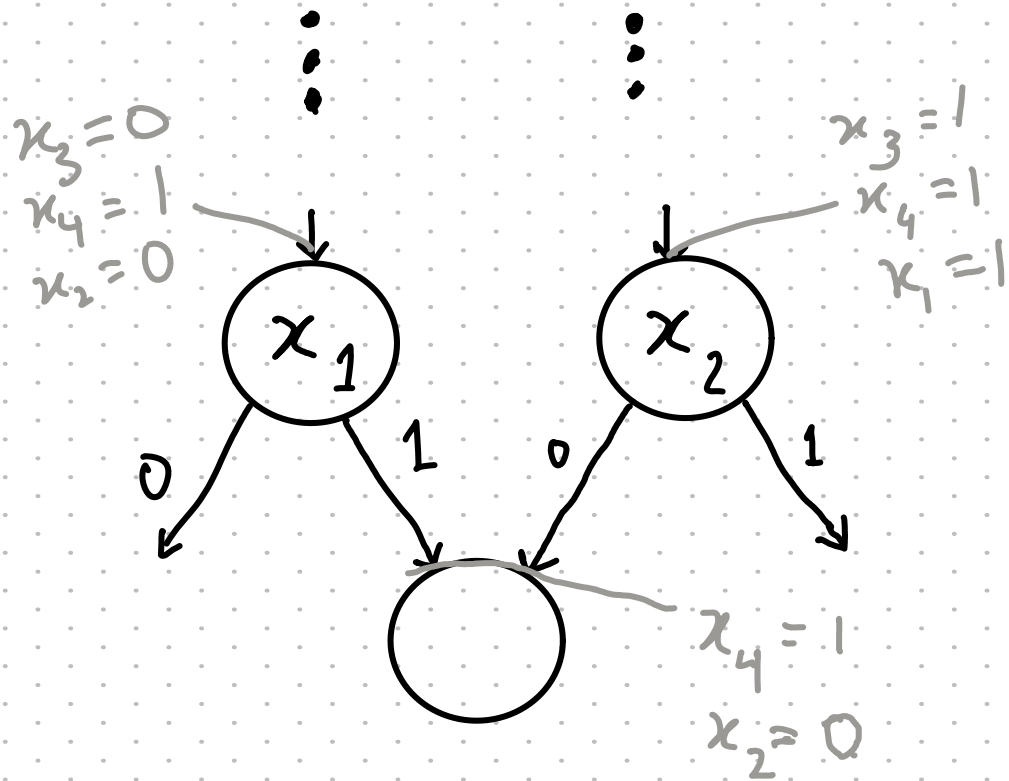


- Every vertex has odd degree

Hard when  $G$  is an expander.



# Decision DAGs are Resolution Proofs

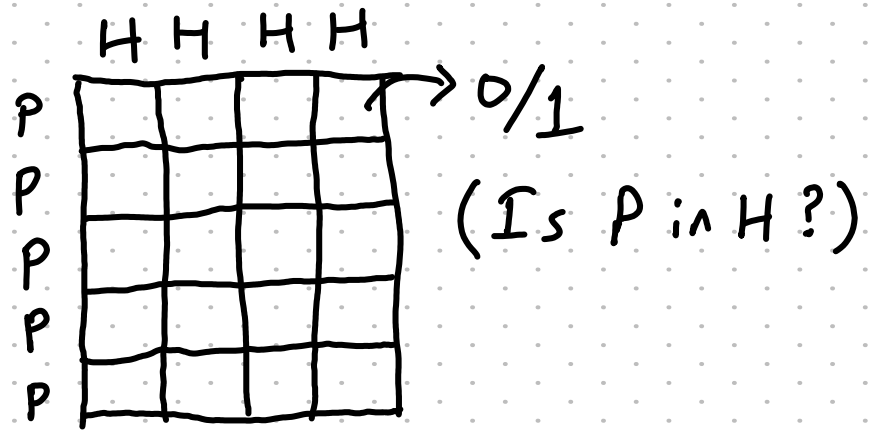


DAG structure allows reusing of proof lines.

Can give exp  $\rightarrow$  poly size reductions for some CNFs!!

# PigeonHole Principle

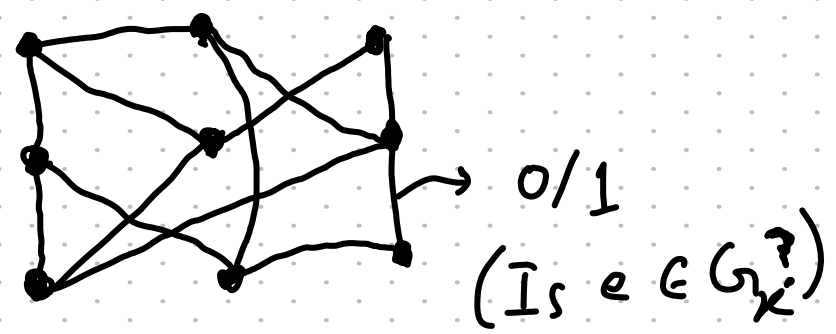
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Hard even for DAGs. [Halpern '85]

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Tseitin on a const. degree graph  $G$  with an odd number of vertices

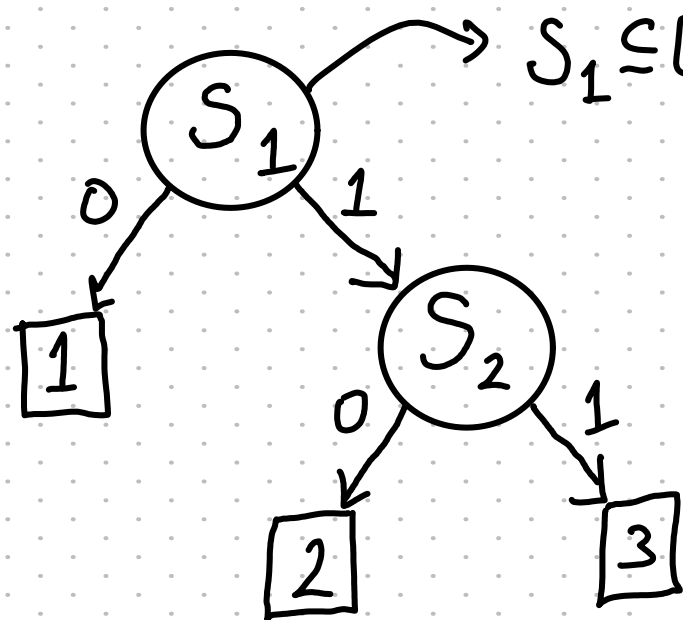


- Every vertex has odd degree

Hard even for DAGs. [Ben-Sasson, Wigderson '01]

# Parity Decision Trees

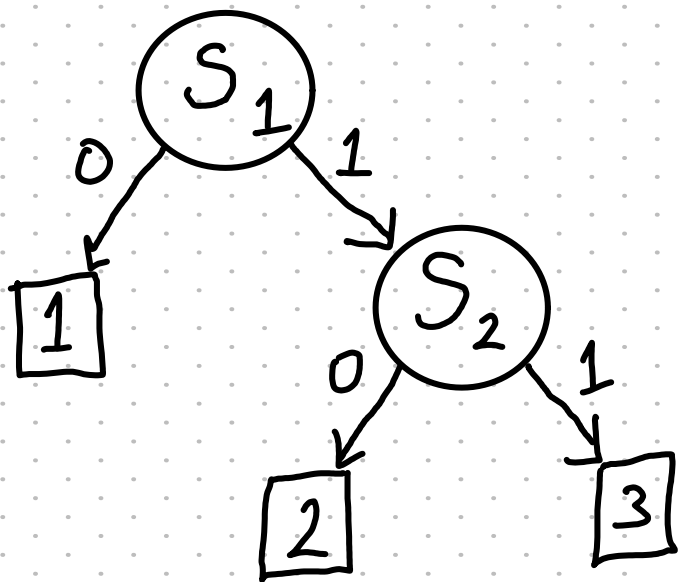
$$T: \{0,1\}^2 \rightarrow [3]$$



$S_1 \subseteq [n]$ , query answer is  $\bigoplus_{i \in S_1} x_i$

# Parity Decision Trees are Tree-like Res( $\oplus$ ) Proofs

$$T: \{0,1\}^2 \rightarrow [3]$$



Can define Parity DAGs  
 $\equiv$   
 Res( $\oplus$ )

Line: Disjunction of parities

Example:  $(x_1 \oplus x_2 = 0) \vee (x_1 \oplus x_3 \oplus x_5 = 1)$   
 $\equiv$

$$(x_1 \oplus x_2 = 0) \vee (x_2 \oplus x_3 \oplus x_5 = 0)$$

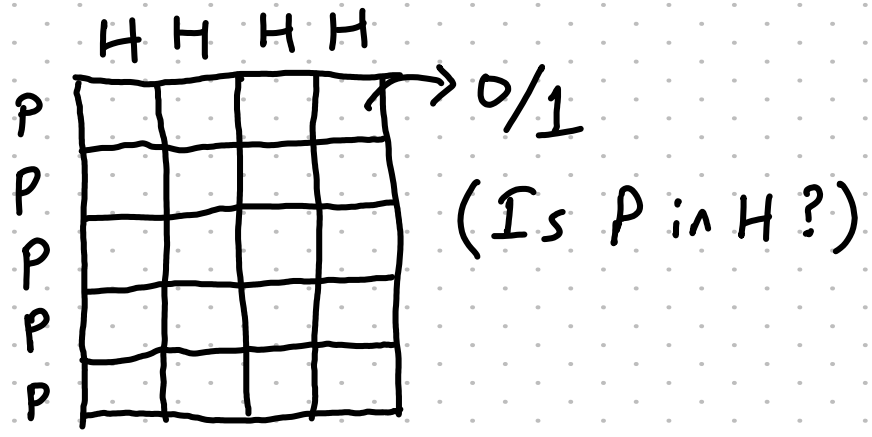
Resolution:

$$\frac{\Phi_1 \vee (P=0) \quad \Phi_2 \vee (P=1)}{\Phi_1 \vee \Phi_2}$$

Rebase:

$$\frac{\Phi \vee P_1 \vee P_2}{\Phi \vee P_1 \vee (\neg P_1 \oplus P_2)}$$

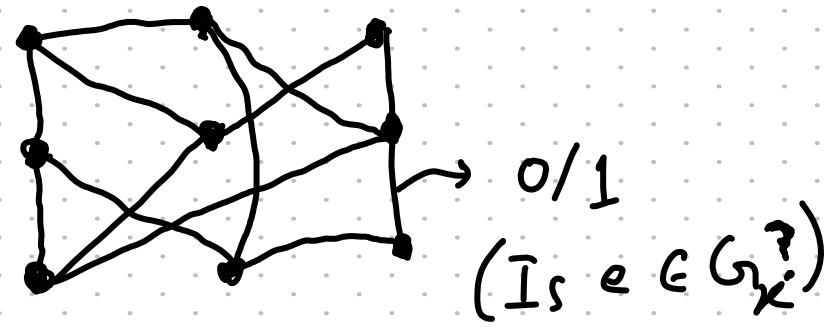
# PigeonHole Principle



- Each Pigeon is in a Hole
- No Hole has  $>1$  Pigeon

Hard even for PDTs. [IS '14]

Tseitin on a const. degree graph  $G$  with an odd number of vertices



- Every vertex has odd degree

Easy for PDTs.

No CNF is known to be hard for Parity DAGs!

## Hardness of Parity LBs

- Parity constraints are non-local correlations  
Hard to keep track and reason about.
- There are  $2^n$  parity queries that can be asked.  
The Decision DAG size-width relation proof  
no longer works.

How can we tame the power of these parities?

This talk: A look at some parity-poopers.

# Lower Bounds for Splitting by Linear Combinations

- Dmitry Itsykson & Dmitry Sokolov

- 1) Lower bound for Pigeon Hole Principle
- 2) Lower bound for CNF related to Tseitin
- 3) Other interesting results



# 1) Lower bound for Pigeon Hole Principle

Weakness of Parity query exploited:



If  $k$  parity constraints are set and



then for any  $k+1$  indices there is a different solution differing only in those indices. Add  $n-k+1$  constraints.

Used to say that outputting "Pigeon  $P$  is in a Hole" as a false clause is hard.

## 2) Lower bound for CNF related to Tseitin

Tseitin variables:

$x_e$  for  $e$  in  $G$

F variables:

$y_{e,1}, y_{e,2}$  for  $e$  in  $G$

Let  $x_e := y_{e,1} \wedge y_{e,2}$

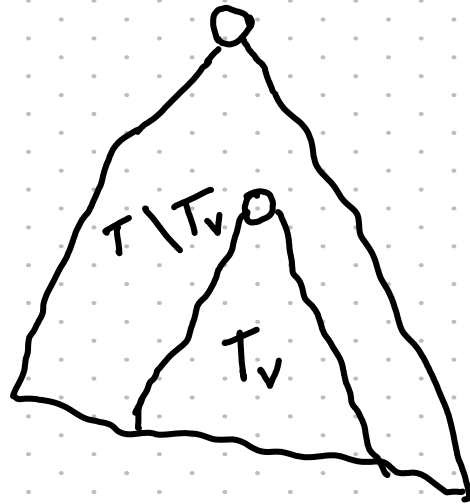
"Vertex  $v$  has odd degree" is a fn of  $2 \cdot \deg(v)$  variables.

$\Rightarrow$  Can be written as a CNF with  $2^{2 \cdot \deg(v)}$  clauses of width  $2 \cdot \deg(v)$

$\therefore$ ,  $F$  has  $O(m)$  clauses, const. width.

Weakness of Parity query exploited:

Small size PDT  $\Rightarrow$  Low rand. comm. comp.



Can check if input goes to  $T_v$  or not with const. communication.

$$R^{cc}(FC_e) \leq \log \text{PDT size}(FC_e)$$

Can embed DIST on  $n^{1/3}$  bits in  $F$ .

[Beame-Pitassi-Segerlind '07]  $\therefore$  PDT size( $FC_f$ )  $\geq 2^{n^{1/3}}$ .

Overkill???

## Our Contributions:

- A notion of stifling that helps negate <sup>the power of</sup> parity queries
  - A PDT-to-DT simulation theorem with const. size gadgets.
- $\therefore$  For every DT lower bound, a related PDT lower bound.

Parity Queries: Not that global

Consider  $x \in \{0,1\}^n$  (no constraints)

Impose  $x_1 \oplus x_2 \oplus x_3 = 1$ .

Equivalently,  $x_1 = \underbrace{x_2 \oplus x_3 \oplus 1}$ .

Constraint only on  $x_1$ .

$x_2, x_3$  unconstrained.

Set of solns  $\equiv \{0,1\}^{n-1}$ , new constraints are over  $n-1$  bits.

$$\begin{array}{l} x_1 \oplus x_3 \oplus x_5 = 0 \\ \parallel \\ x_2 \oplus x_5 = 1 \end{array}$$

## Parity Query Lower Bound

$$\text{MAJ} : \{0,1\}^7 \rightarrow \{0,1\}$$

Suppose three parity queries were made.

Affects 3 bits. 4 bits still free

Output can be 0.

Output can be 1.

$$\text{PDT}(\text{MAJ}_7) > 3$$

## $k$ -stifled function

$f: \{0,1\}^n \rightarrow \{0,1\}$  is  $k$ -stifled if

$\forall S \subseteq [n], |S| = k$

$\exists$  partial assignments  $x^0, x^1$  to  $[n] \setminus S$

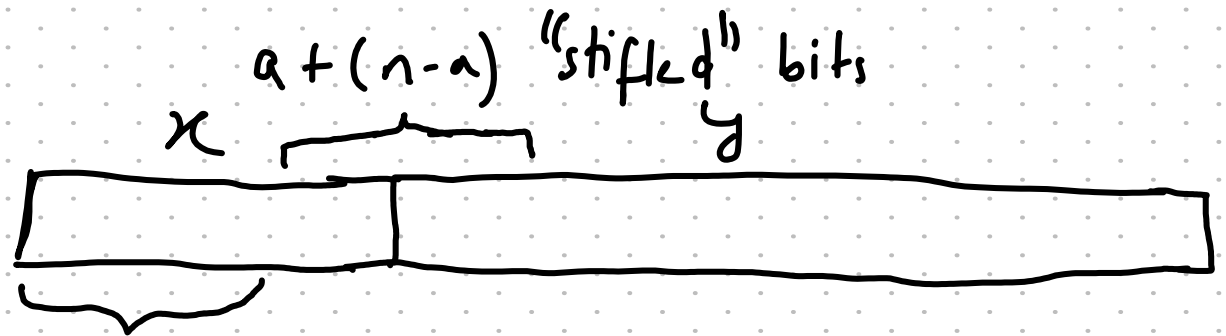
$$f(x^0) = 0, \quad f(x^1) = 1$$

(for every completion)

$\text{MAJ}_{2n+1}$  is  $n$ -stifled  $\quad \text{PDT}(f) > k$

$$\text{IND}: \{0,1\}^n \times \{0,1\}^{2^n} \rightarrow \{0,1\}$$

$$\text{IND}(x, y) = y_x$$



$2^{n-a}$  ways to set these.

At most  $n-a$  of them potentially point to a stifled bit.

Fix a free setting.

Set all corresponding bits in  $y$  to 0/1.



## Our simulation theorem.

Let  $f$  be a  $k$ -stifled function.

For any relation  $R$ ,

- $\text{PDT}(R \circ f) \geq k \cdot \text{DT}(R)$
- $\text{PDT}_{\text{size}}(R \circ f) \geq 2^{k \cdot \text{DT}(R)} \geq \text{DT}_{\text{size}}(R)^k$
- $\text{SubspaceDT}(R \circ f) \geq k \cdot \text{DT}(R)$
- $\text{NAPDT}(R \circ f) \geq k \cdot \text{NADT}(R)$

$R = FC_{Tseitin}$

$$f: \{0,1\}^t \rightarrow \{0,1\}$$

$R$

variables:

$$x_e \text{ for } e \in G$$

$R \circ f$

variables:

$$y_{e,1} \dots y_{e,t} \text{ for } e \in G$$

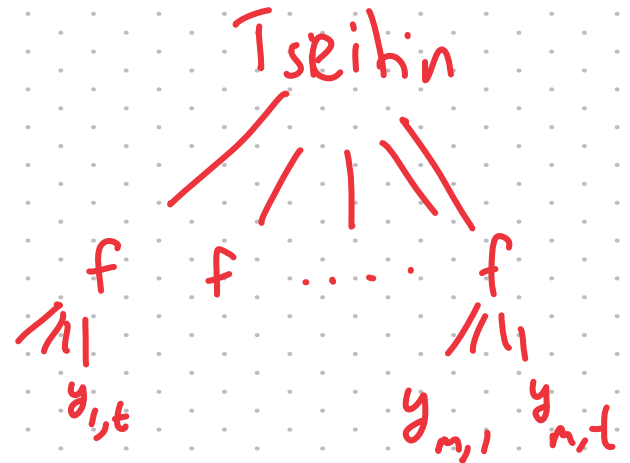
$$x_e := f(y_{e,1} \dots y_{e,t})$$

$$\text{Num of clauses} : \leq 2 \cdot m$$

$$\text{Width of clauses} : \leq t \cdot \text{width}(R)$$

Finding falsified clause for  $\vec{y}$  gives falsified clause for  $\vec{x}$

$R$

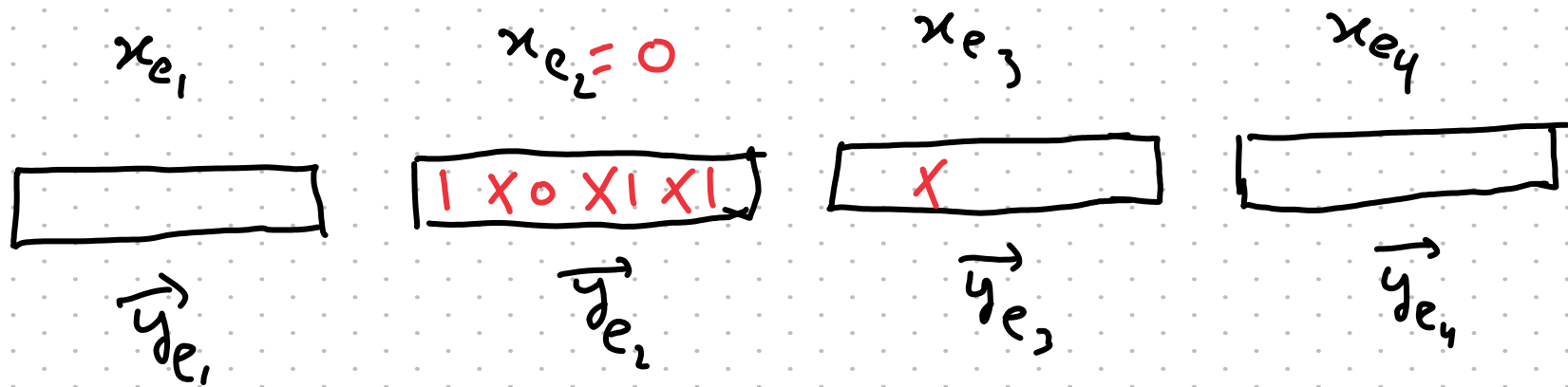


## Proving query lower bounds: The adversary

### Querier - Adversary Game for $R$

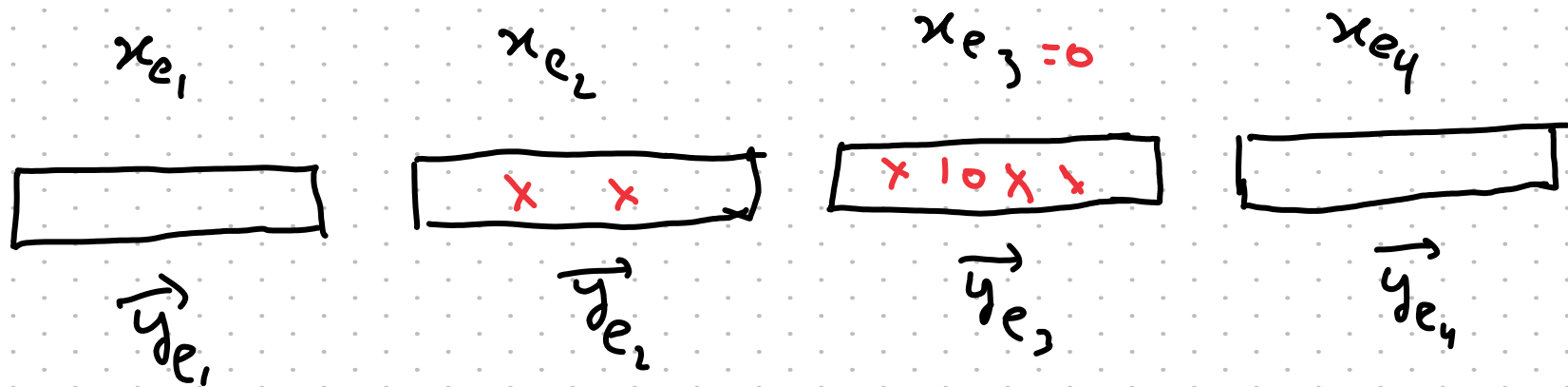
- Querier queries, adversary answers.
- Querier wants to output  $i$  s.t.  
 $\forall x$  consistent with queries answered,  $(x, i) \in R$
- $DT(R) \geq k \iff$  Adversary can force querier to make  $k$  queries.

# Being an adversary for R of:



- When a new parity query is made, answer it arbitrarily and mark a bit as affected.
  - When a block, say  $\vec{y}_{e_i}$ , has  $k$  bits affected, query  $x_{e_i}$ : Set free bits in  $\vec{y}_{e_i}$  to guarantee  $x_{e_i}$ .
- Simulate querier-adversary game for R.

Being an adversary for  $R \circ f$ :



If  $< k \cdot DT(R)$  parity queries made,

$< DT(R)$  queries made to  $R$ -adversary.

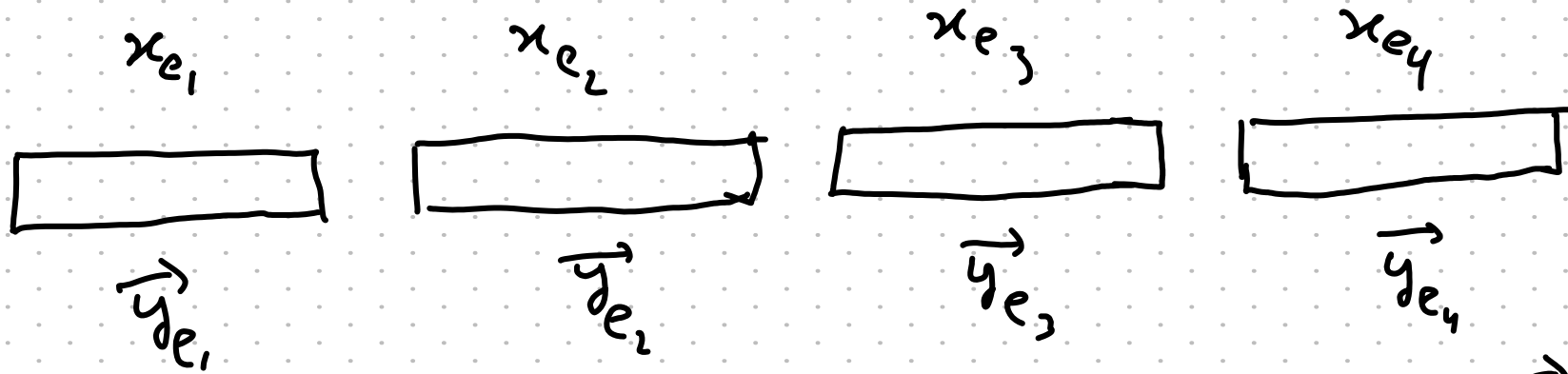
Each unqueried  $x_e$  can be independently set, b/c  $\leq k$  bits affected in each  $y_e$ .

$\therefore$ , we cannot answer  $R$ .

$\therefore$ , we cannot answer  $R \circ f$ .

$$P_{DT}(R \circ f) \geq k \cdot DT(R)$$

# Being an adversary for Rof (size edition):

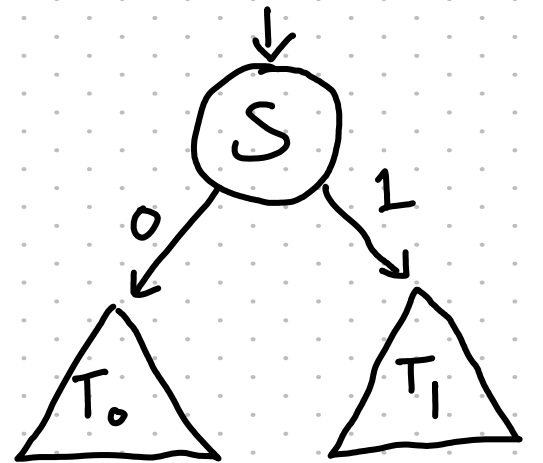


- When a new parity query is made, answer it **arbitrarily** and mark a bit as affected. → go to smaller subtree.

Suppose querier uses opt. sized PDT.

Forces answer in  $\lfloor \log \text{PDT size}(Rof) \rfloor$  queries.

$$\log \text{PDT size}(Rof) > k \cdot \text{DT}(f)$$

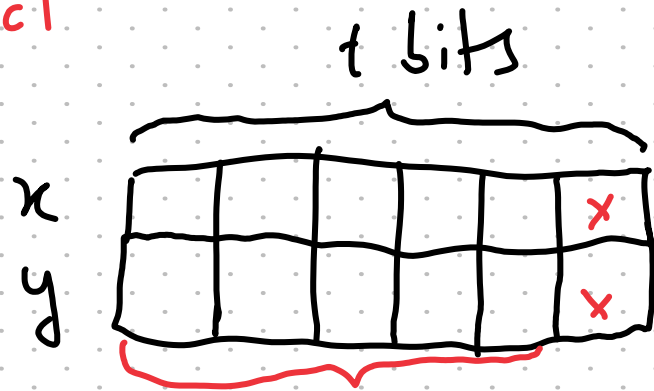


dude this sounds like exactly the sort of stuff we were doing no

On Dispenser/Lifting Properties of the  
Index and Inner-Product Functions  
- Paul Beame and Sajin Koroath

They prove that  $\text{PDT size}(R \circ \text{IND}_3) \geq 2^{\text{DT}(R)}$

# Inner-Product



$$\sum x_i y_i \pmod{2} = ?$$

$$\sum x_i y_i = 6 \pmod{2}$$

Is 1-stifled.

Is not 2-stifled.

$\text{PDT}(R \circ \text{IP})$  ought to be  $\geq t \cdot \text{DT}(R)$ .

What property of  $\oplus$  can we exploit?



## Other Open Problems:

- Can we prove  $\text{MOD}_p\text{DT}$  lower bounds?  
PHP requires large  $\text{MOD}_p\text{DTs}$ . [Part Tzameret '19]
- Can we prove Parity DAG size / width lower bounds?

Open / closed

## Itsykson-Sokolov PHP lower bound

$G \subseteq \{0,1\}^{k \times (k+1)}$  = "assignments where no hole has  $> 1$  pigeon"

Let  $T$  solve  $FC_{PHP}$  w/ the promise that input is in  $G$ .

(Leaves of  $T$  labeled with "pigeon clauses".)

Let  $\lambda$  be a leaf of depth  $< k/2$ . Defined by  $< k/2$  parity constraints.

Let  $S_\lambda \subseteq \{0,1\}^n$  be the subspace corr. to  $\lambda$ .

## Itsykson-Sokolov PHP lower bound

Let  $\lambda$  be a leaf of depth  $< k/2$ . Defined by  $< k/2$  parity constraints.

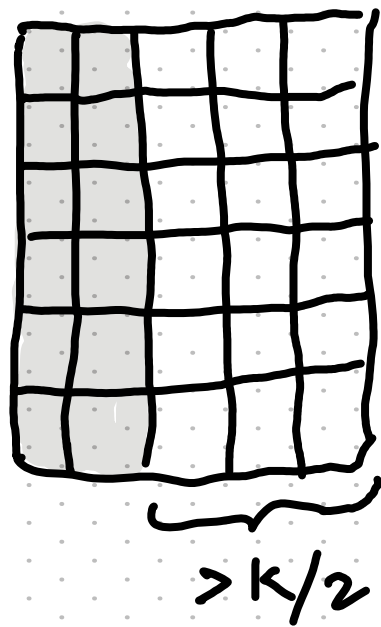
Let  $x \in G \cap S_\lambda$ . If  $|x| \geq k/2$ , can find  $x' \subset x$ ,  $x' \in S_\lambda$  (also  $x \in G$ )

$\exists x' \in G \cap S_\lambda$  with  $|x'| < k/2$ .

So  $> k/2$  empty holes.

$\forall i \exists x'' \in G \cap S_\lambda$  with

Pigeon  $i$  in one of these holes.



$\therefore \text{PDT}_{\text{size}} \geq 2^{\lceil k/2 \rceil}$

## Part - Tzamarat '19

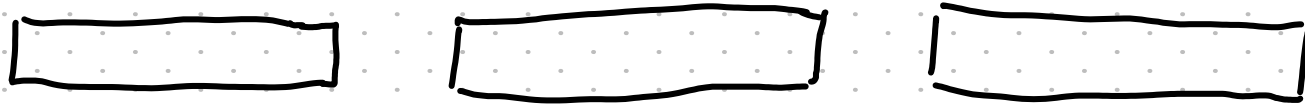
Even with  $\text{MOD}_p$  queries, if  $\exists$  a Boolean assignment satisfying  $K$  constraints, then for any  $I \subseteq [n]$ ,  $|I| > K$ ,  $\exists$  another Boolean assignment differing only in  $I$ .

$\therefore$  as an adversary:

need to ensure a Boolean assignment reaches a leaf.

or  $G =$  "Boolean assignments where no hole has  $> 1$  pigeon"

Our adversary:



The "marked" bit depends on the free bits

The "free" bits are set in order to fix the output value.

The "marked" bit might not be Boolean!