

TFNP

TBD

Proof Complexity, Circuit Complexity, and TFNP

Noah Fleming

Memorial University

Based on work with **Sam Buss** and **Russell Impagliazzo**

Monotone Circuit Complexity

Task

Object

Model



Monotone
Circuit Model M

Monotone Circuit Complexity

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Monotone
Function f

Monotone
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Monotone Circuit Complexity

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Monotone
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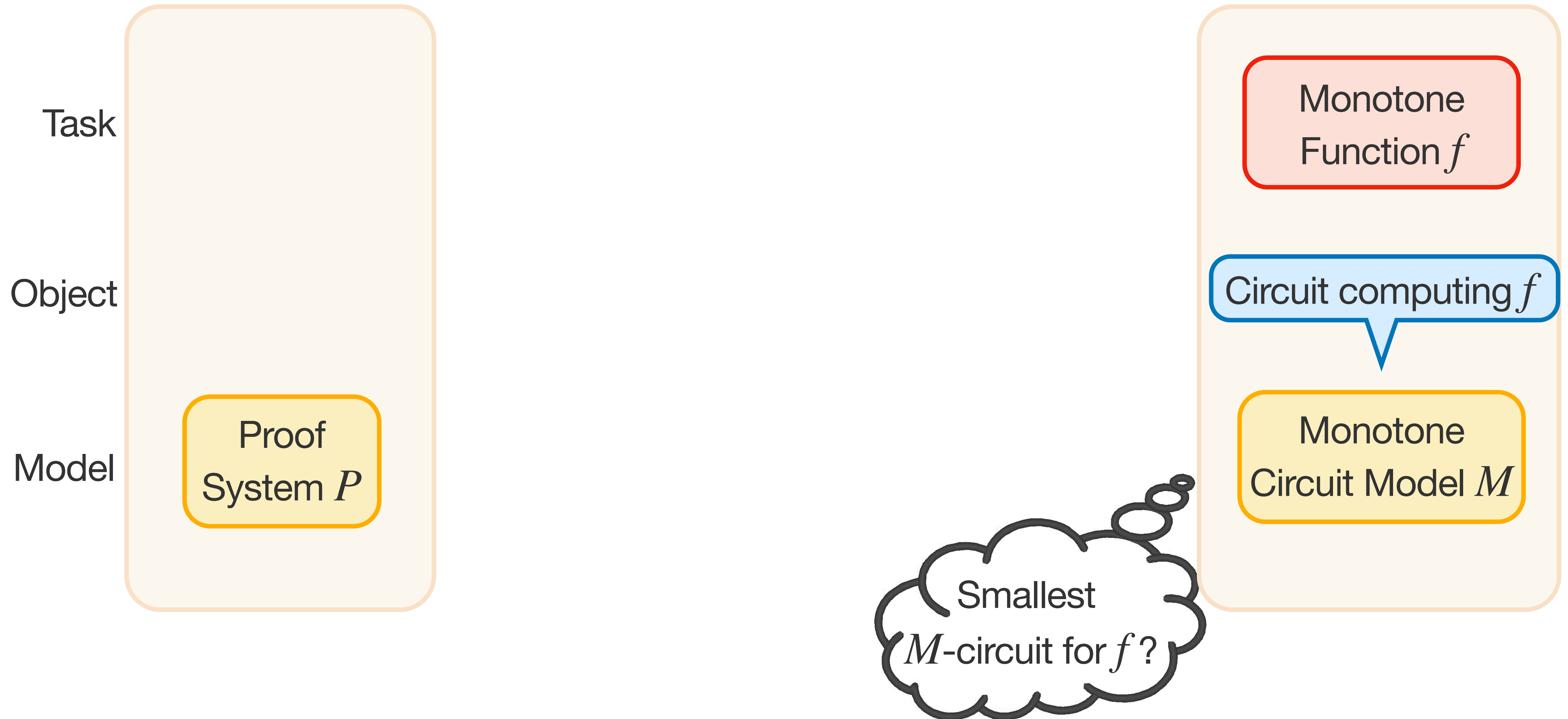
Circuit computing f

Model

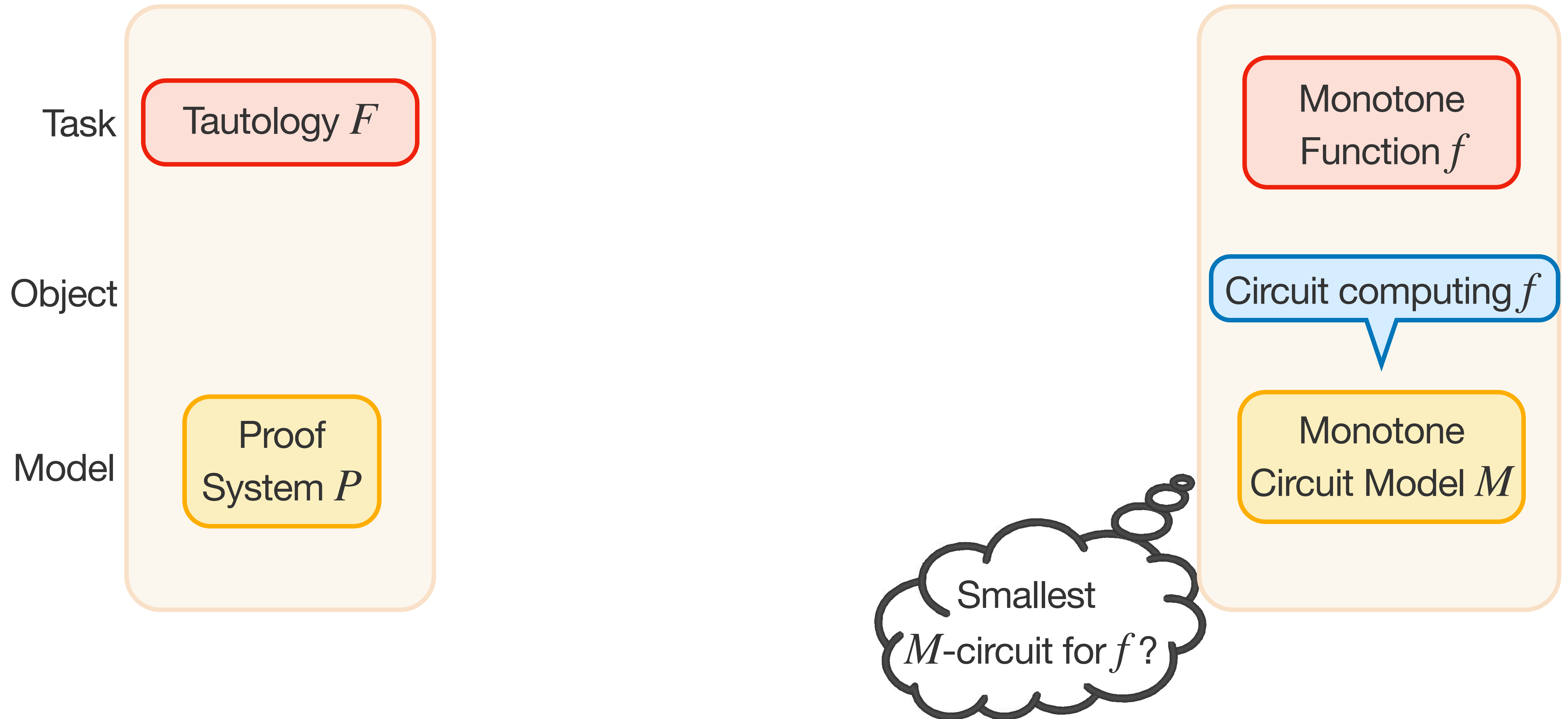
Monotone
Circuit Model M

Smallest
 M -circuit for f ?

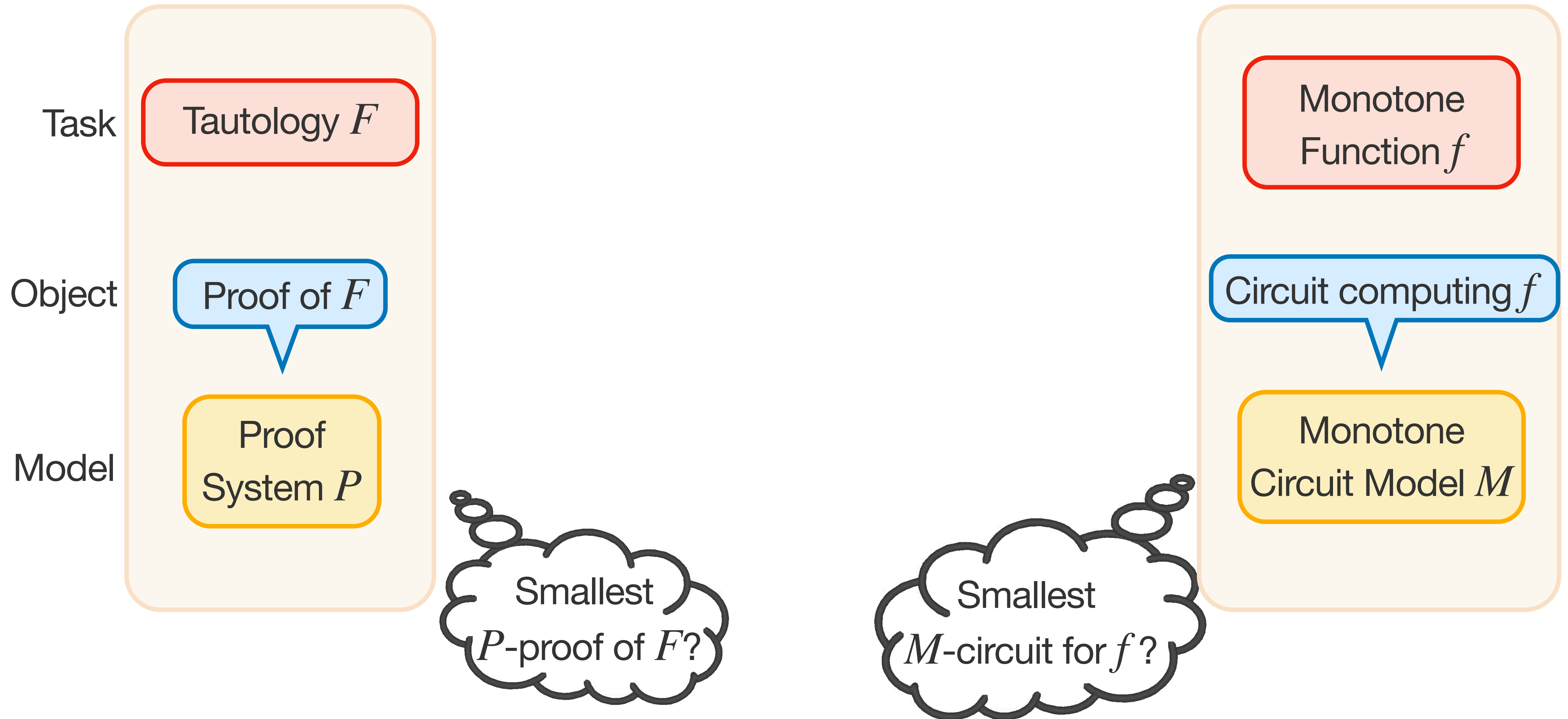
Proof Complexity



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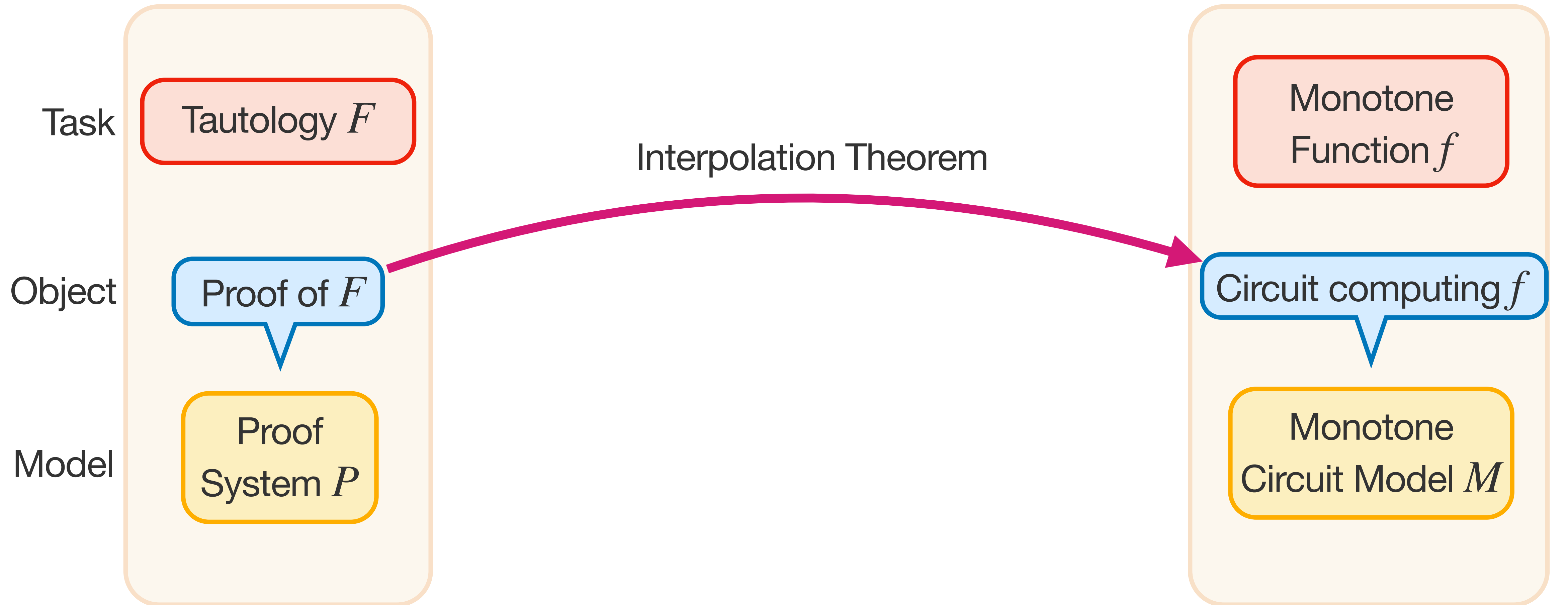


Interplay



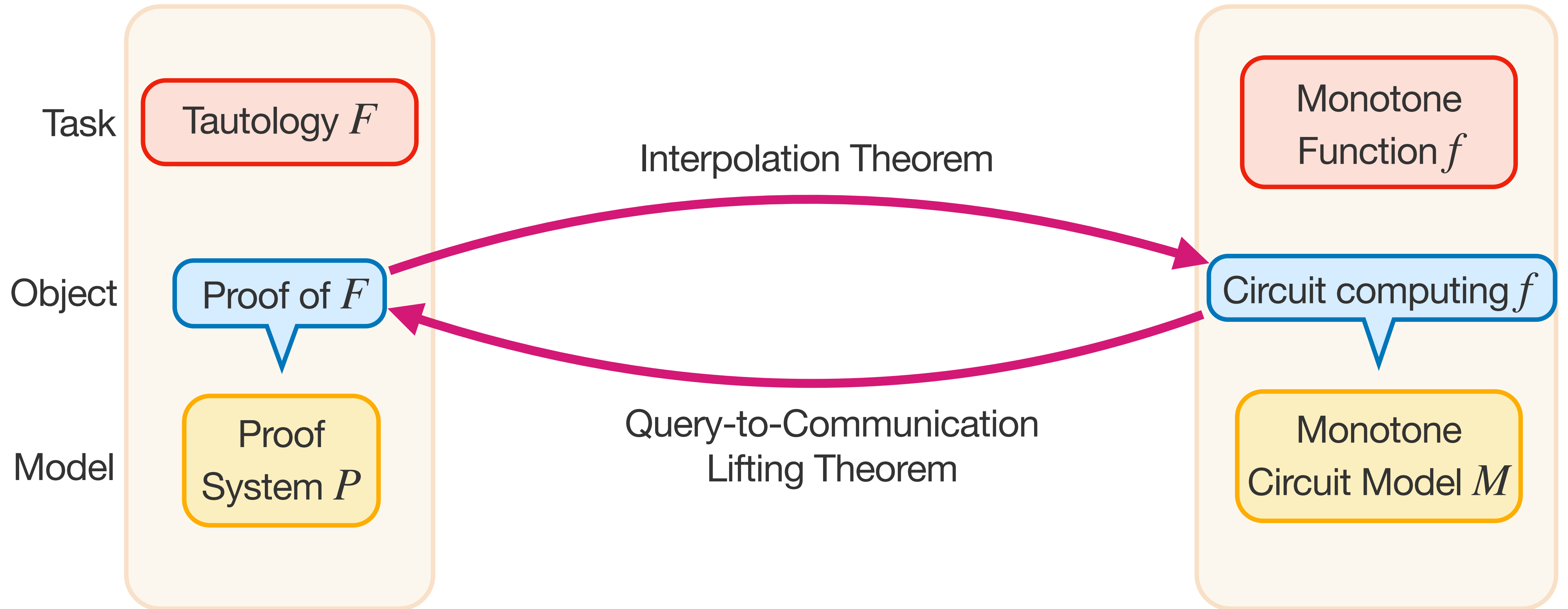
Major breakthroughs resulted from uncovering deep connections between these areas!

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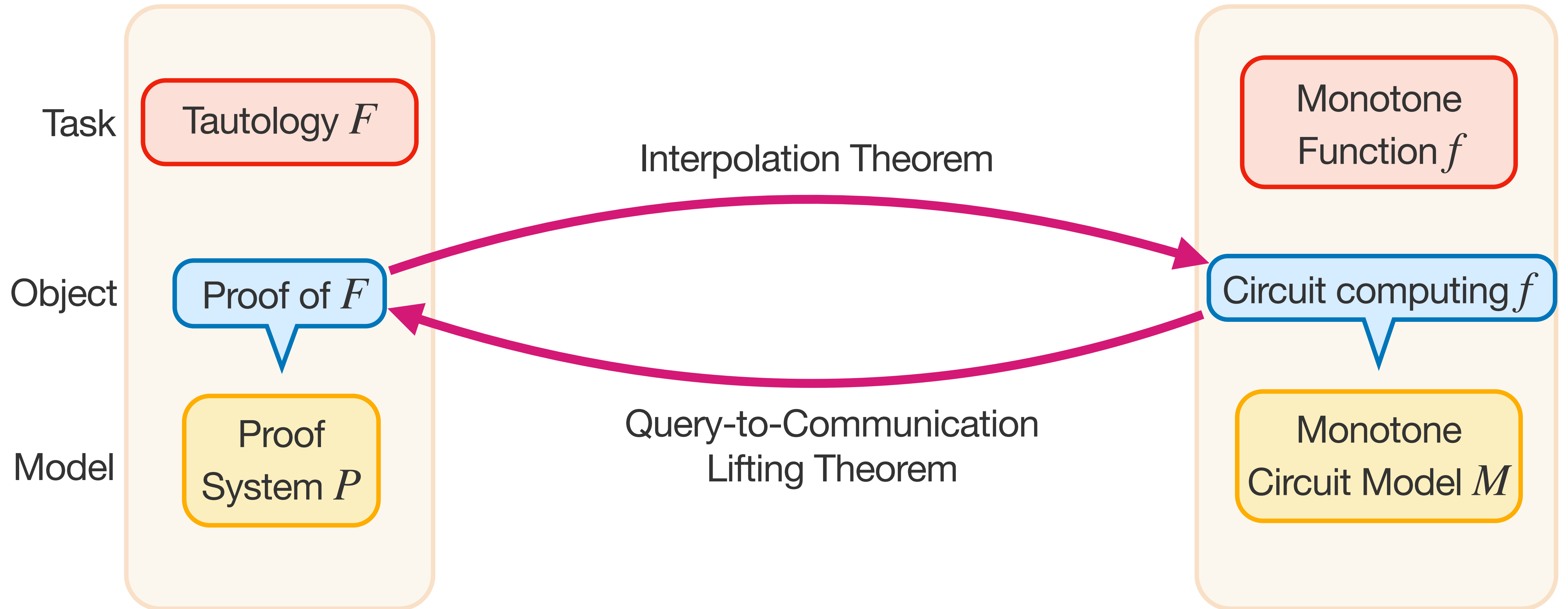
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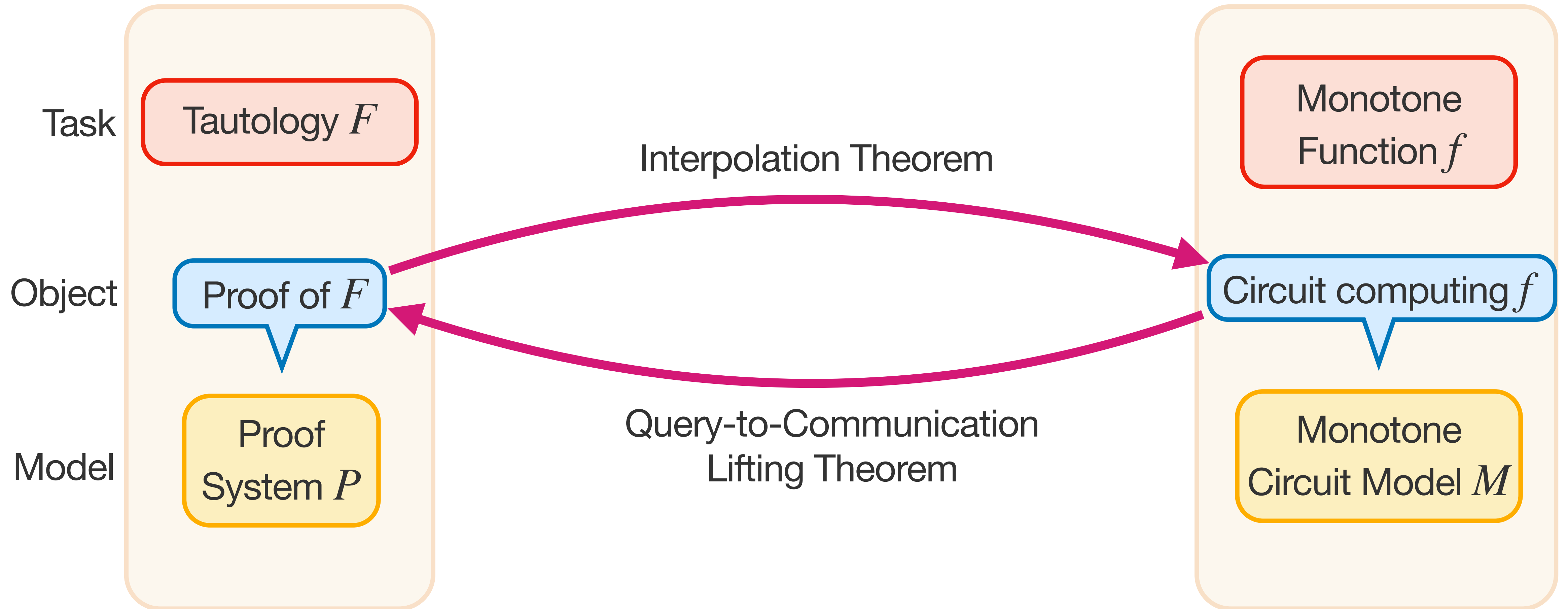
Upshot: Tools from one area can be applied to the other!

Interplay

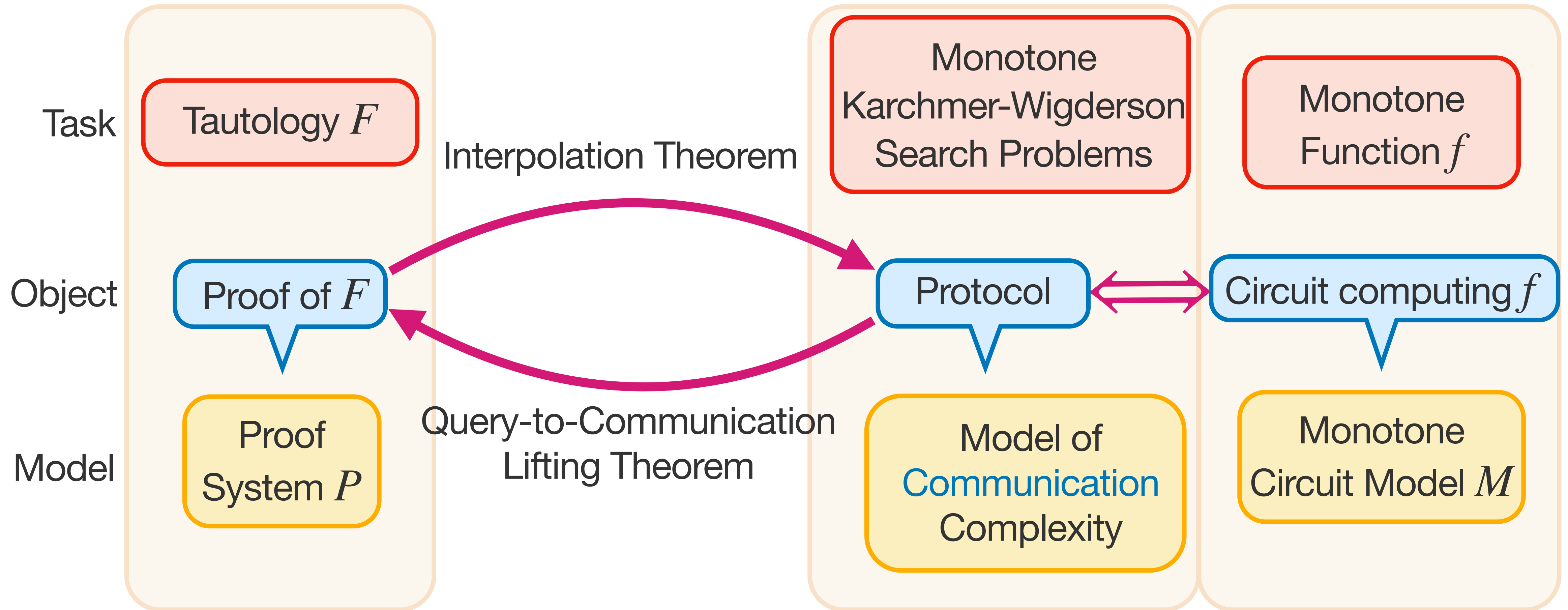
Q. When and why do these connections occur?

TFNP has emerged as a **roadmap** for interpolation and lifting theorems

Characterizations by Total Search Problems

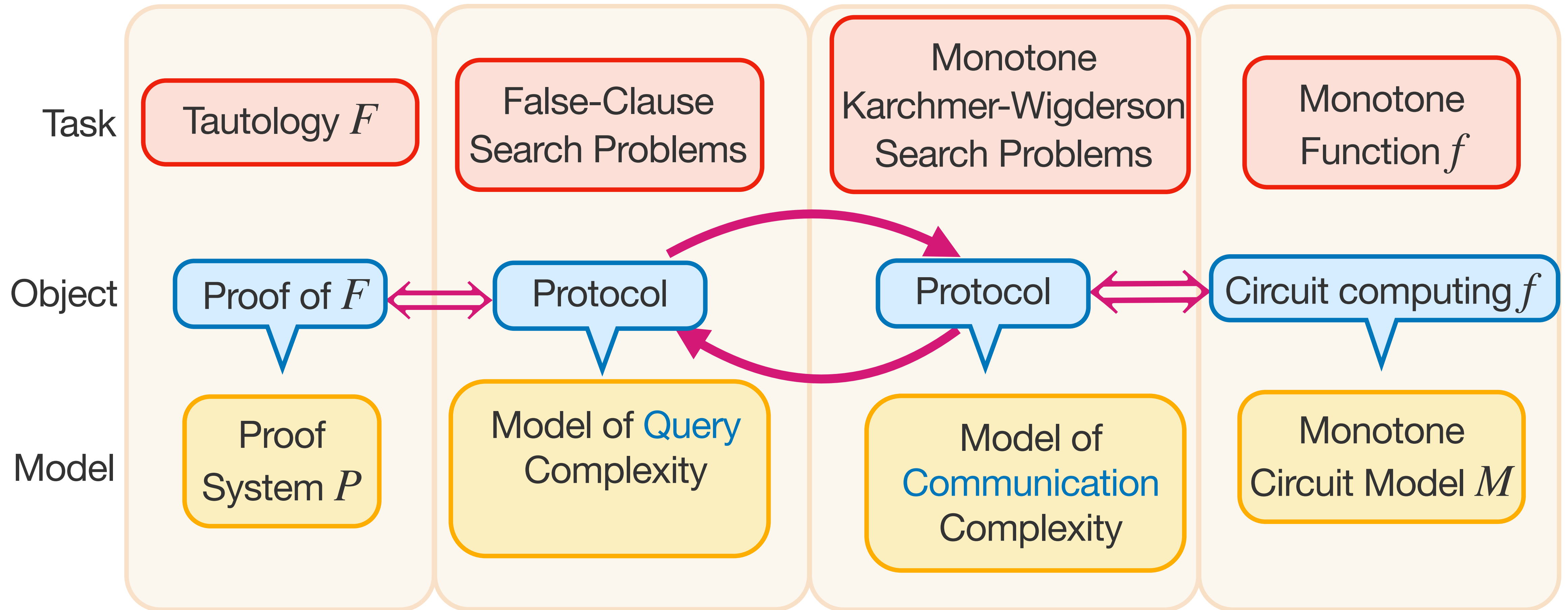


Characterizations by Total Search Problems



mKW_f : Given $(x, y) \in f^{-1}(1) \times f^{-1}(0)$ output $i \in [n]$ such that $x_i \neq y_i$

Characterizations by Total Search Problems



$Search_F$: Given $x \in \{0,1\}^n$ output the index of a clause of F falsified by x

TFNP

Studies the complexity of computing **total search problems**

TFNP

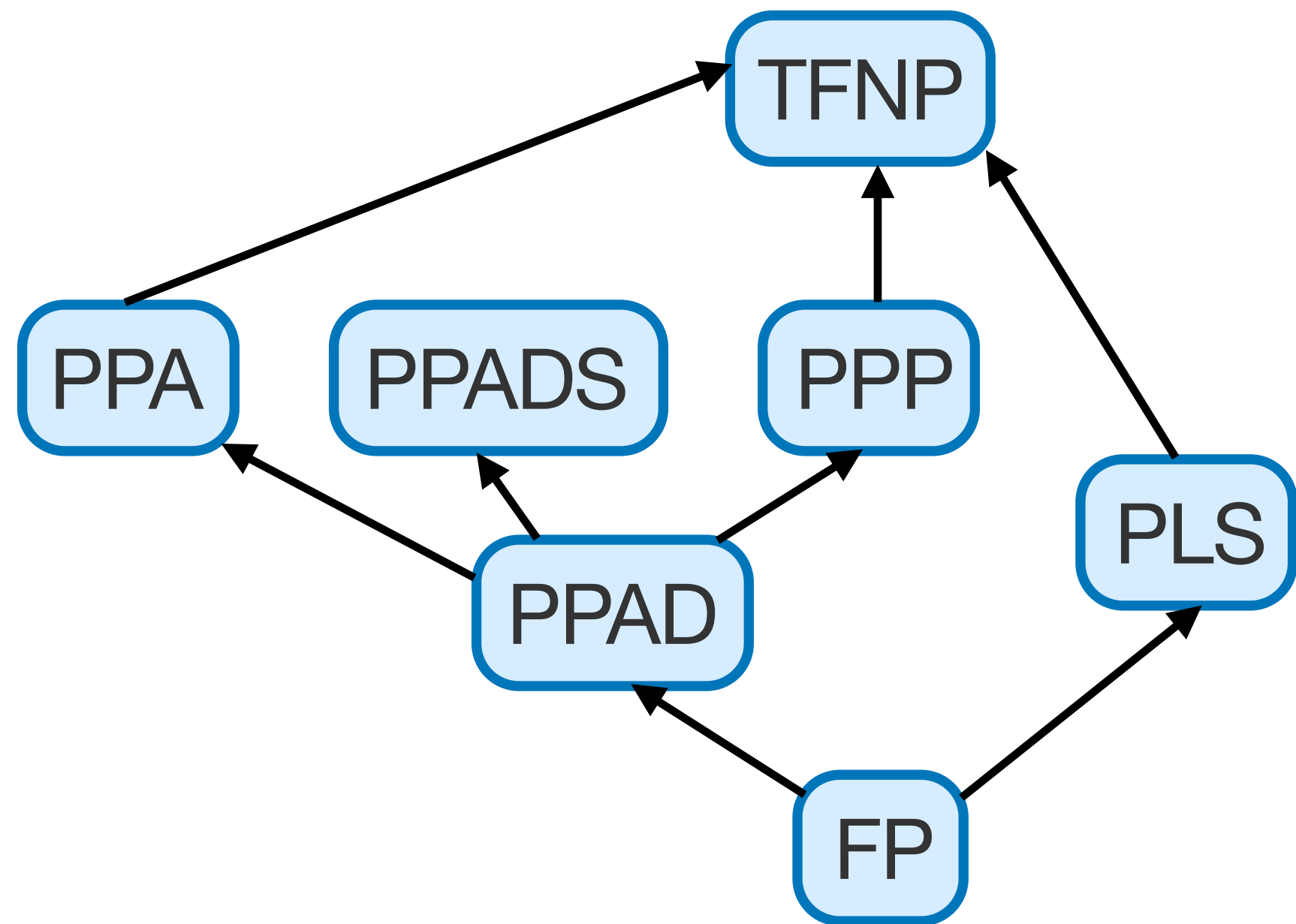
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→ Organizes them into a variety of classes with complete problems

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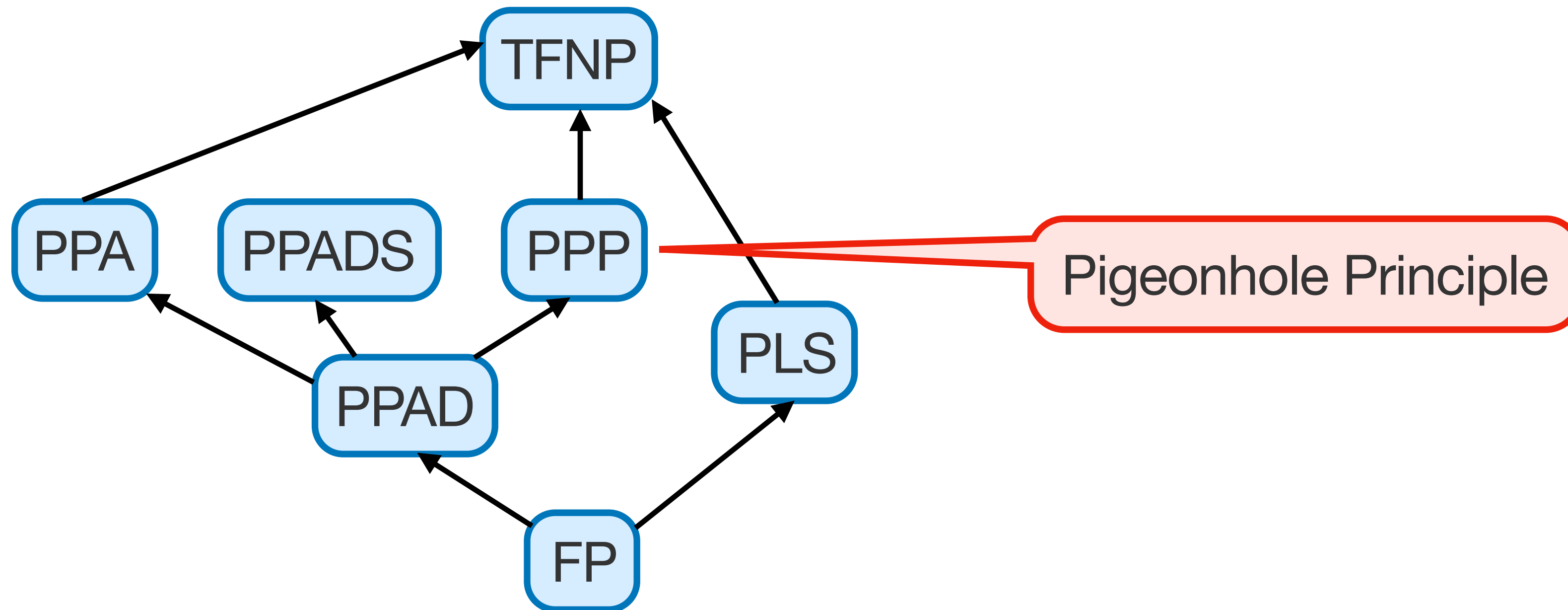
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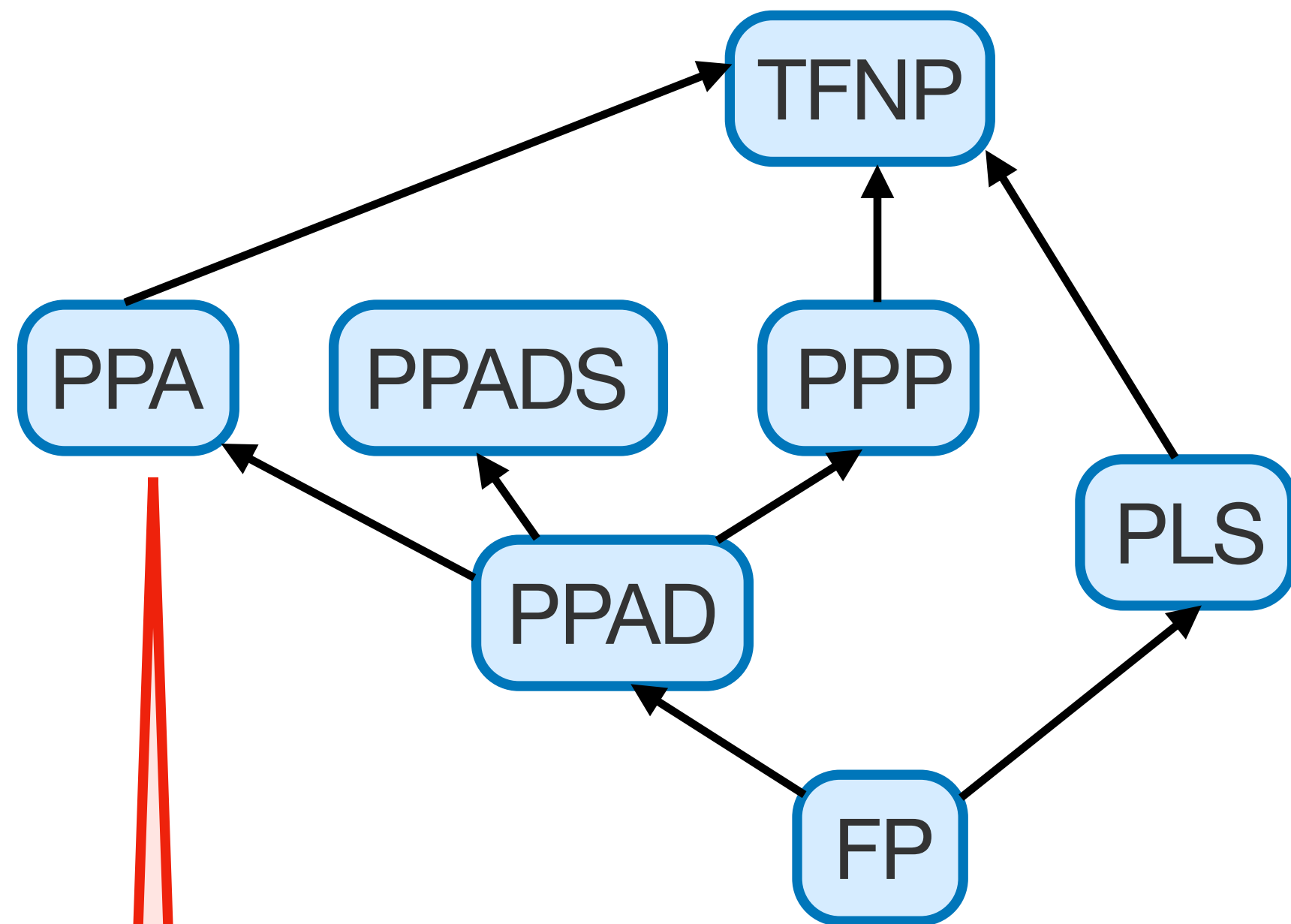
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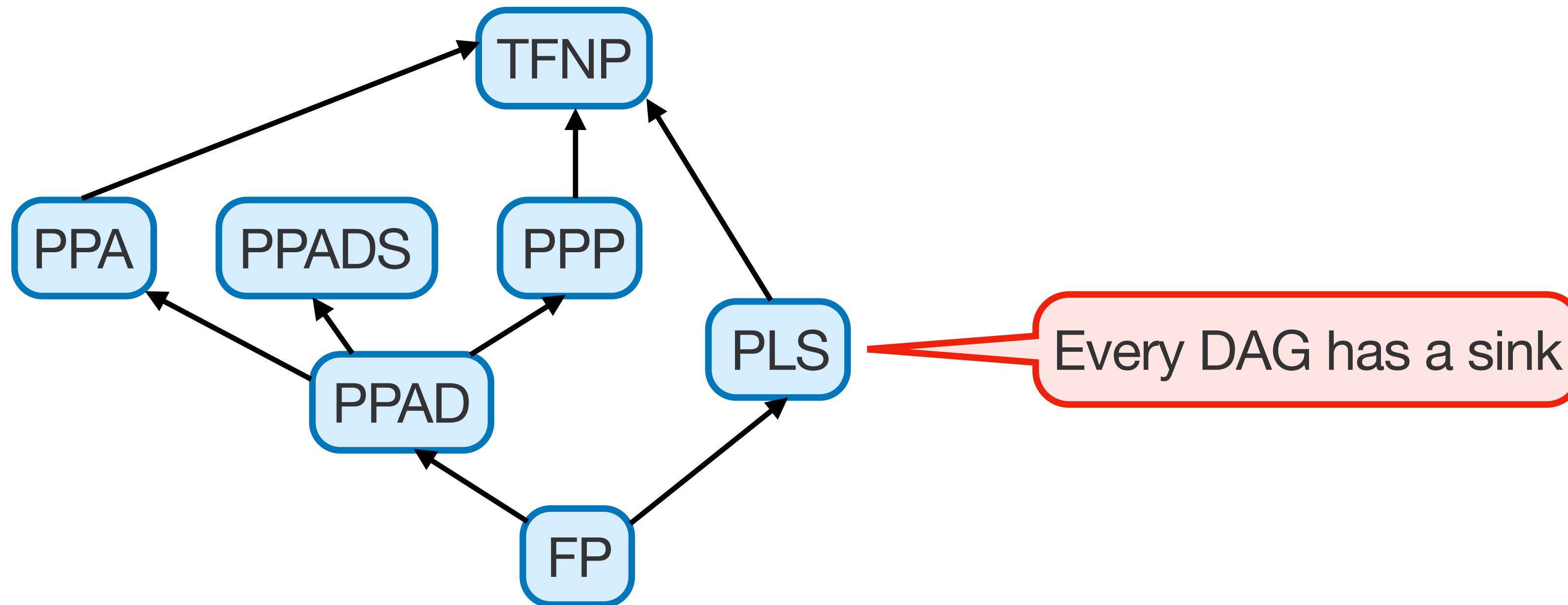


Every odd degree vertex has another

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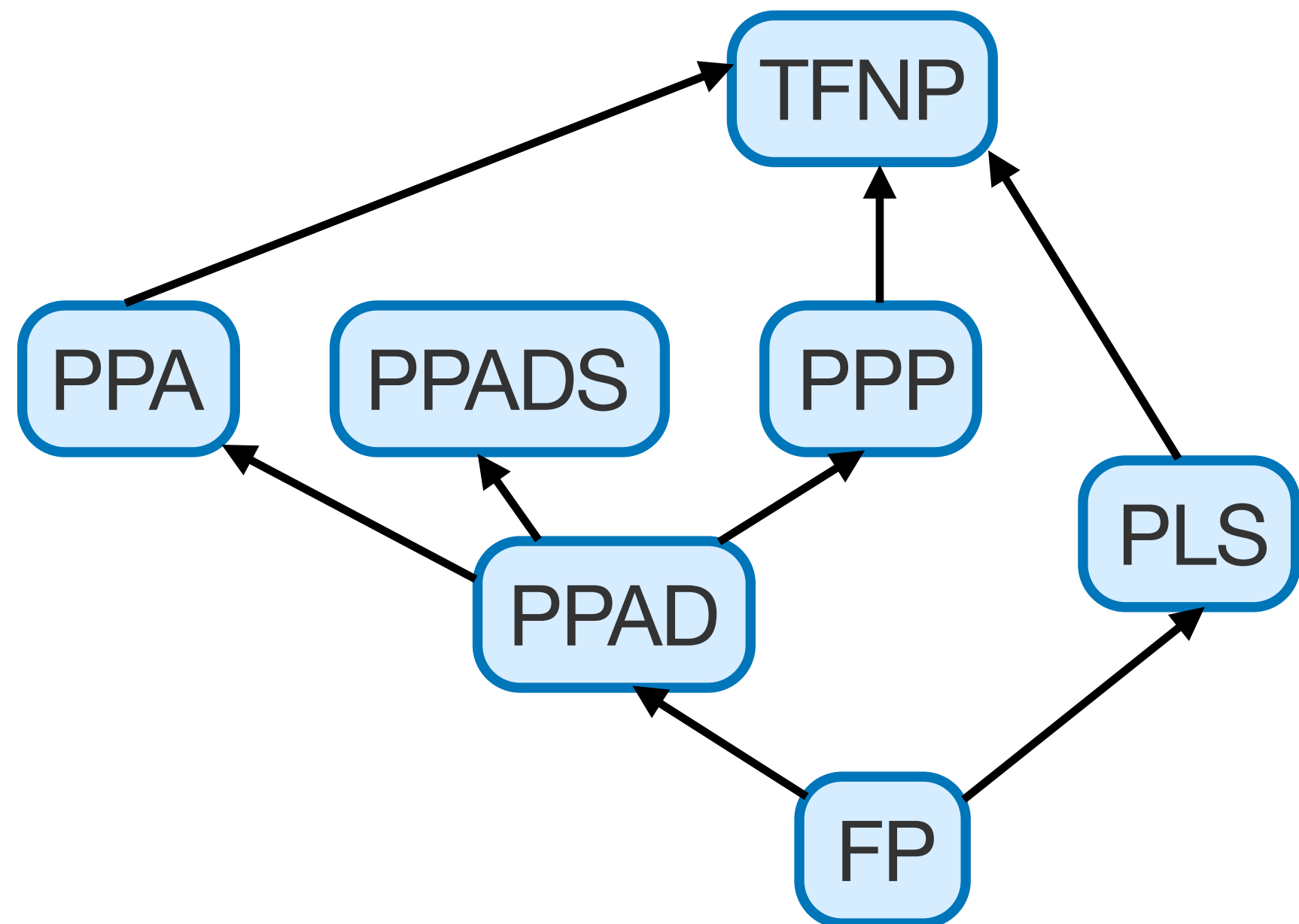
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Every DAG has a sink

SinkOfDag

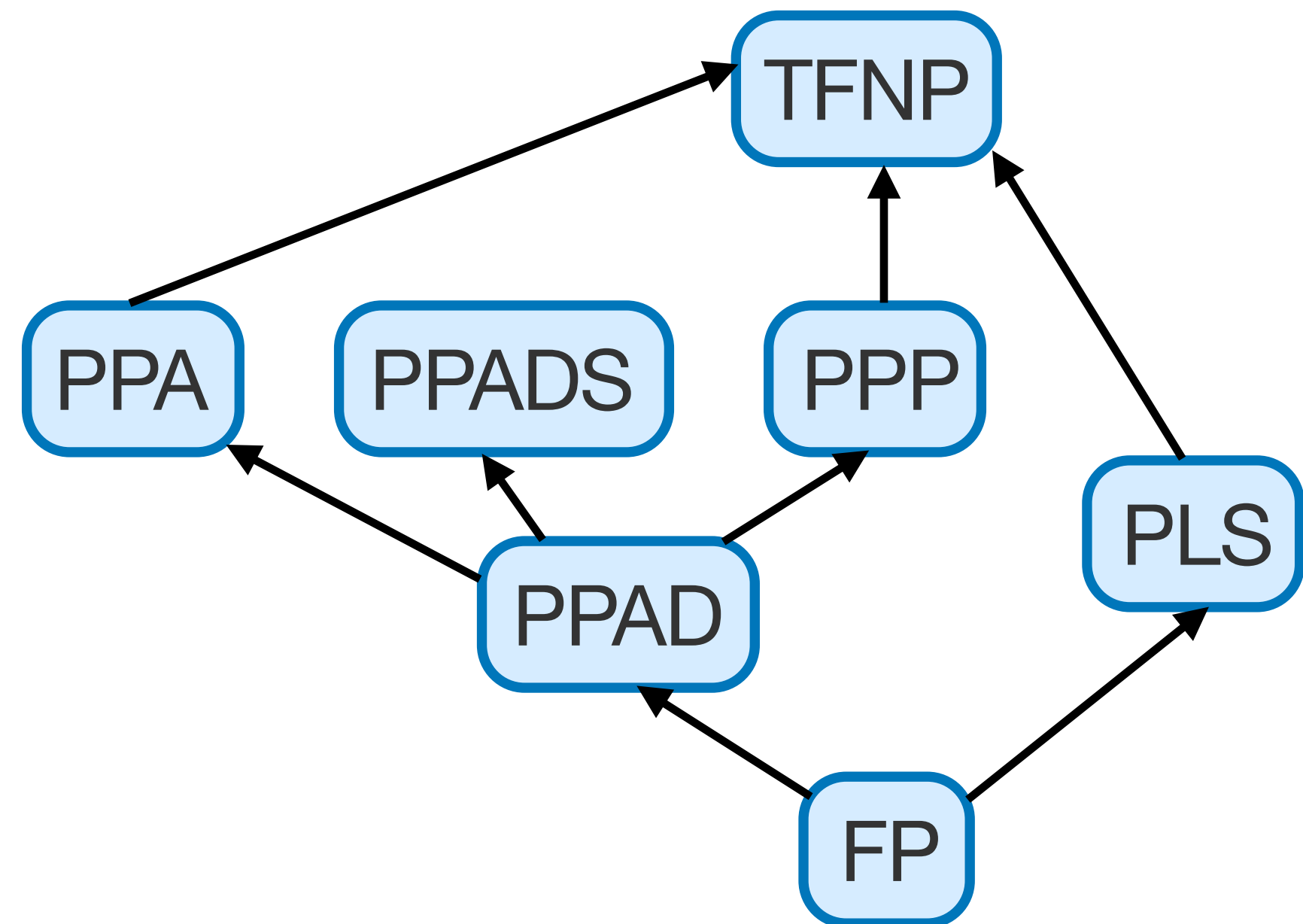
Vertices: $1, \dots, n$

- 1
- 2
- 3
- 4
- 5
- 6

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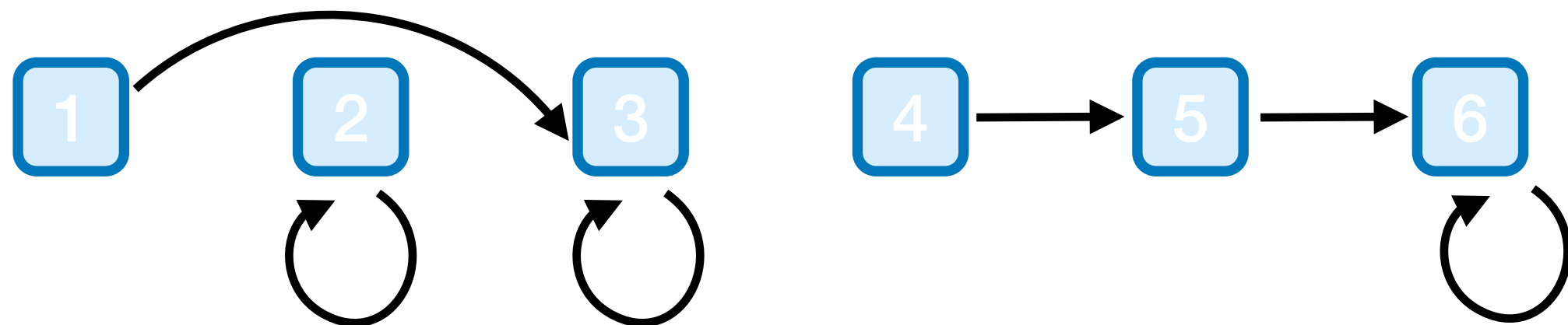


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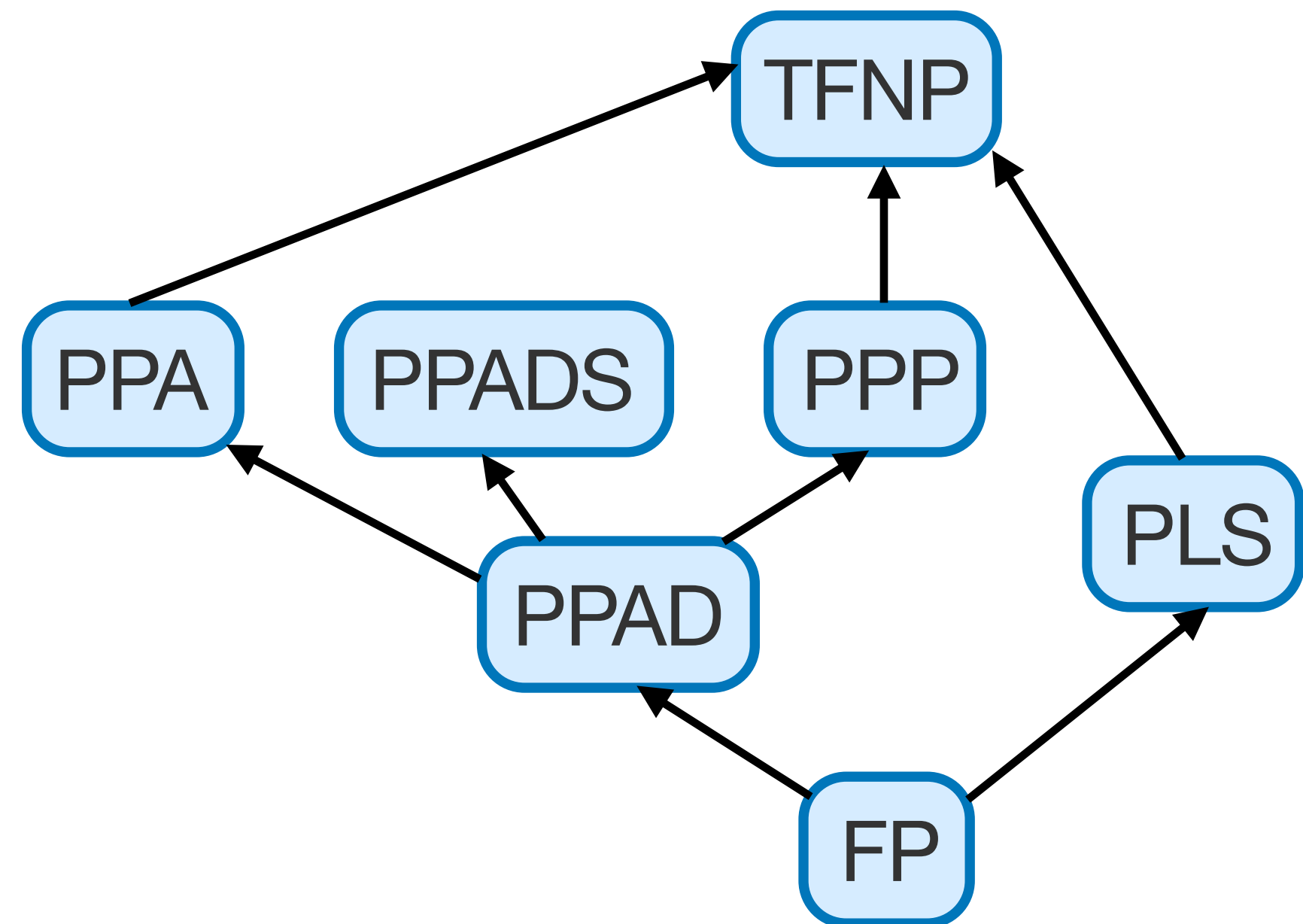
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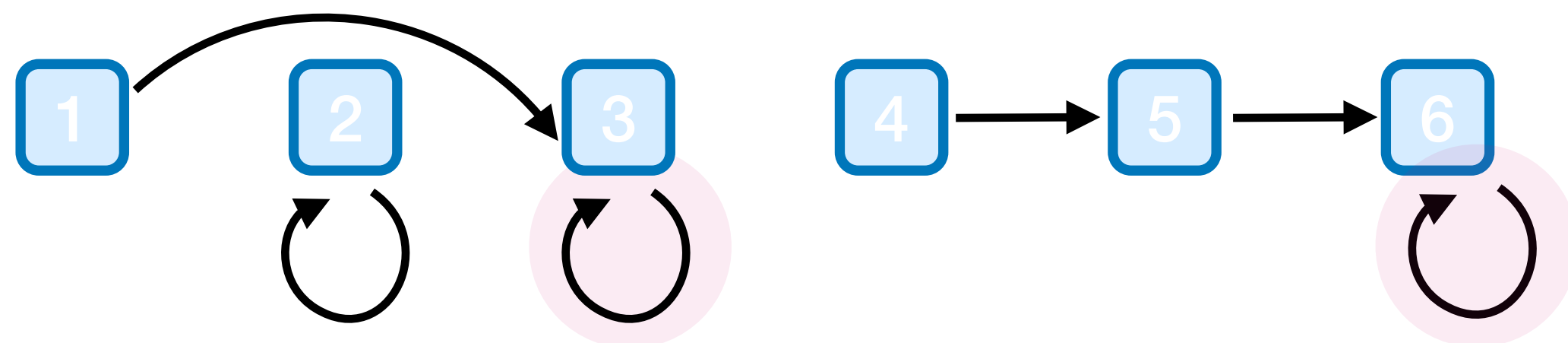
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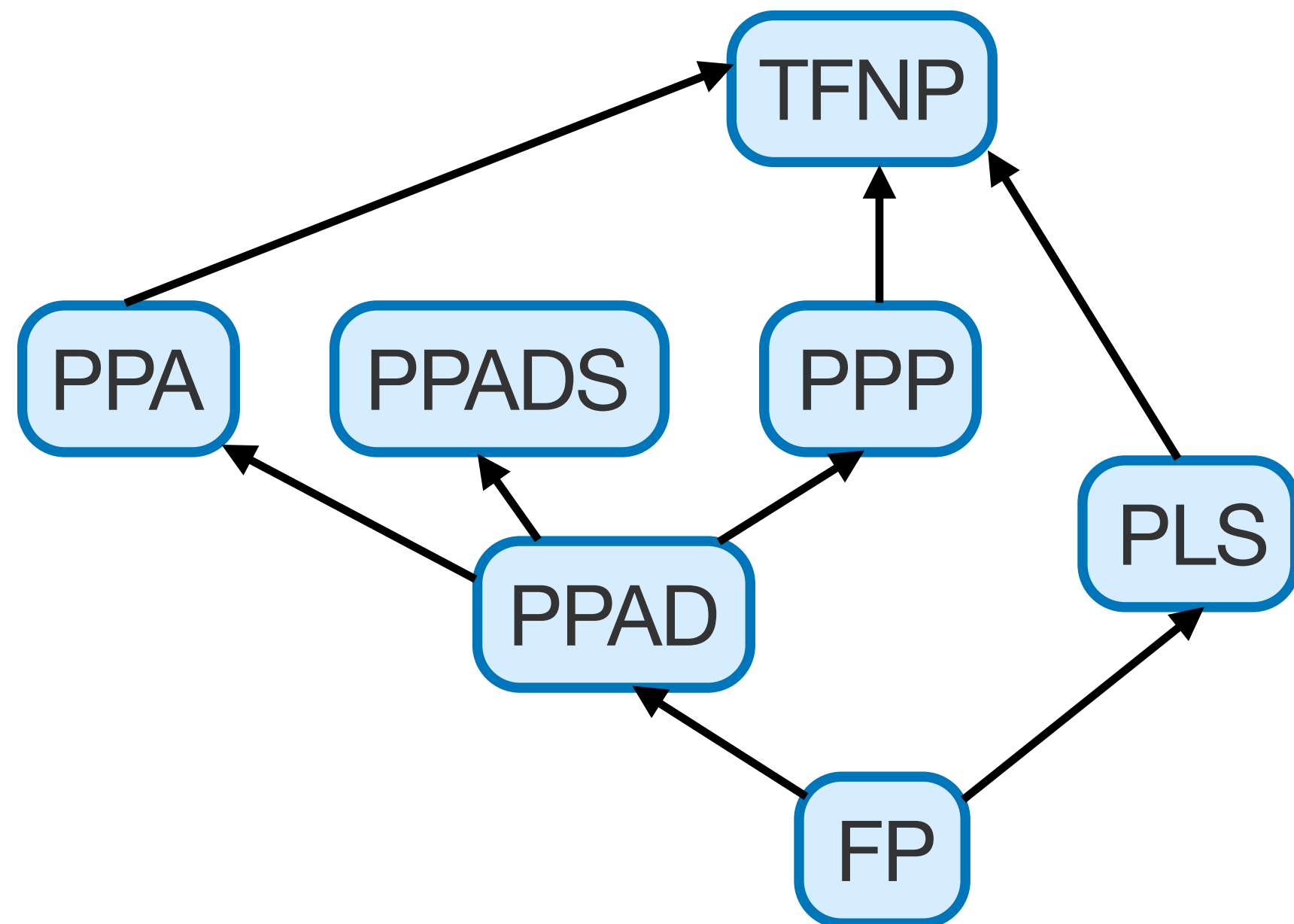
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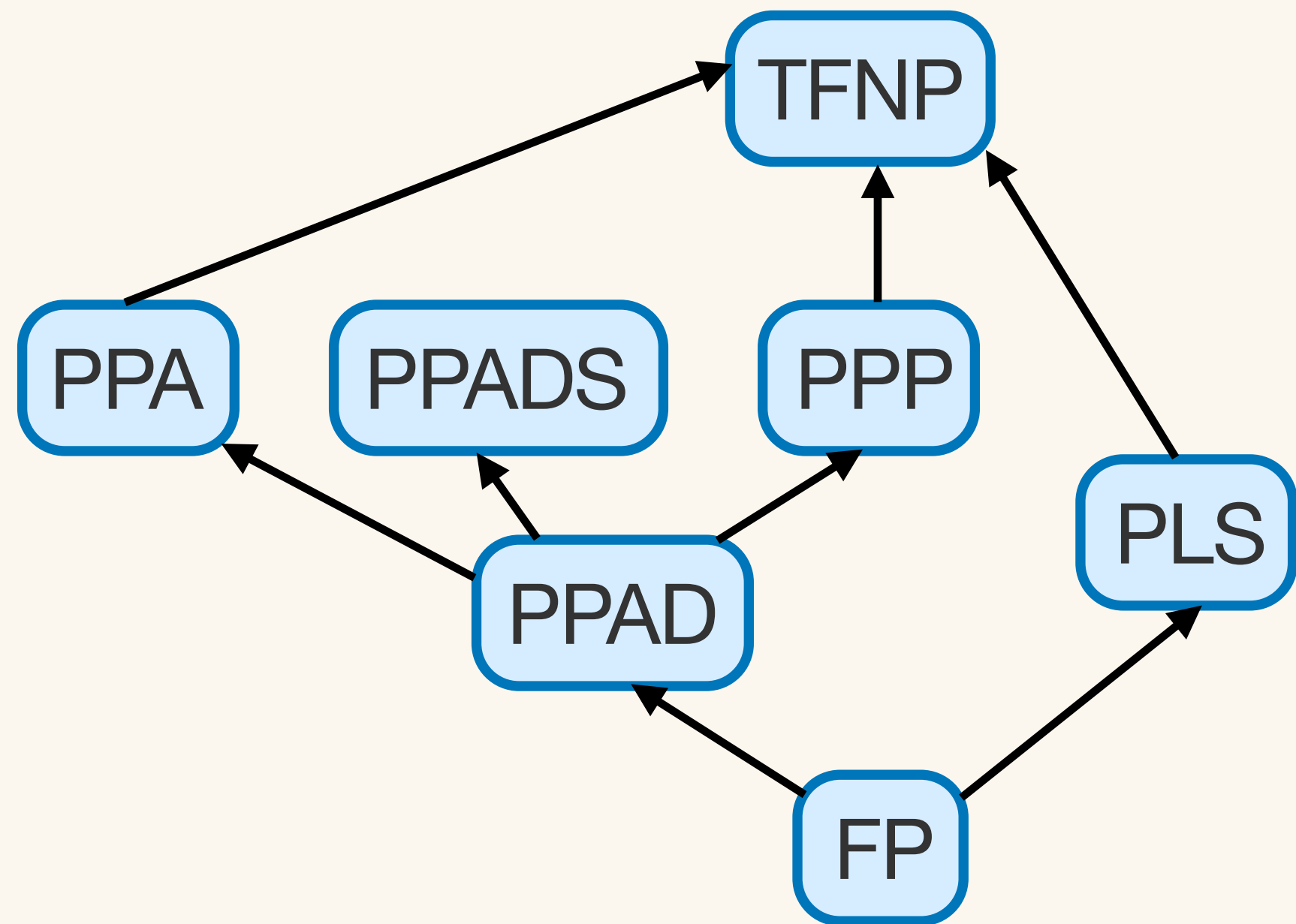
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Typically study the **Turing Machine** complexity of total search problems

However, useful to consider **other models of computation**

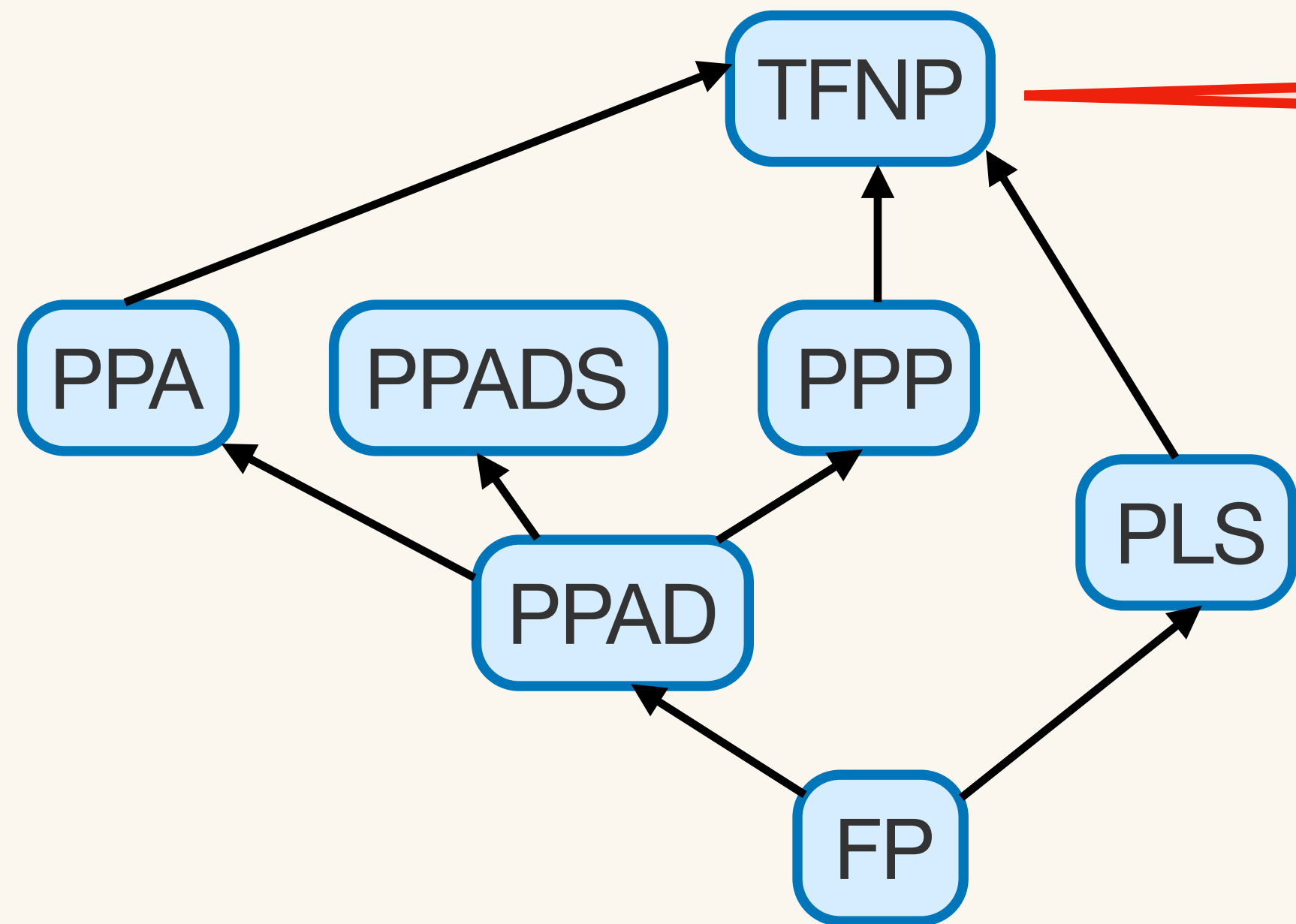
TFNP



Model of Computation:

Decision Trees

TFNP

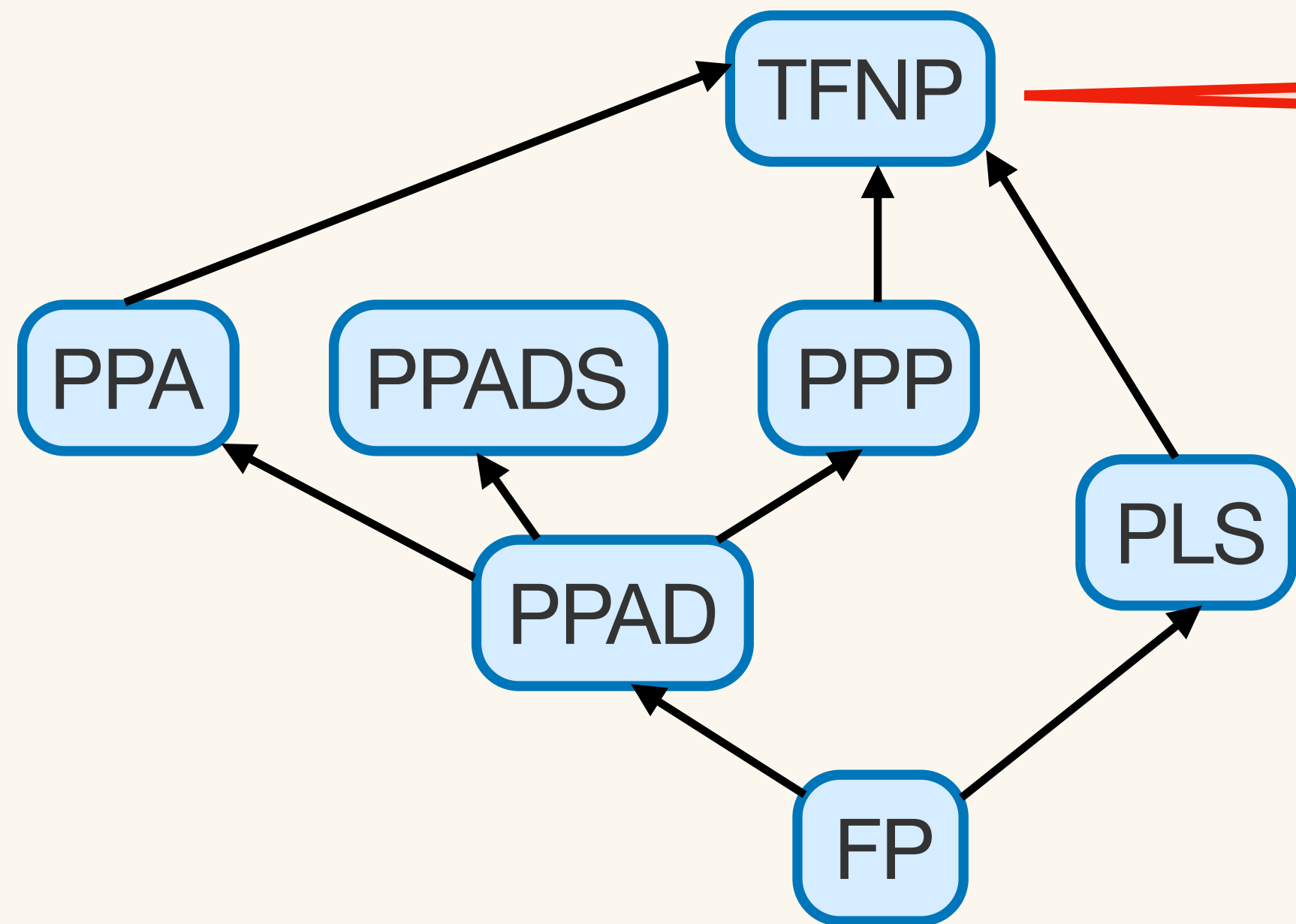


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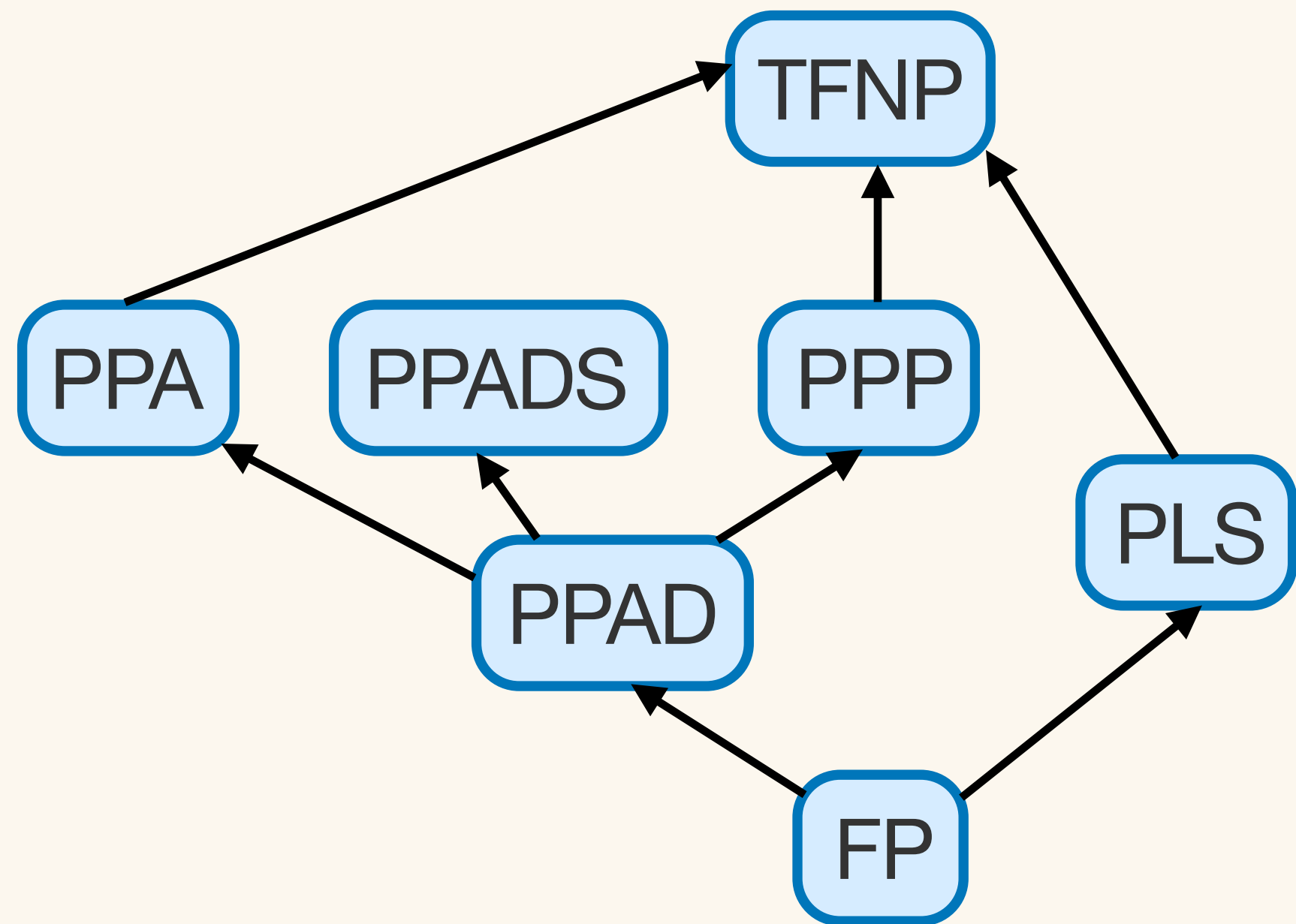
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$\forall \ell \in \mathcal{O}$ there is $polylog(n)$ -depth T_ℓ such that $(x, \ell) \in S \iff T_\ell(x) = 1$

Model of Computation: Decision Trees

TFNP

[BCEIP98] Separations imply **black-box / generic oracle separations**



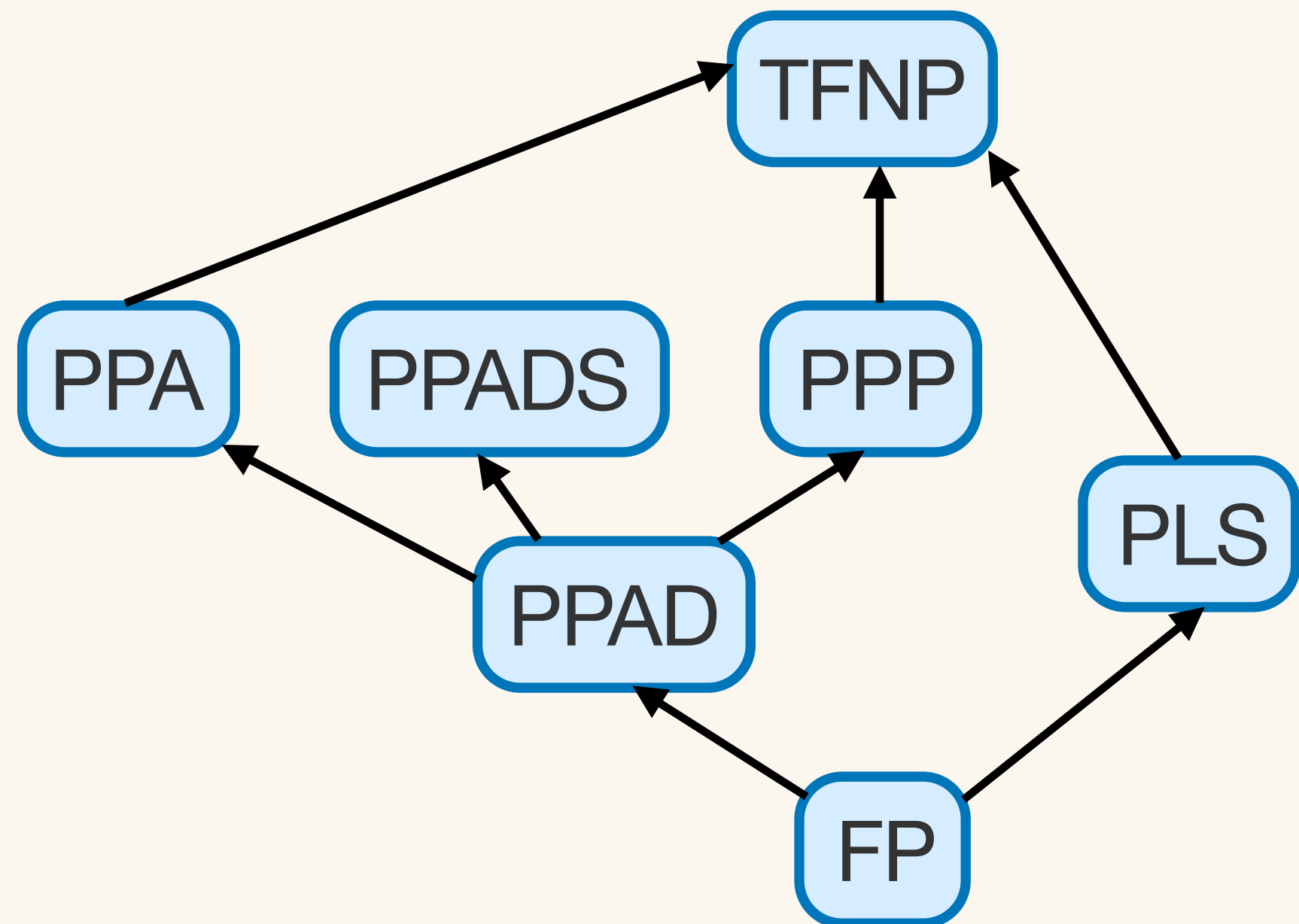
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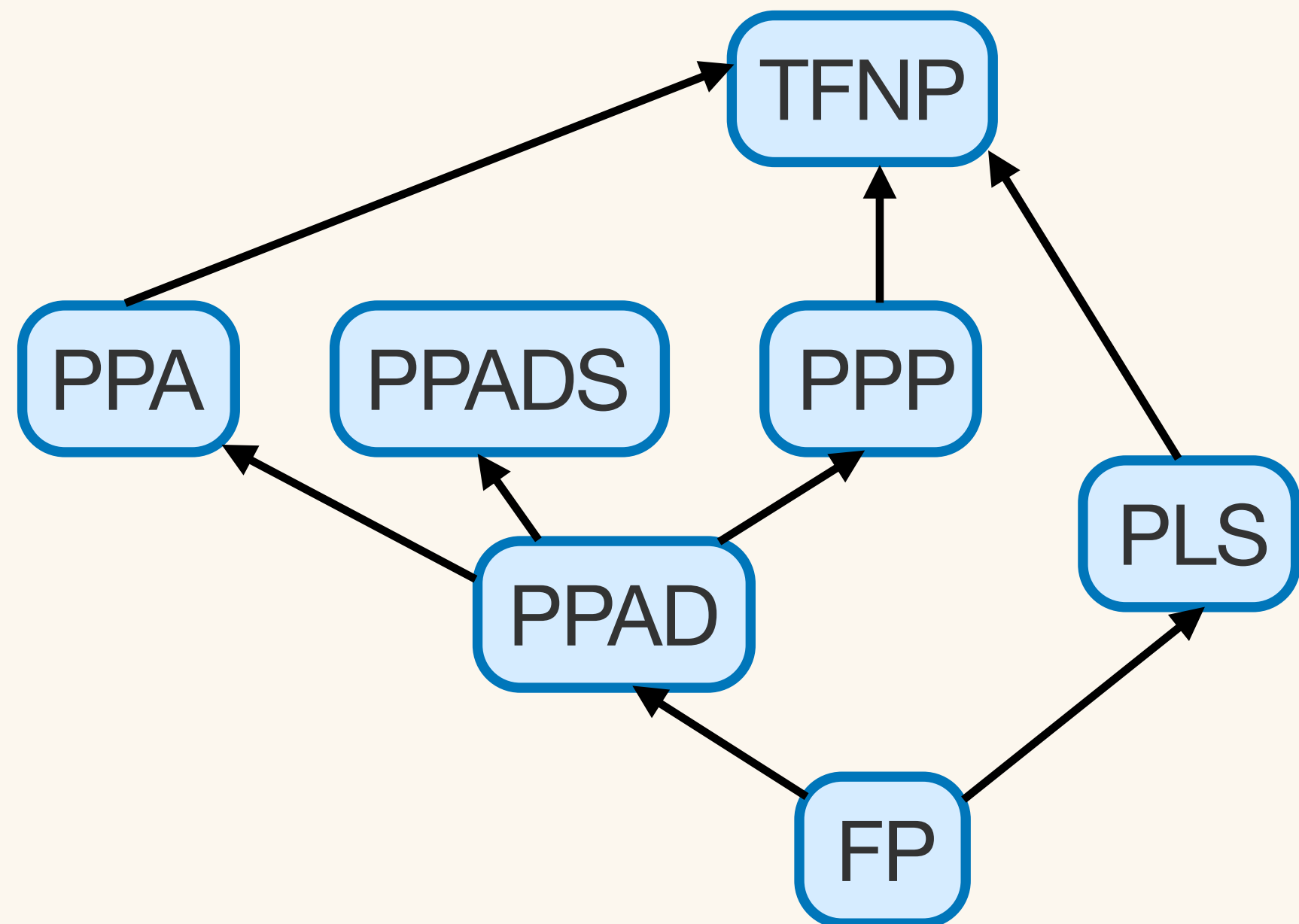
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Say that these proof systems are **characterized** by the TFNP class

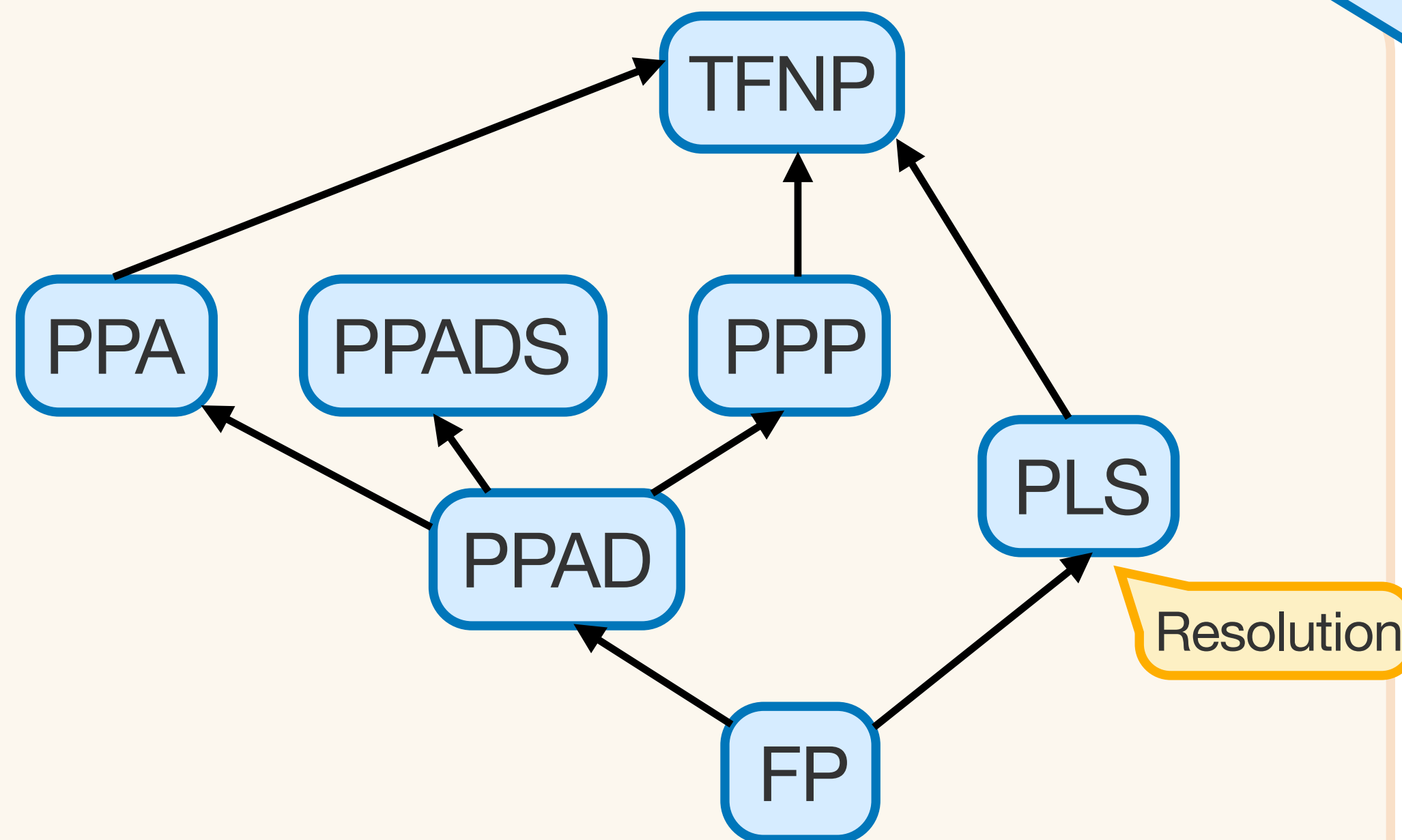
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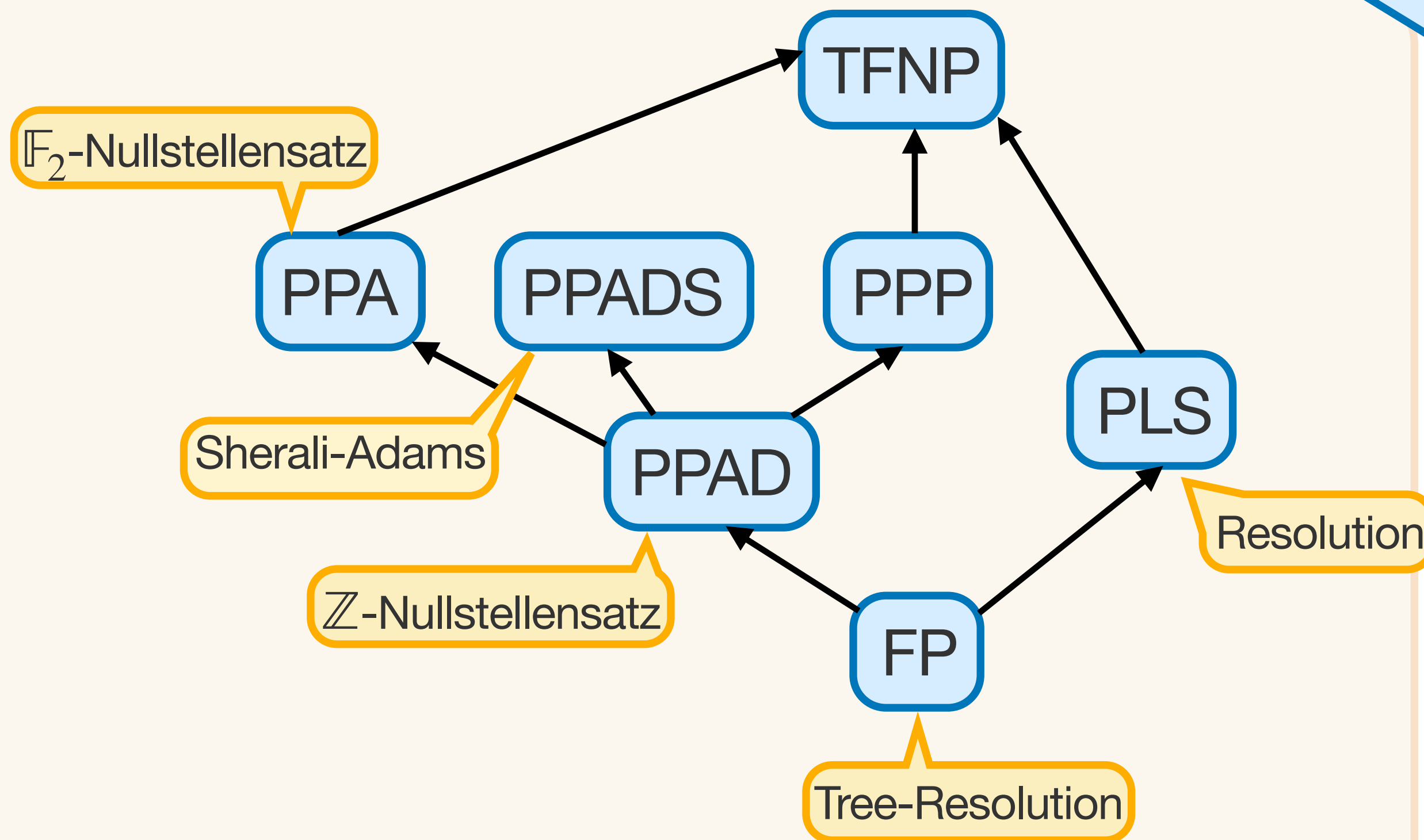
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Model of Computation: Decision Trees

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Expresses that R is **not** total:

A clause of $\neg DNF(T_\ell)$ is false under $x \iff (x, \ell) \in R$

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TFNP subclasses defined as everything $\text{polylog}(n)$ -reducible to a particular search problem

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S

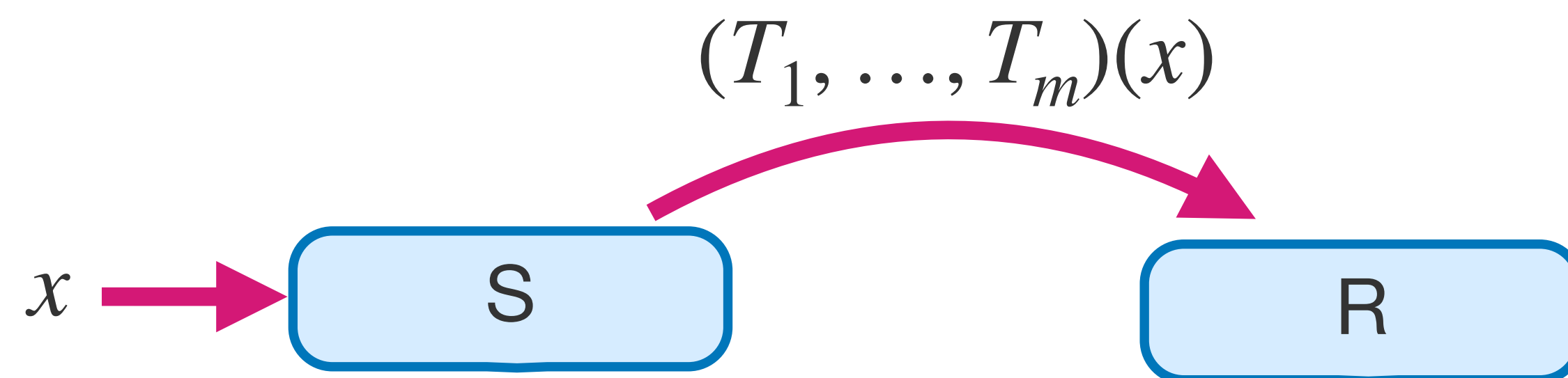
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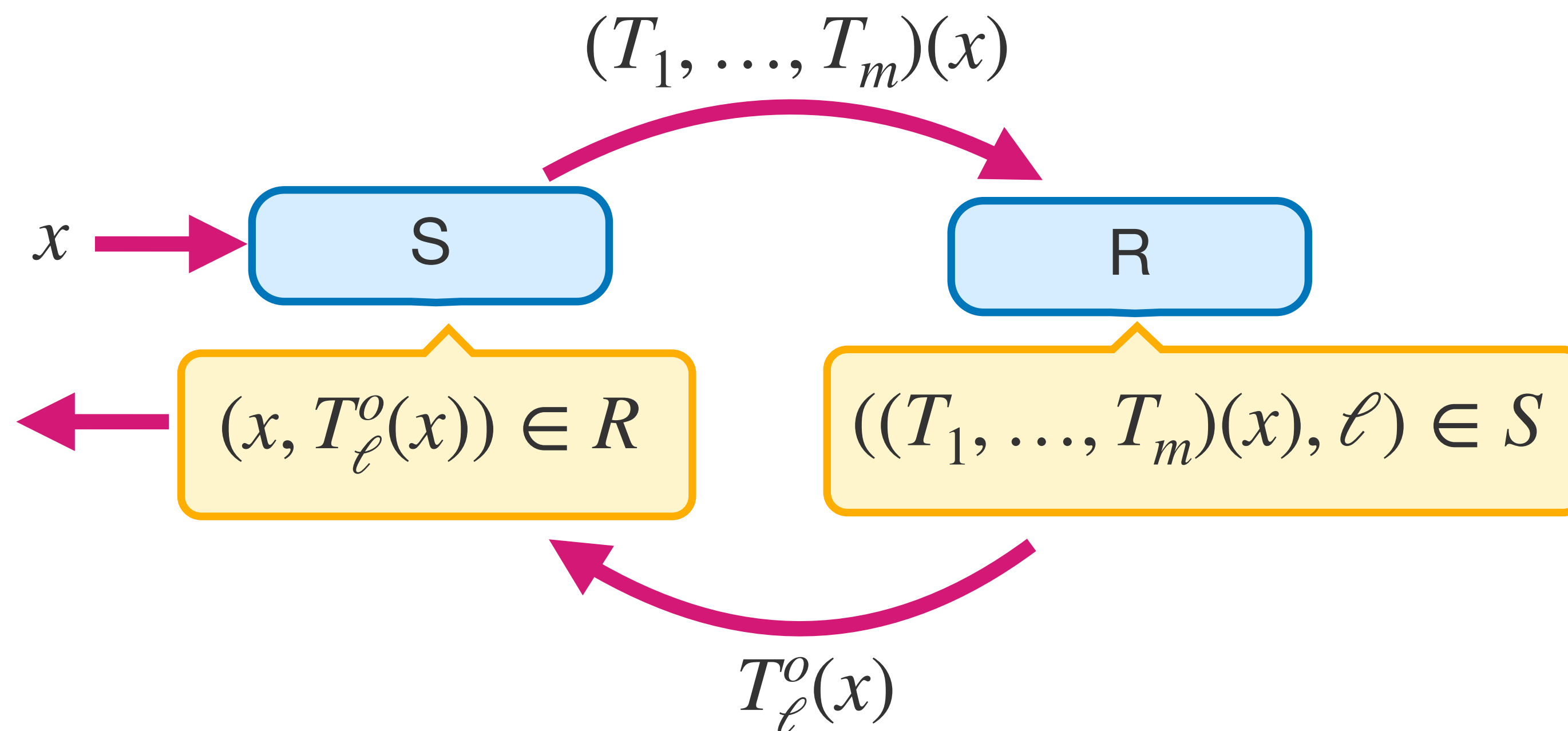


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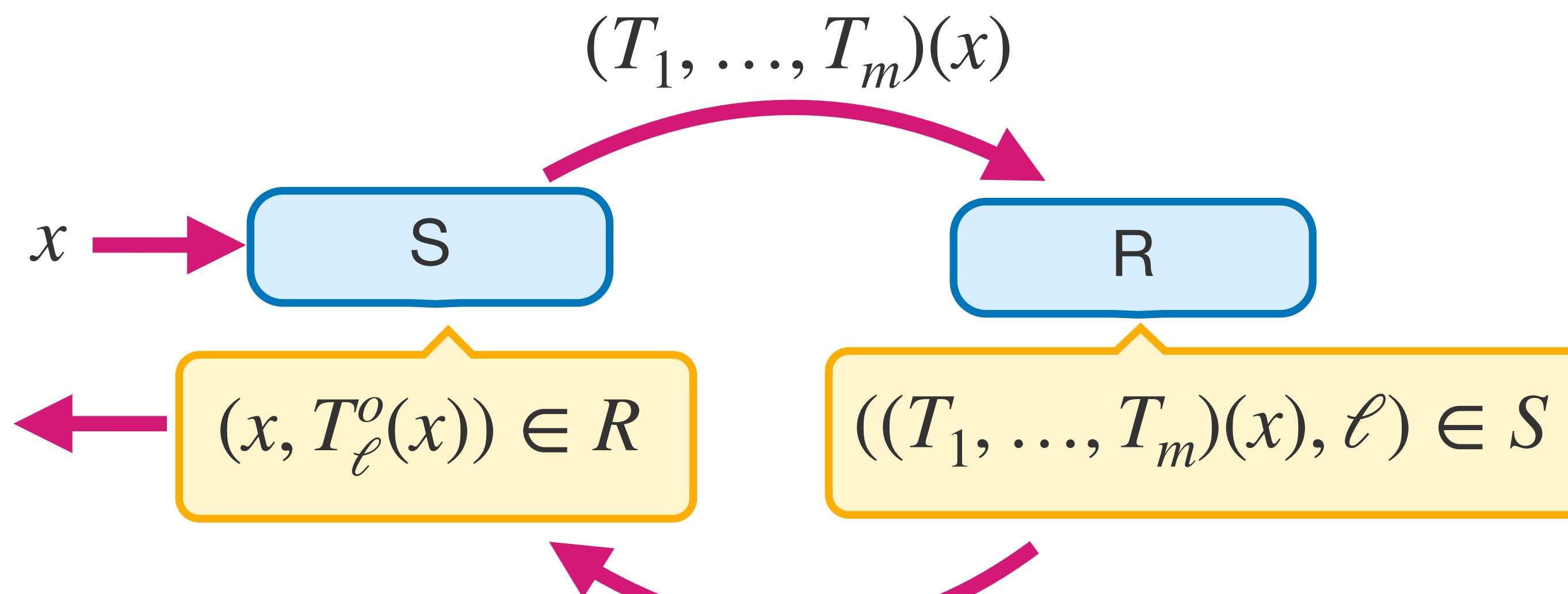


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Complexity: $\log m + \max(\text{depth}(T_i, T_i^o))$ $T_\ell^o(x)$

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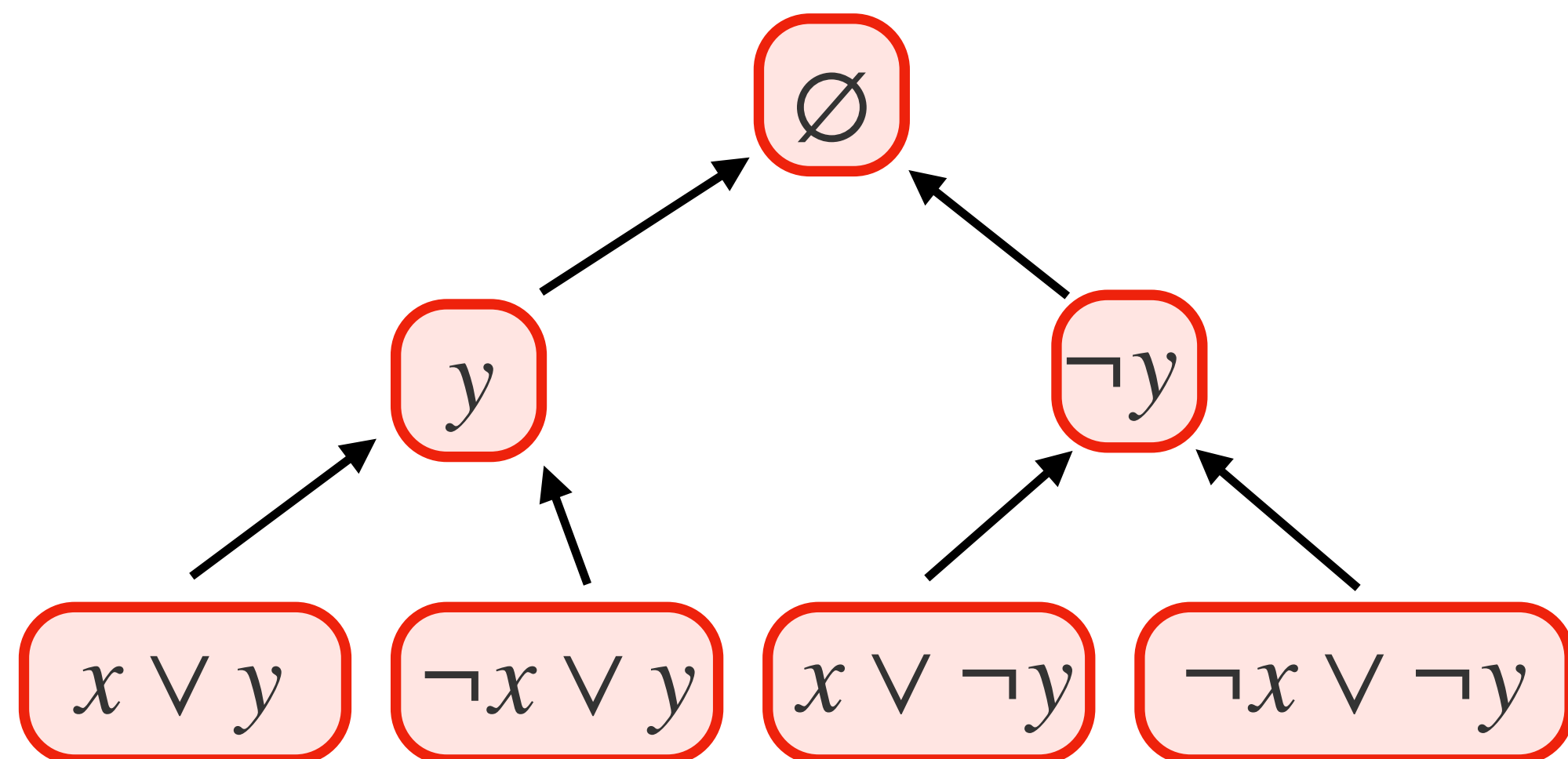
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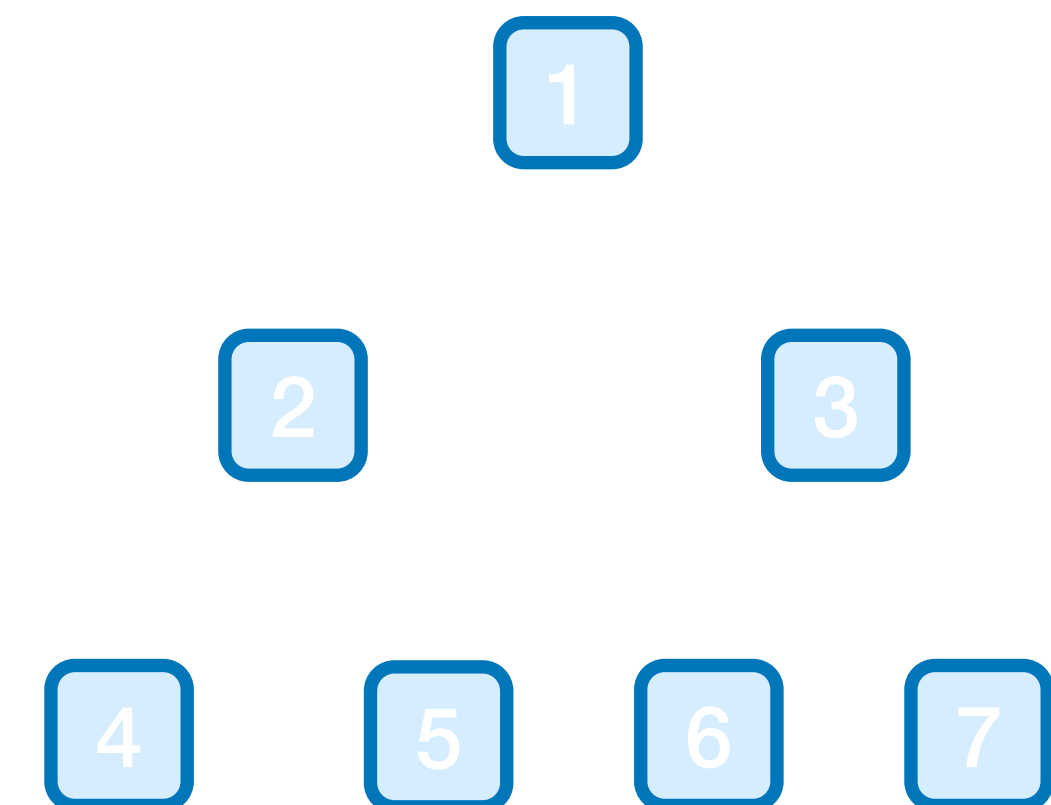
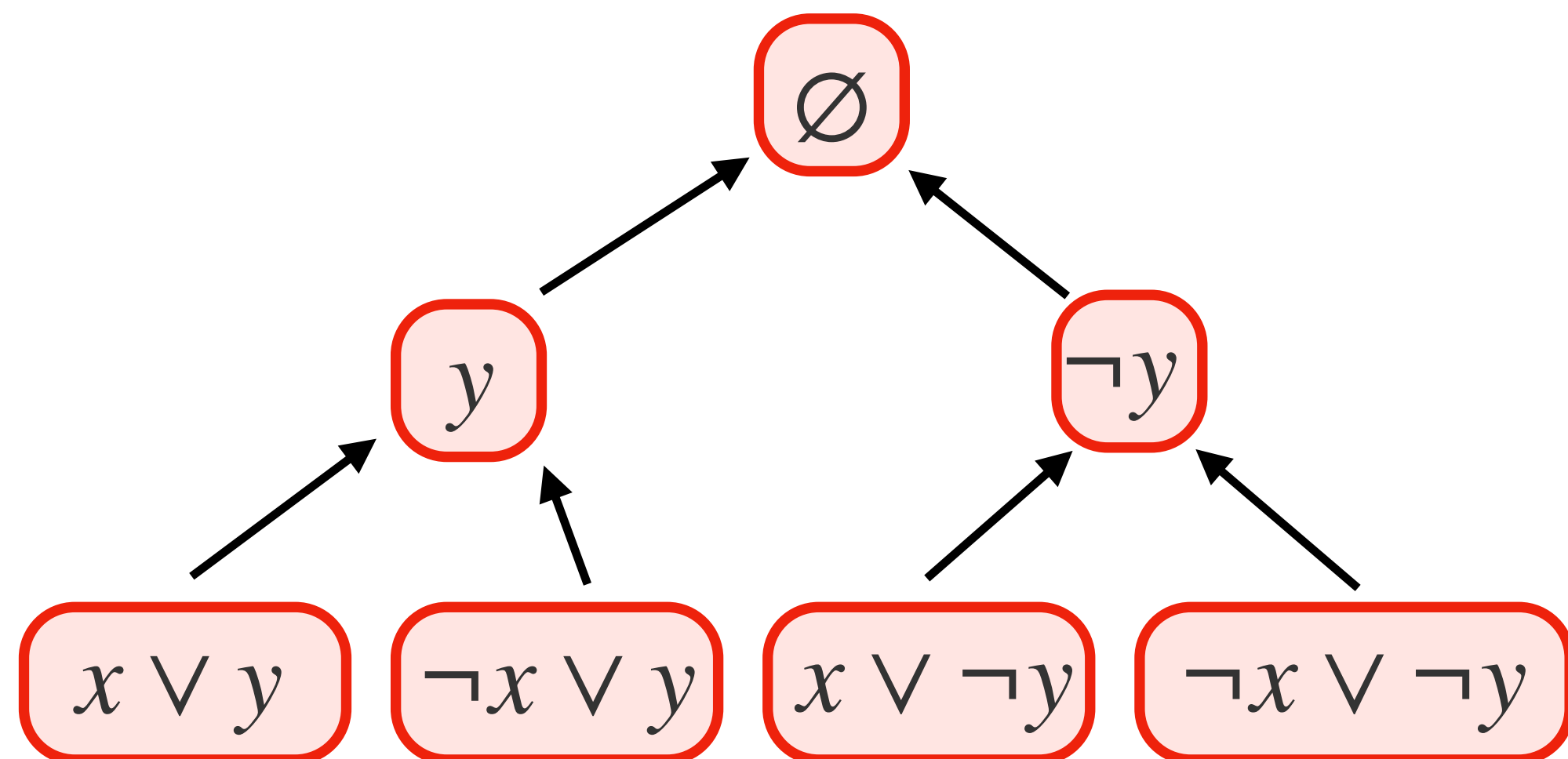
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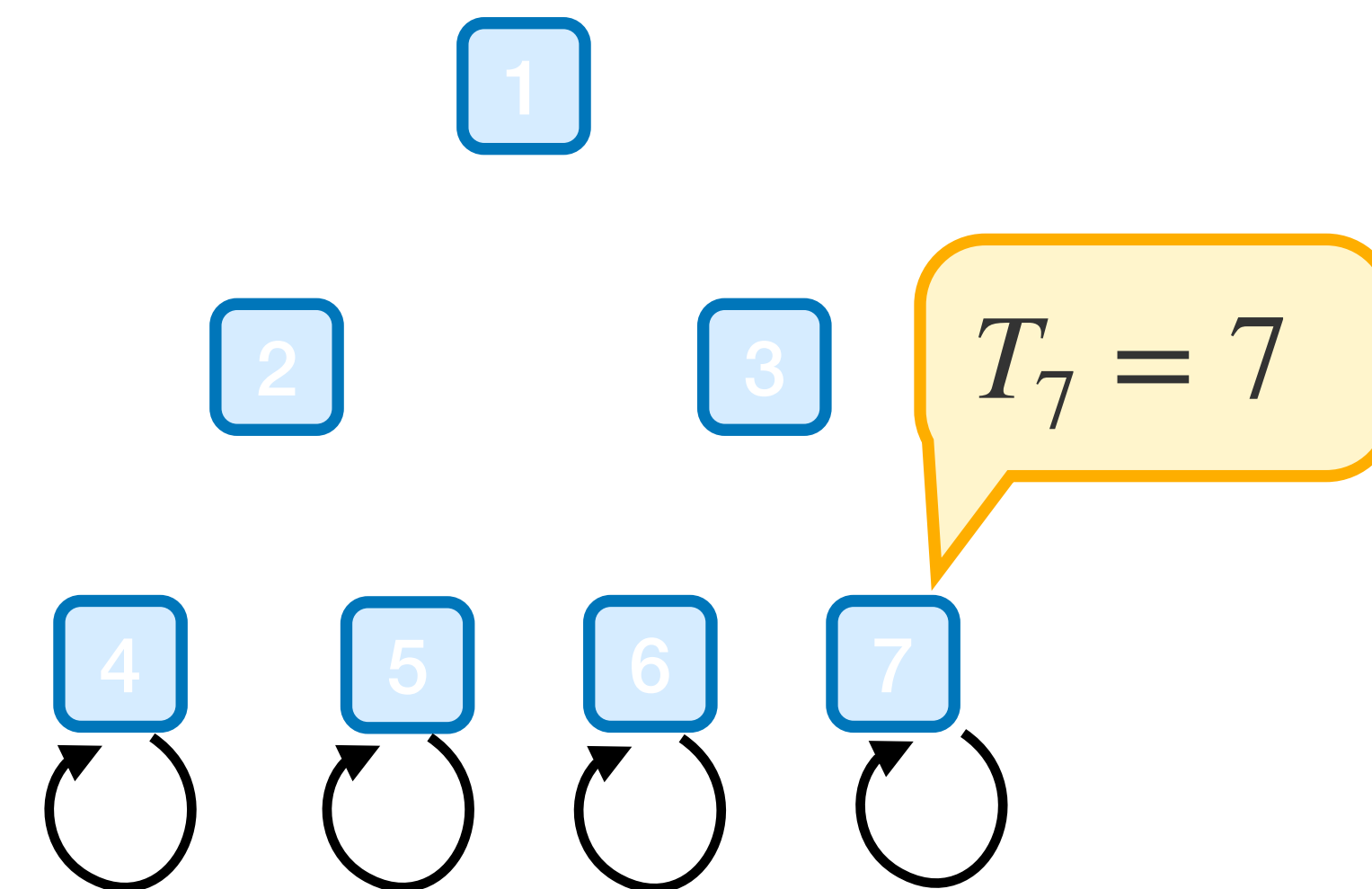
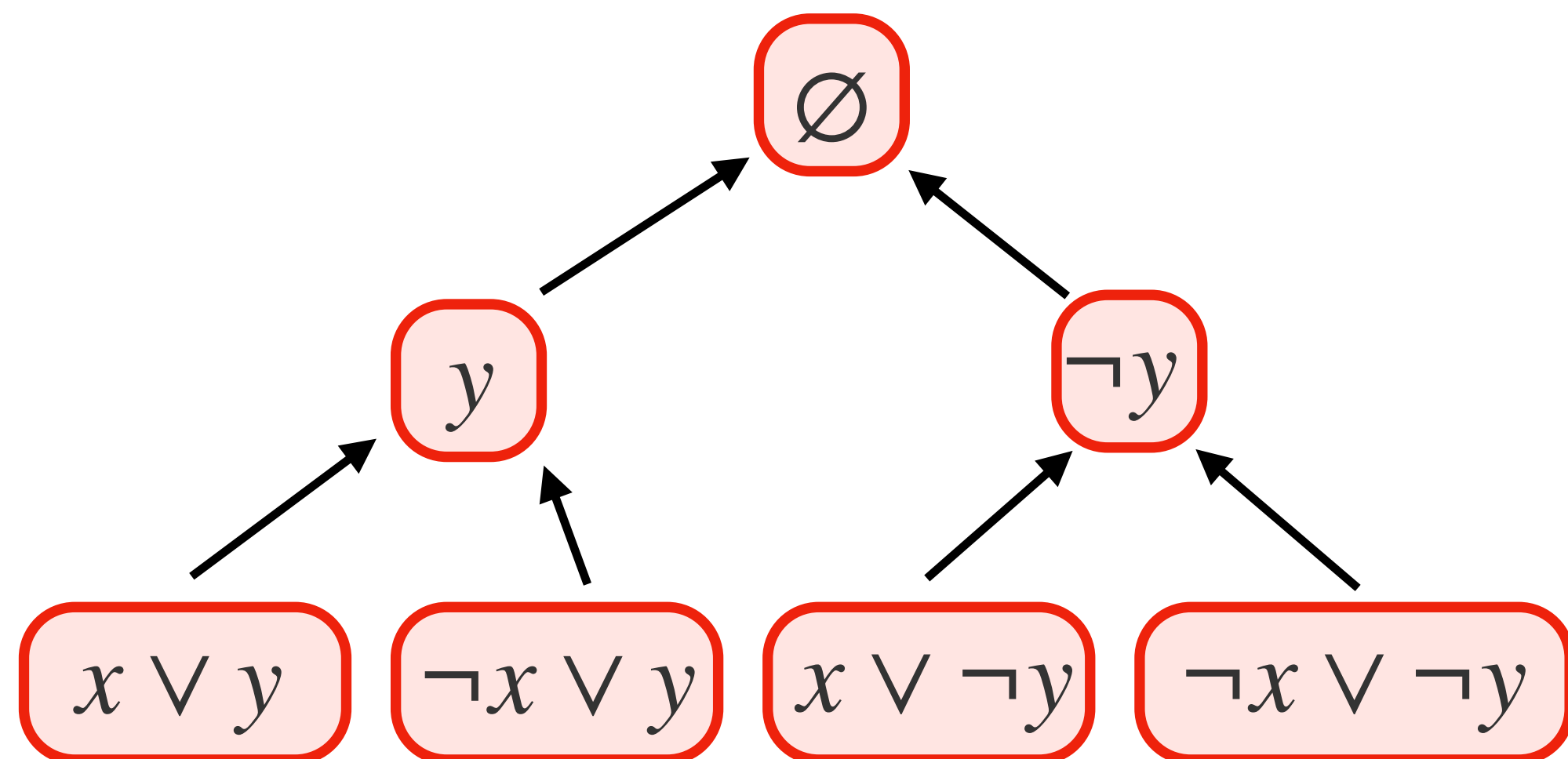
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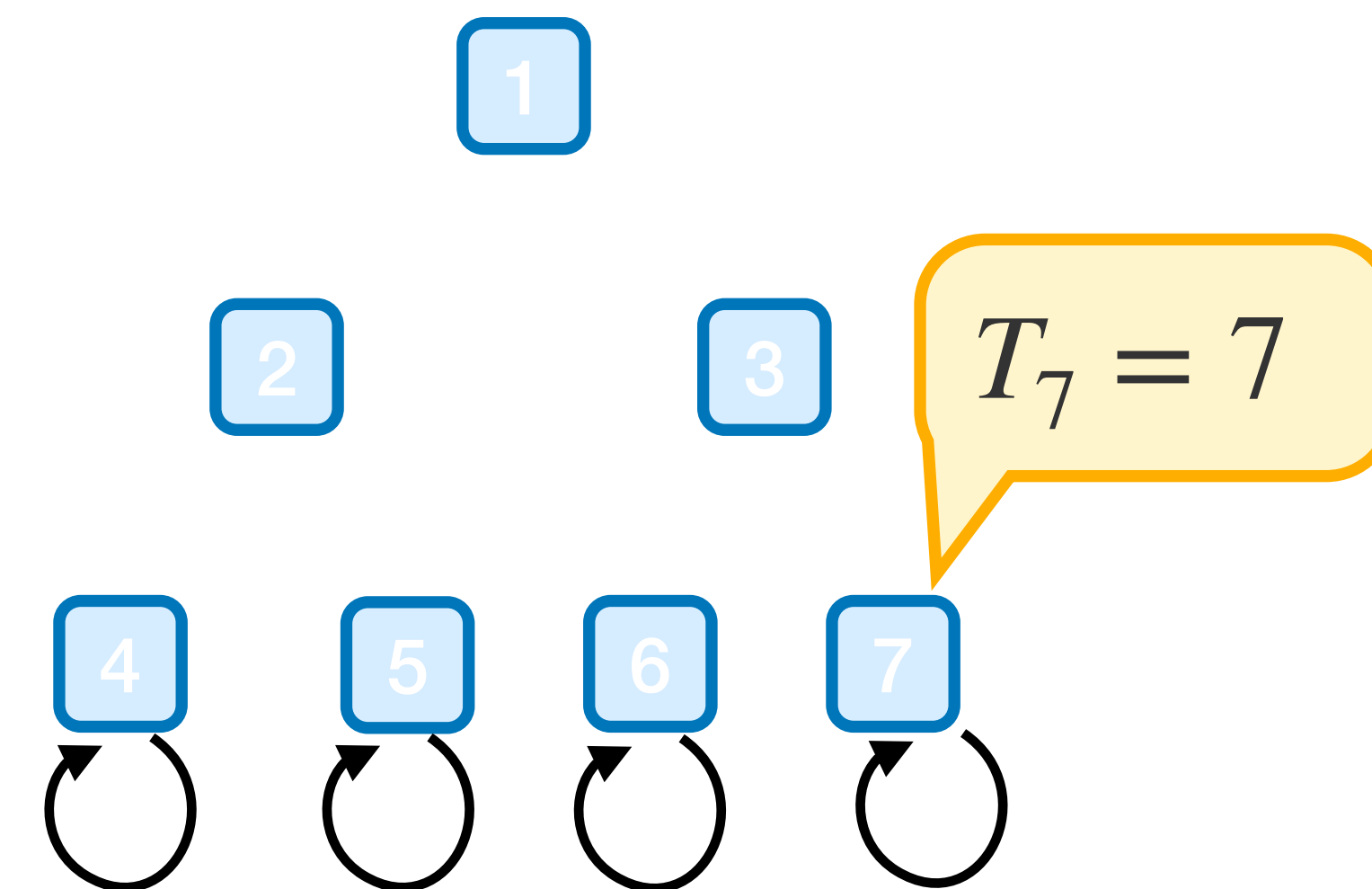
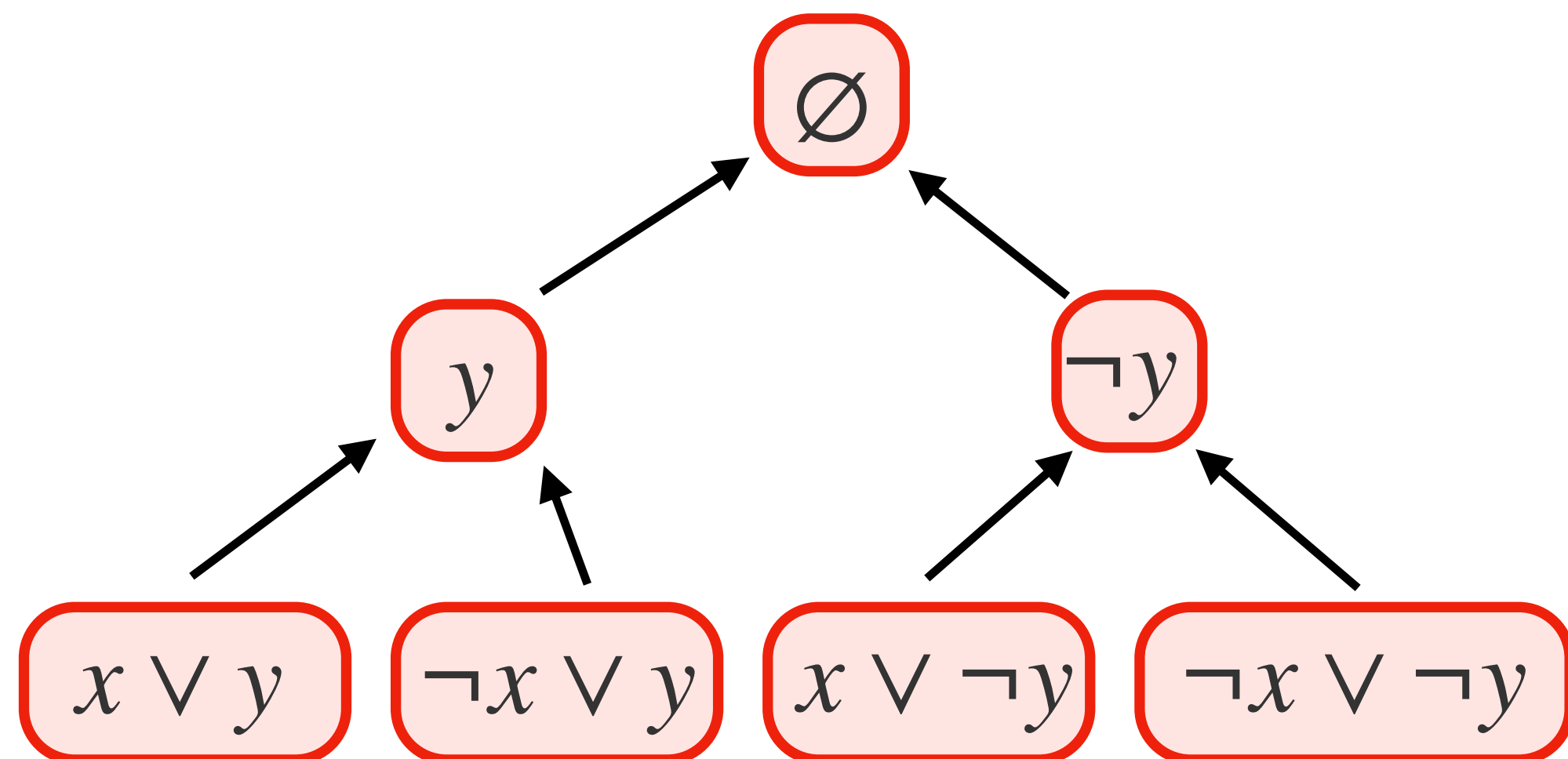
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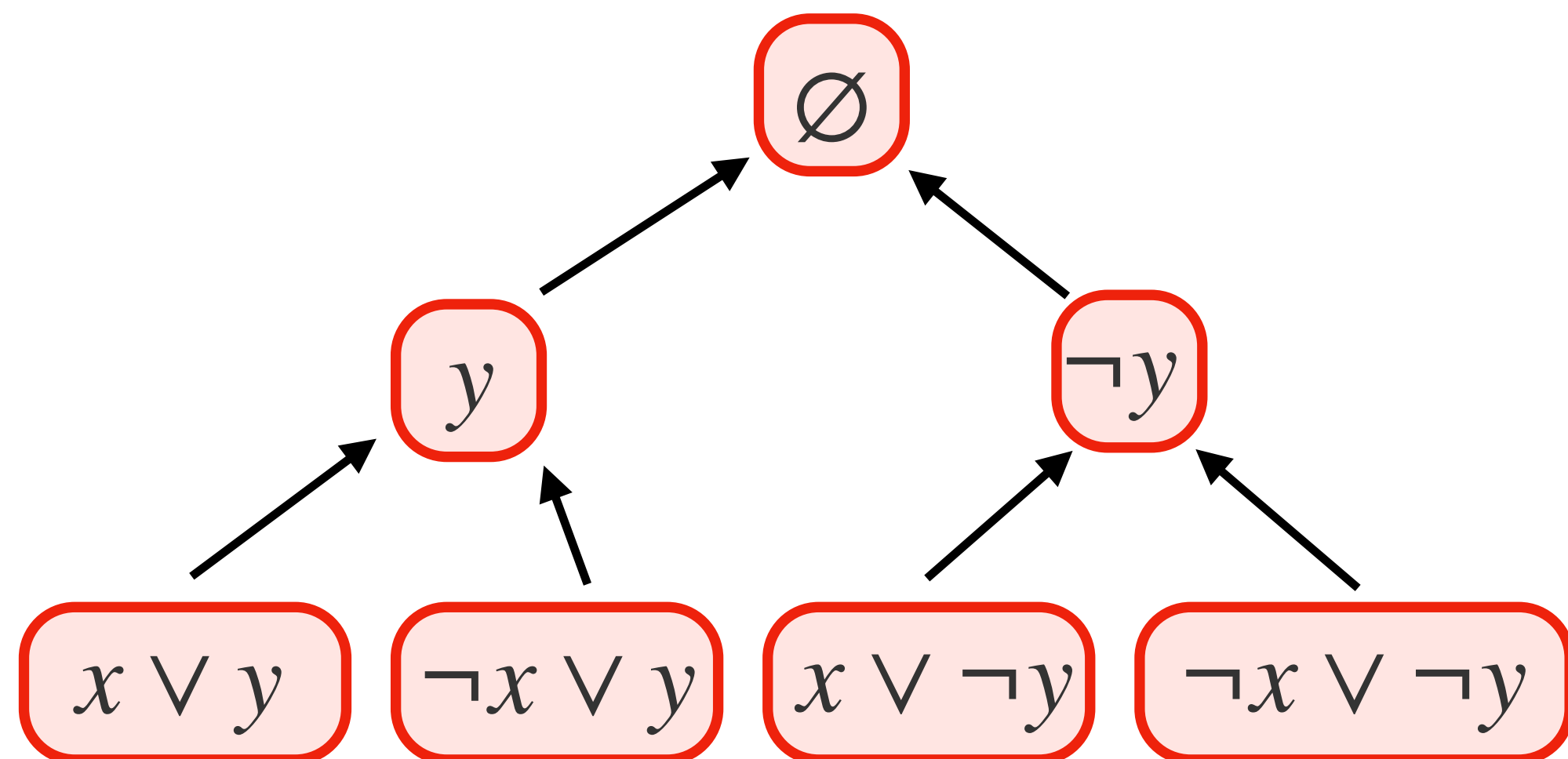
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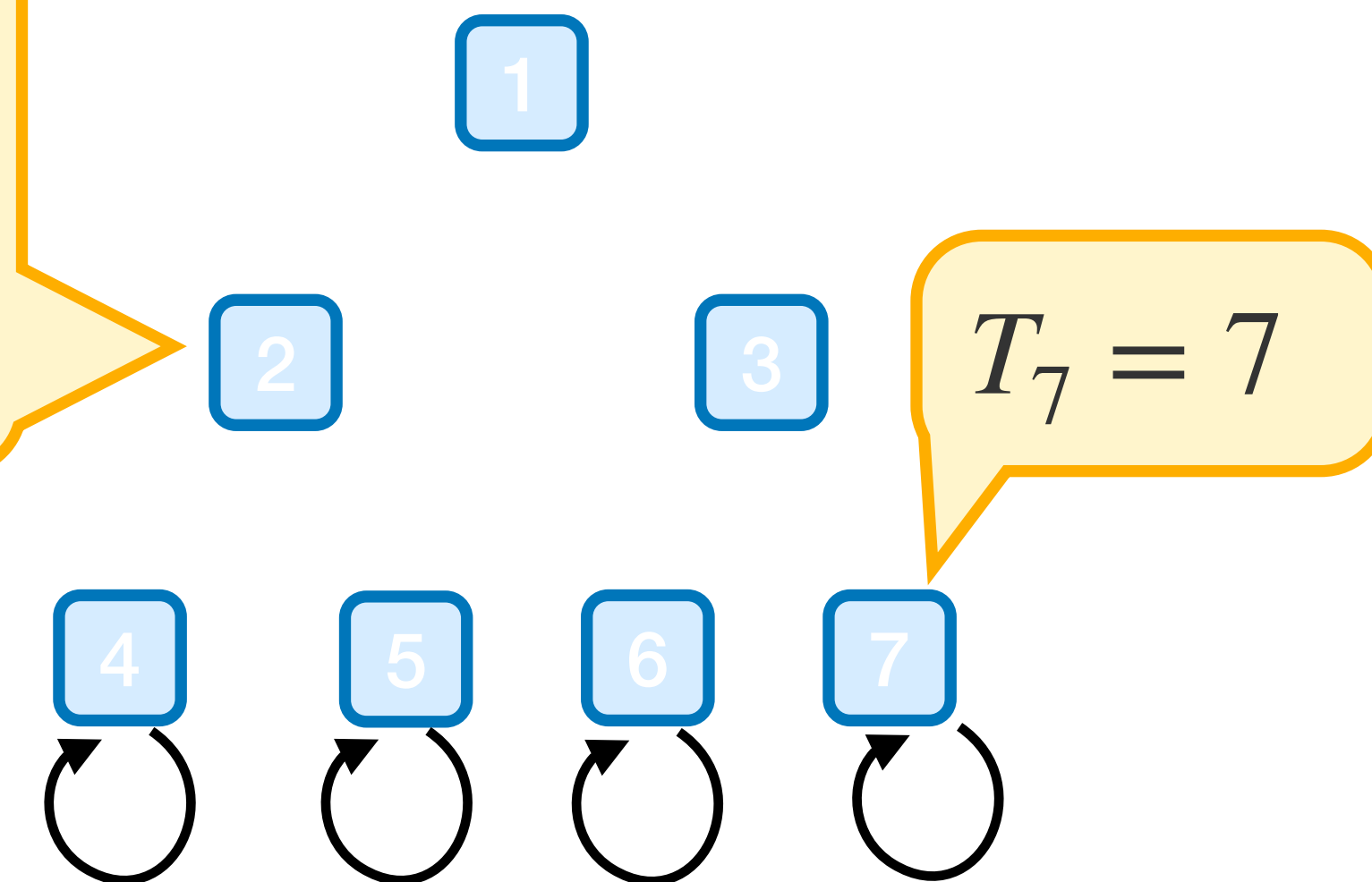
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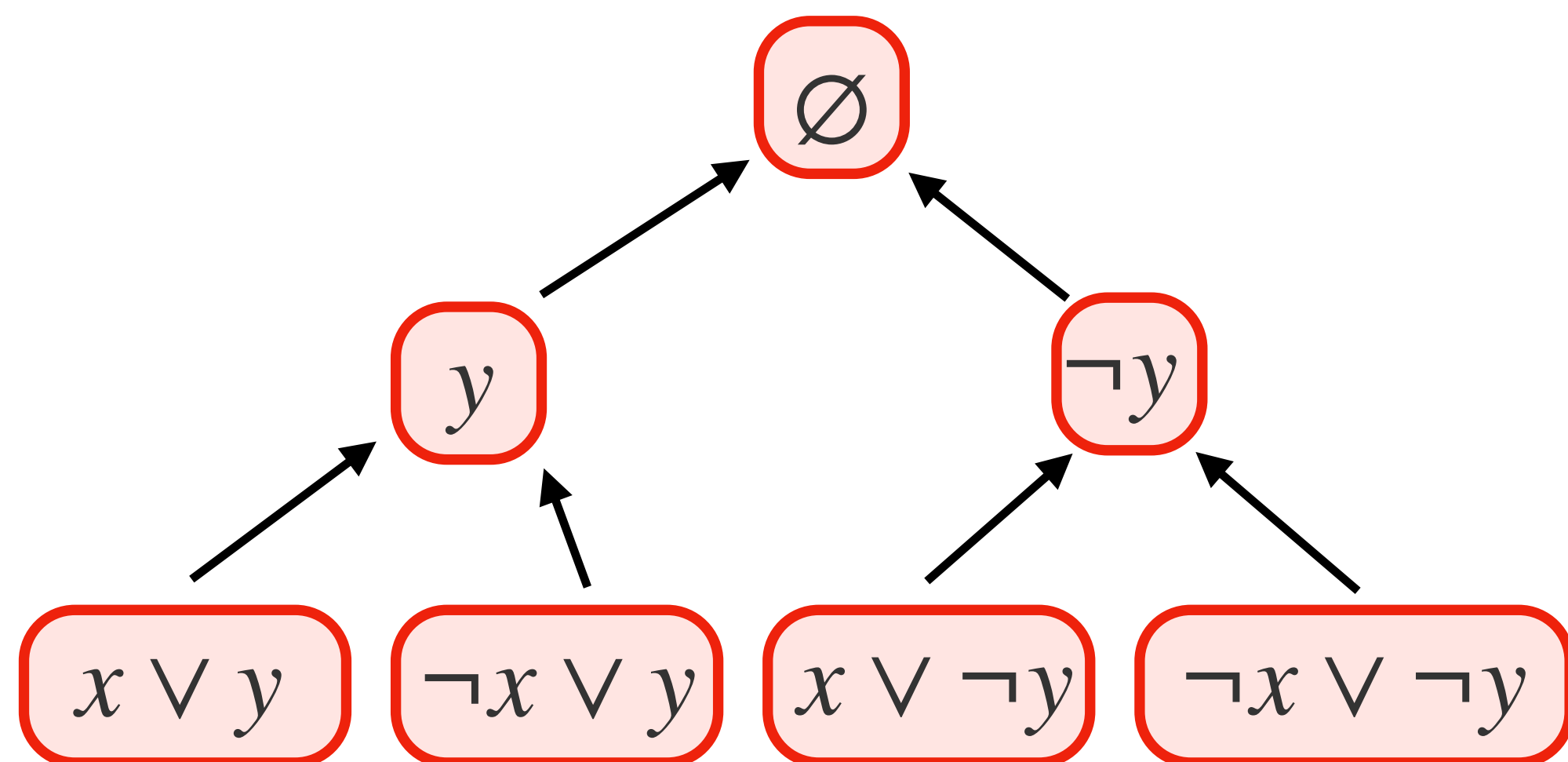
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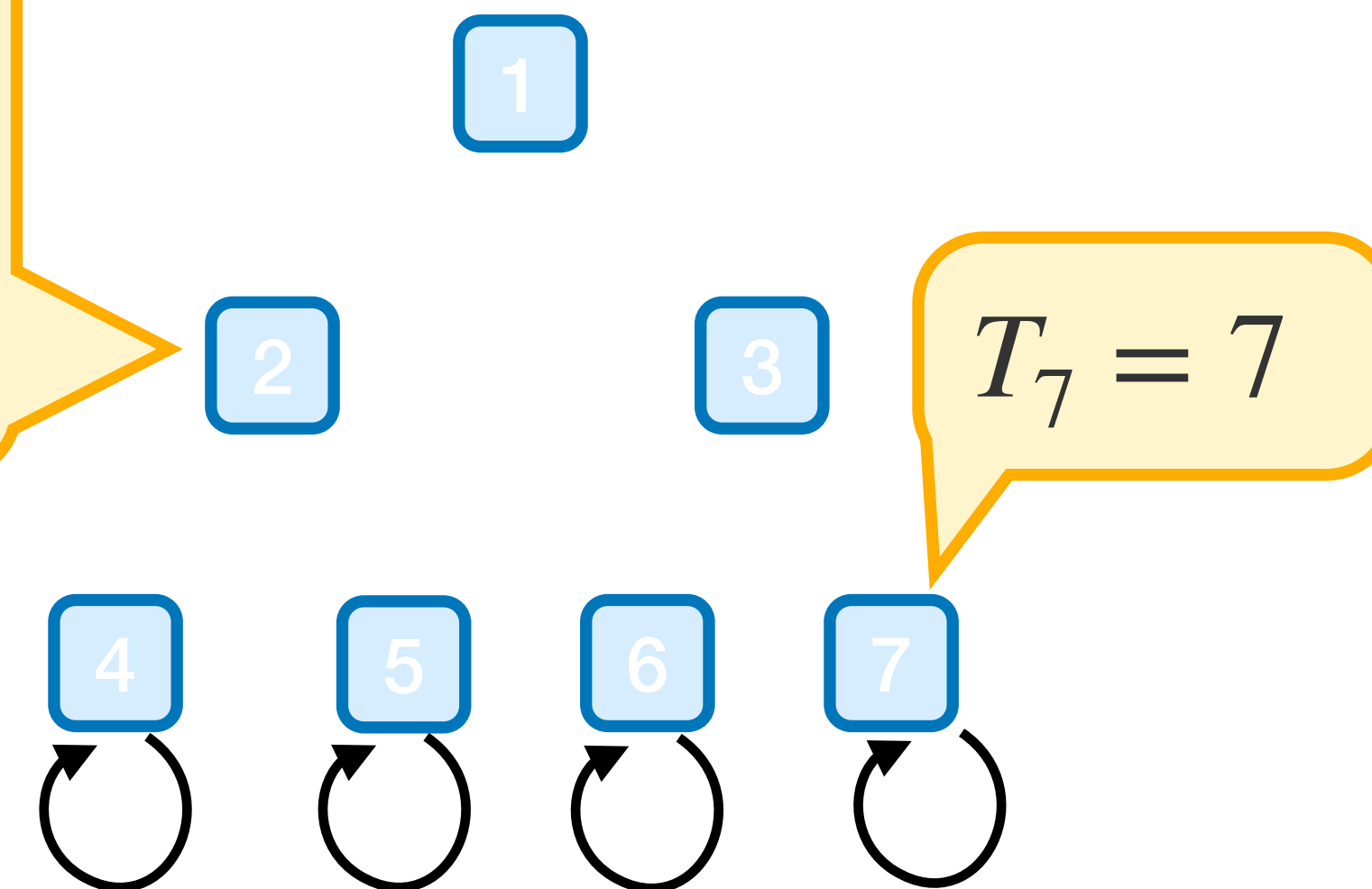
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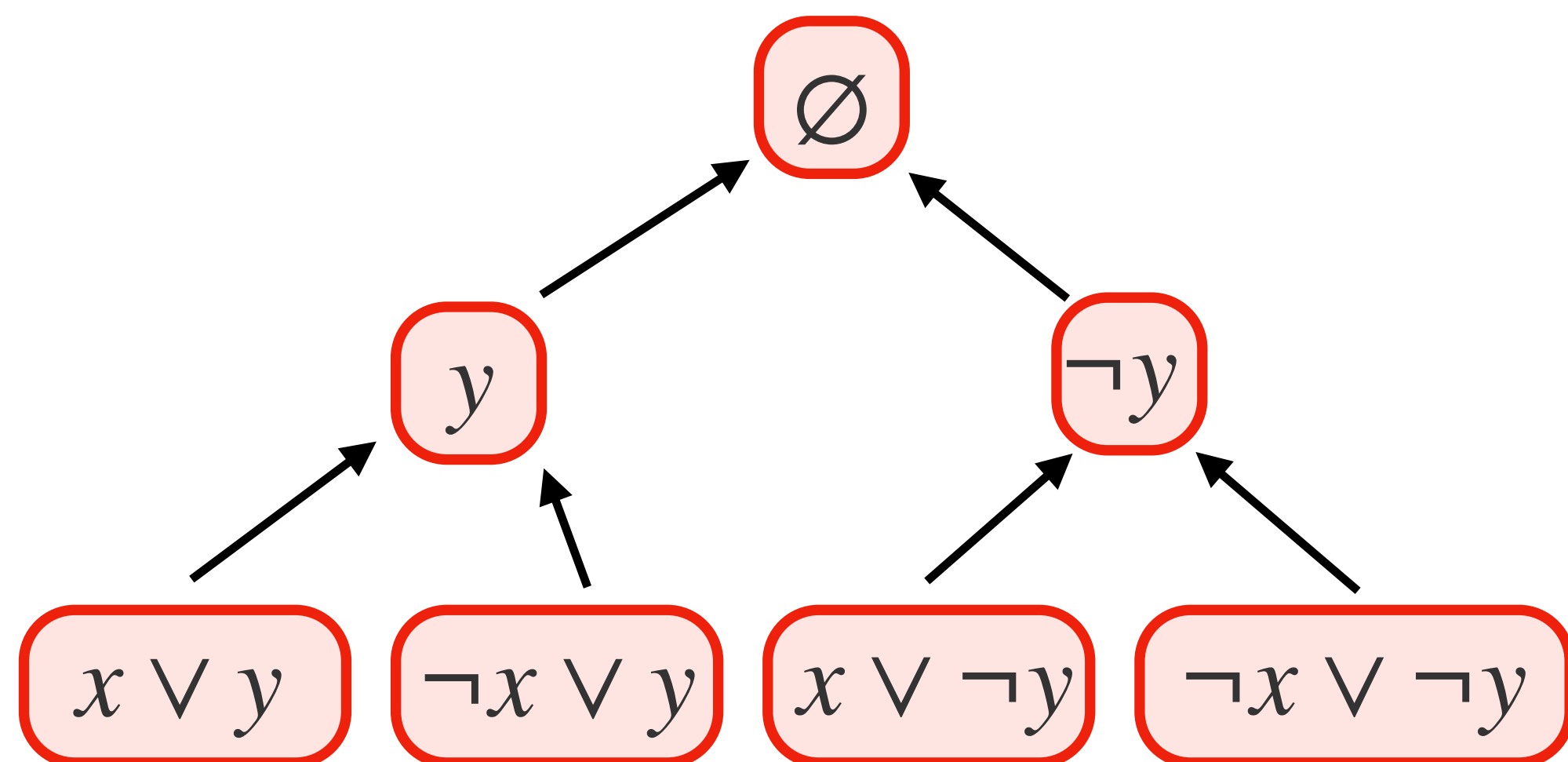
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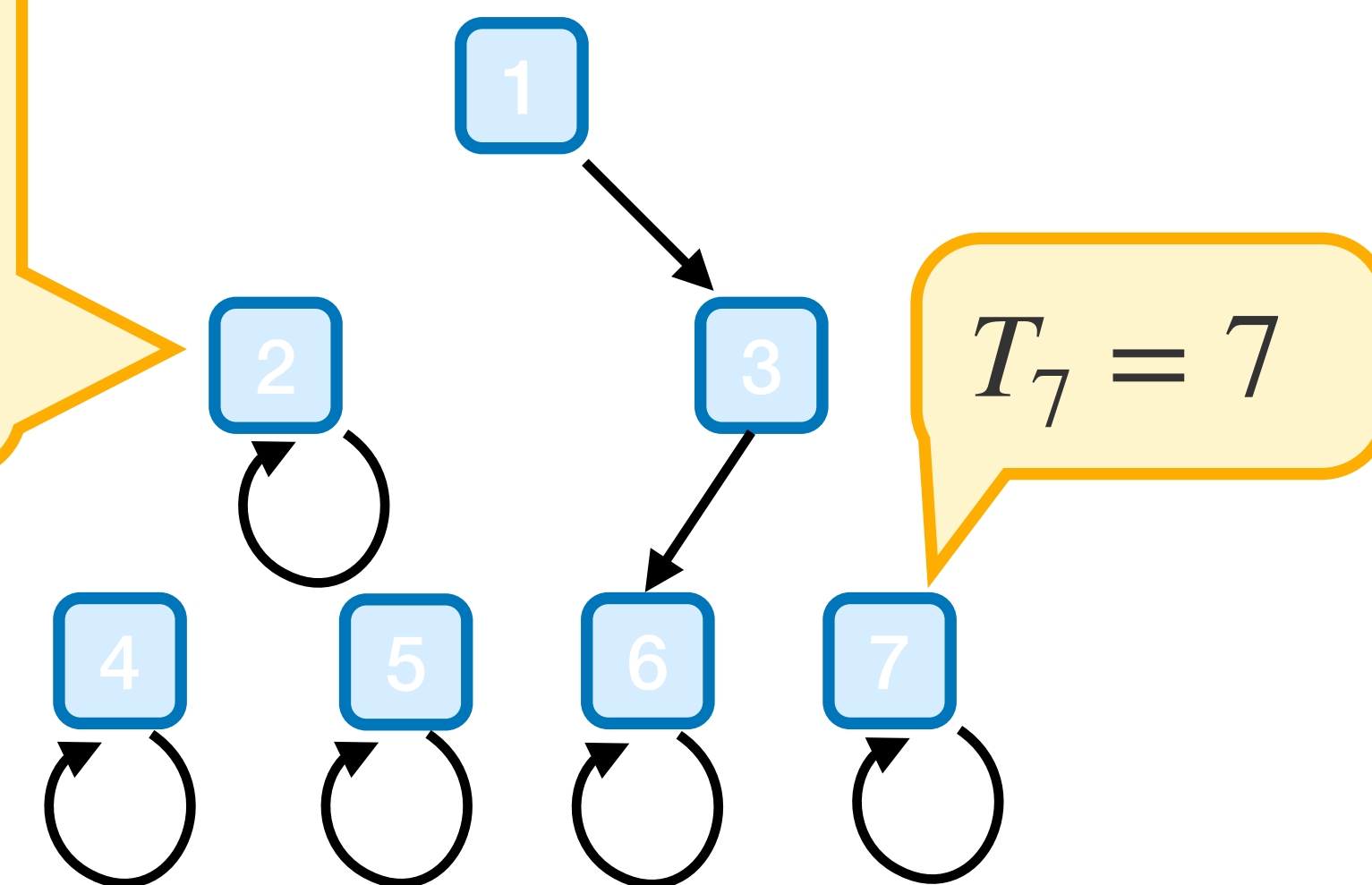
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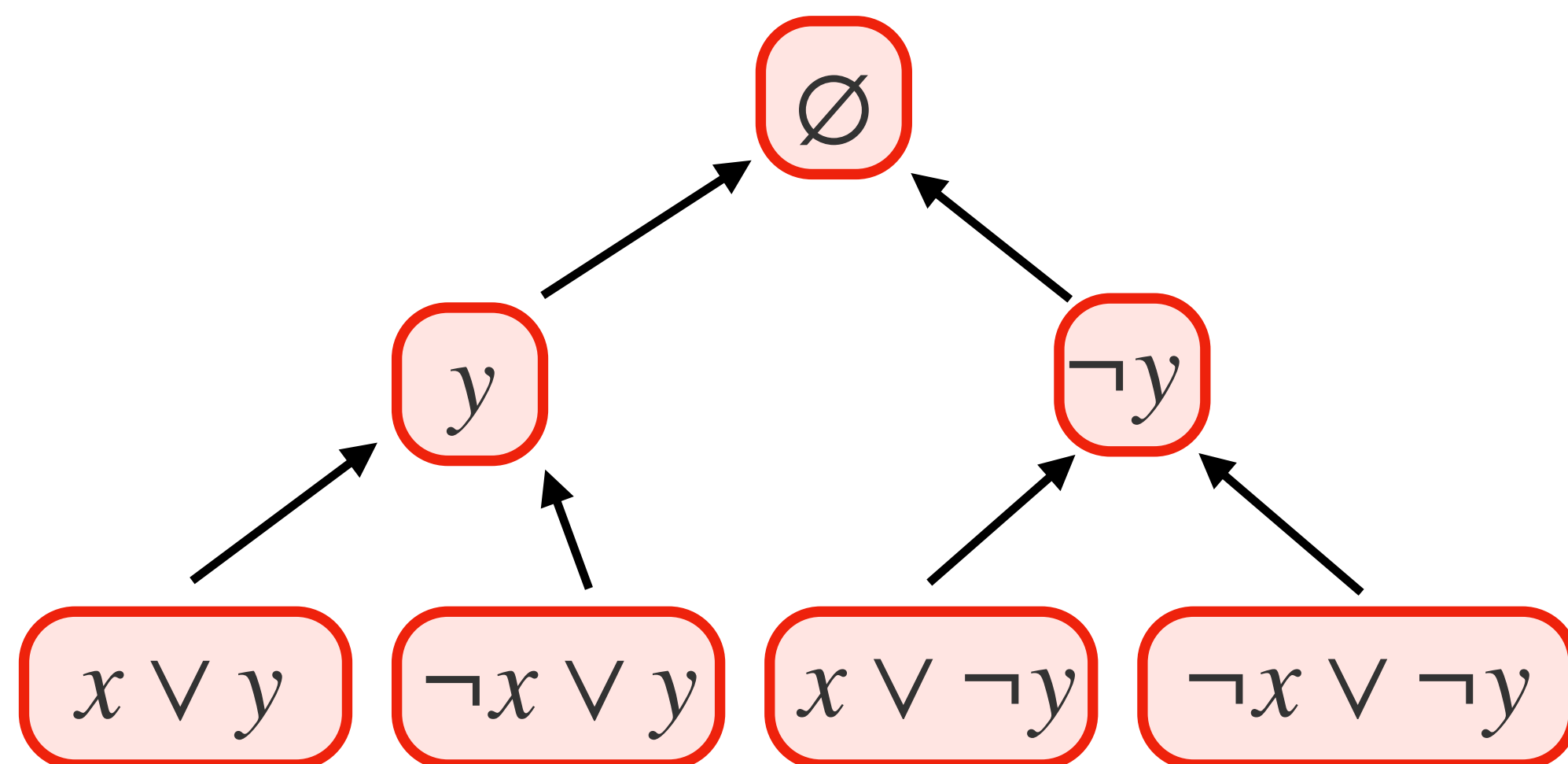
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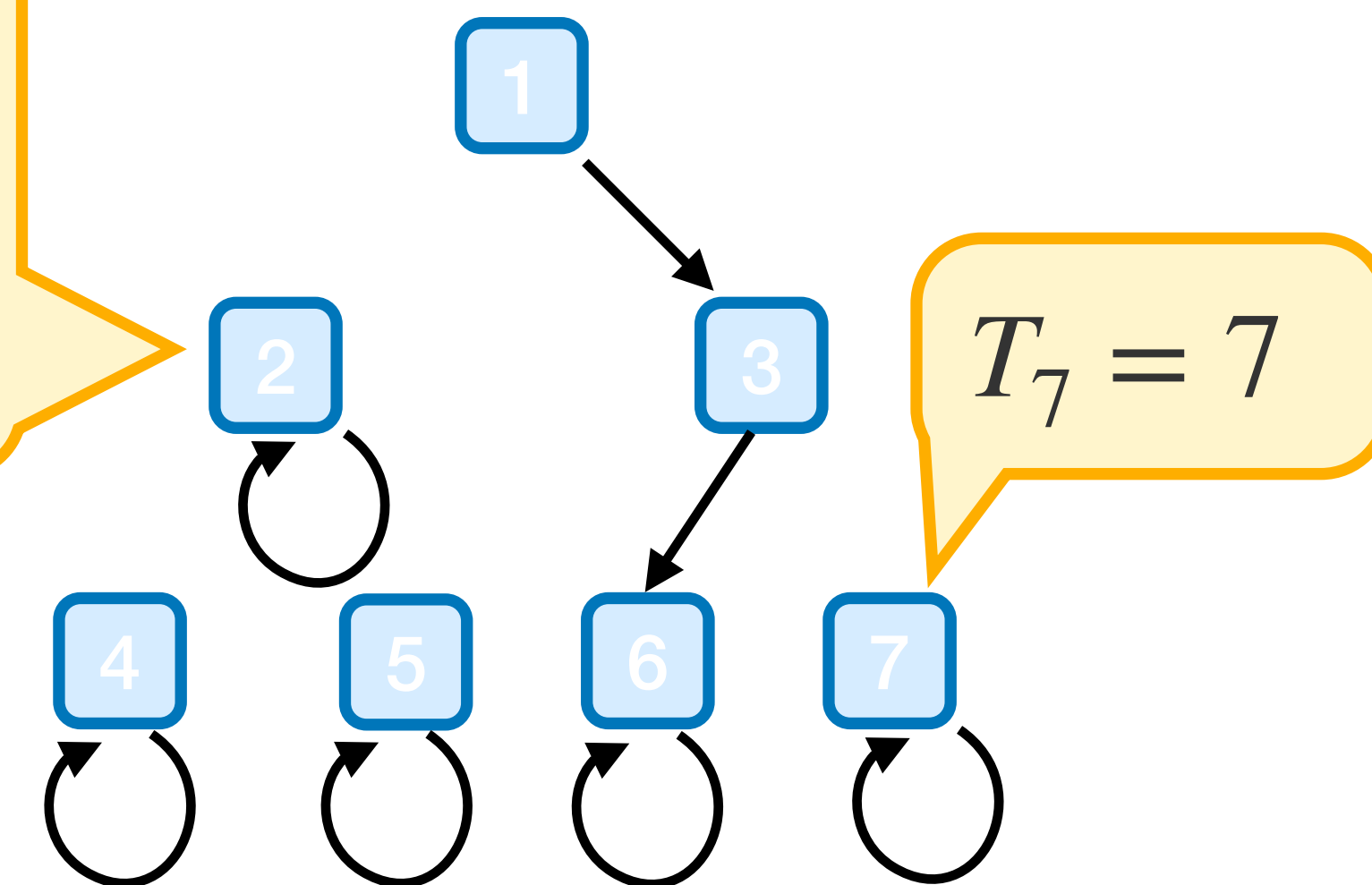
Resolution is sound \implies Solutions are false clauses!



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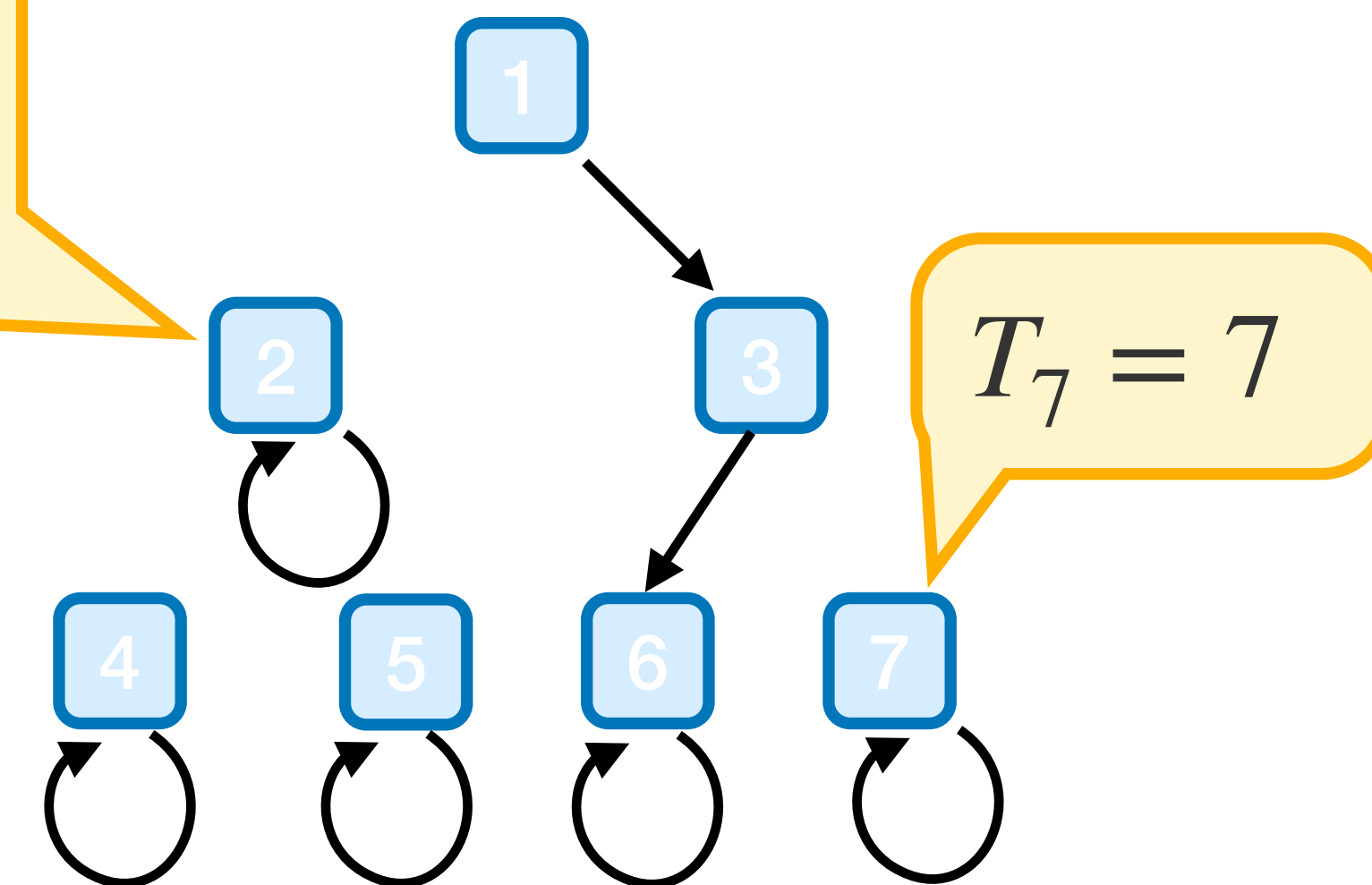
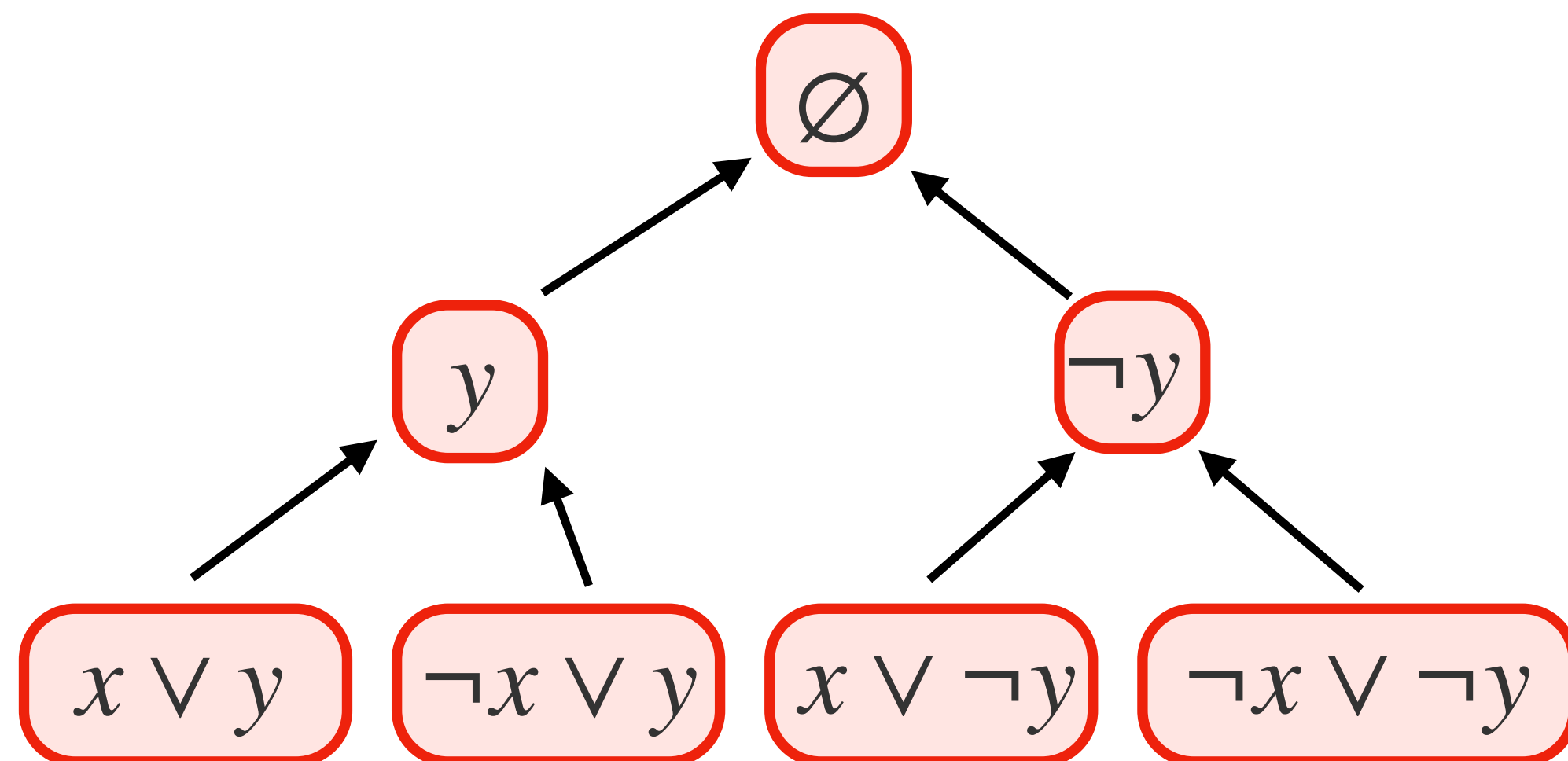


Resolution is sound \implies Solutions are false clauses!

T_2^o queries x, y :

$$T_2^o = \begin{cases} C_1 & \text{if } x \vee y = 0 \\ C_2 & \text{otherwise} \end{cases}$$

e.g. $x = 01$



Resolution is PLS

Resolution Complexity: of proof Π is $\log \text{size}(\Pi) + \text{width}(\Pi)$

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Delayer Prover Game on F :

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w -Prover strategy \implies Complexity $w \log n$ Resolution proof

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Memory

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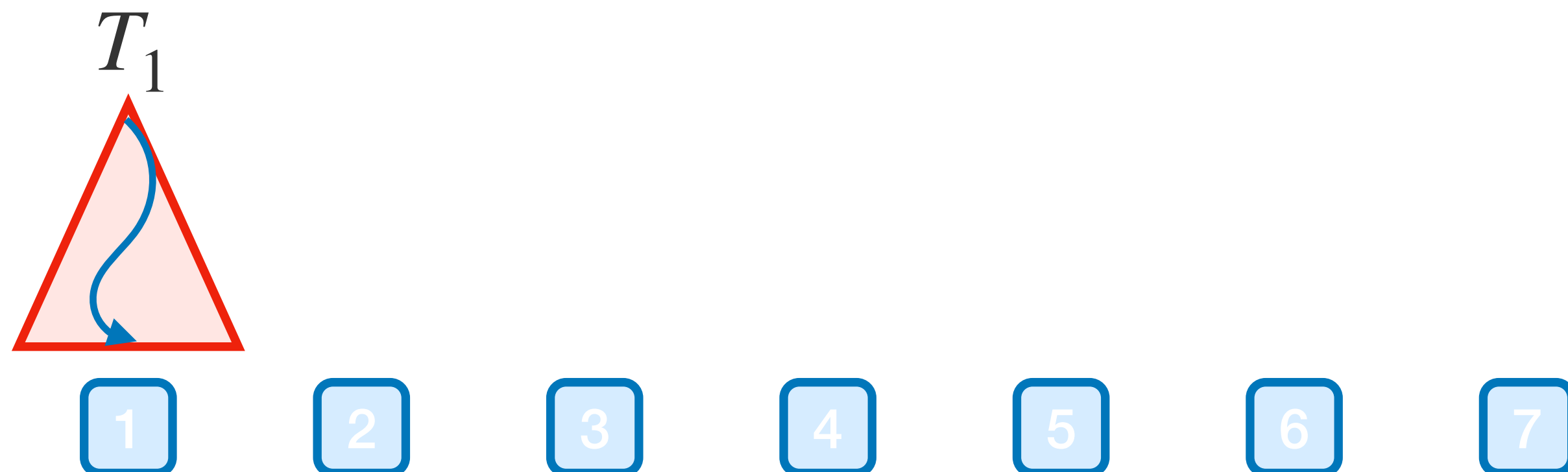
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T_1

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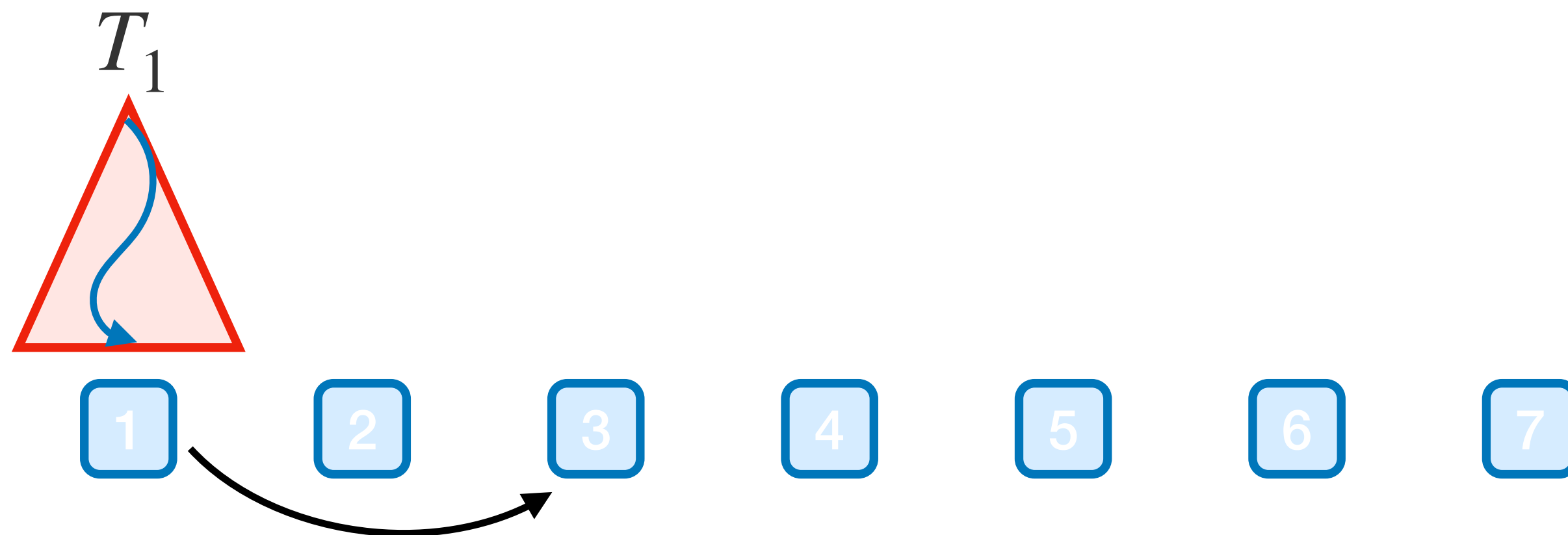
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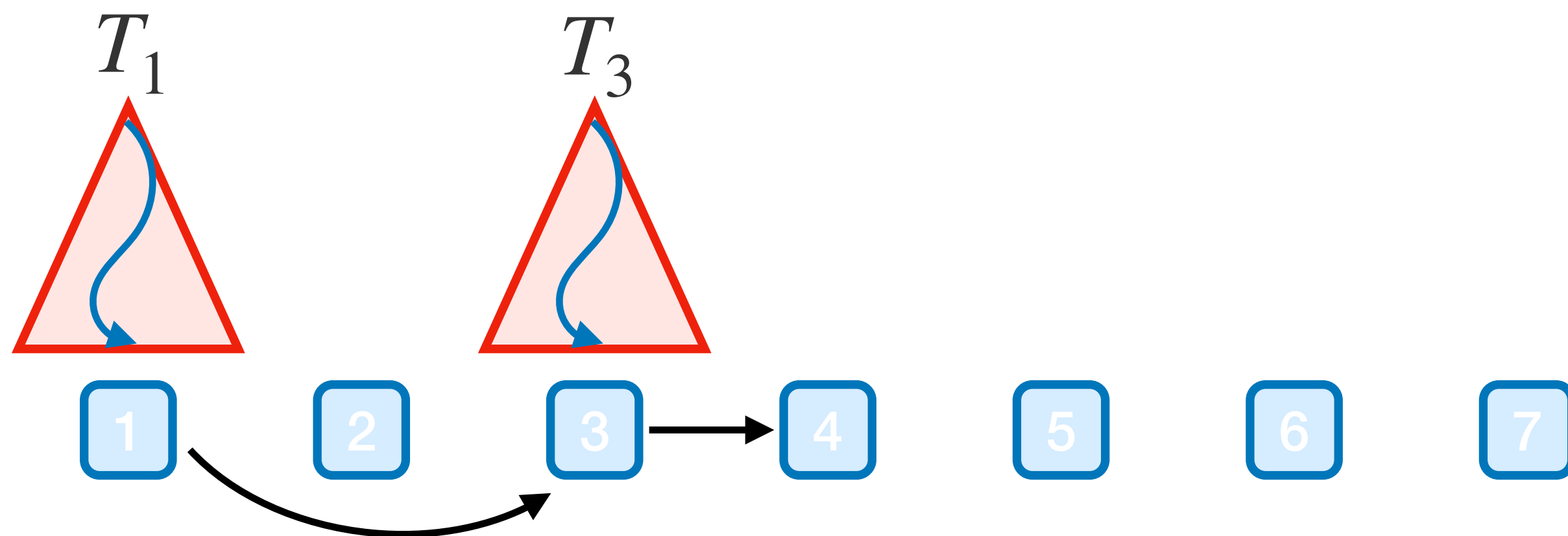
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Memory

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T_3

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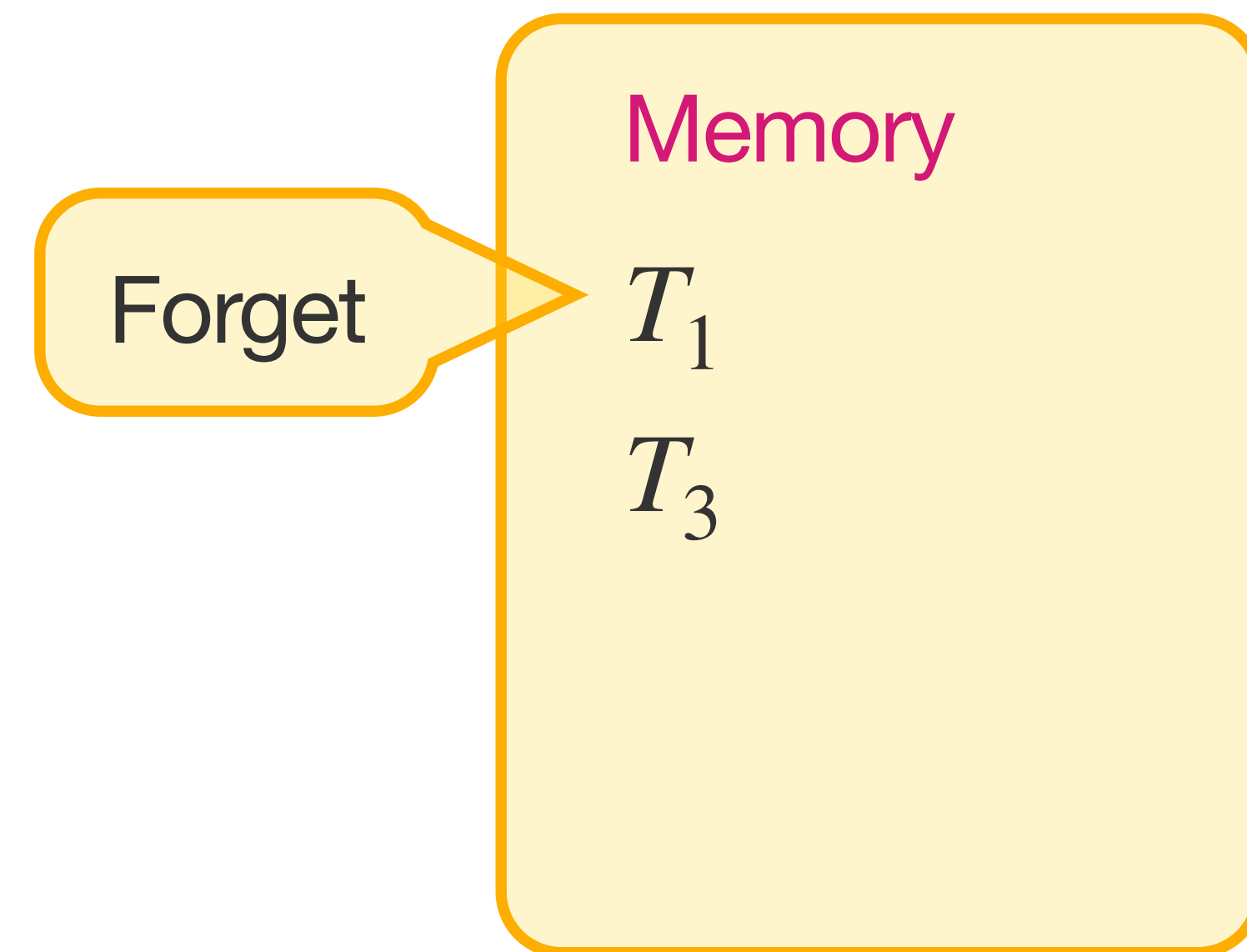
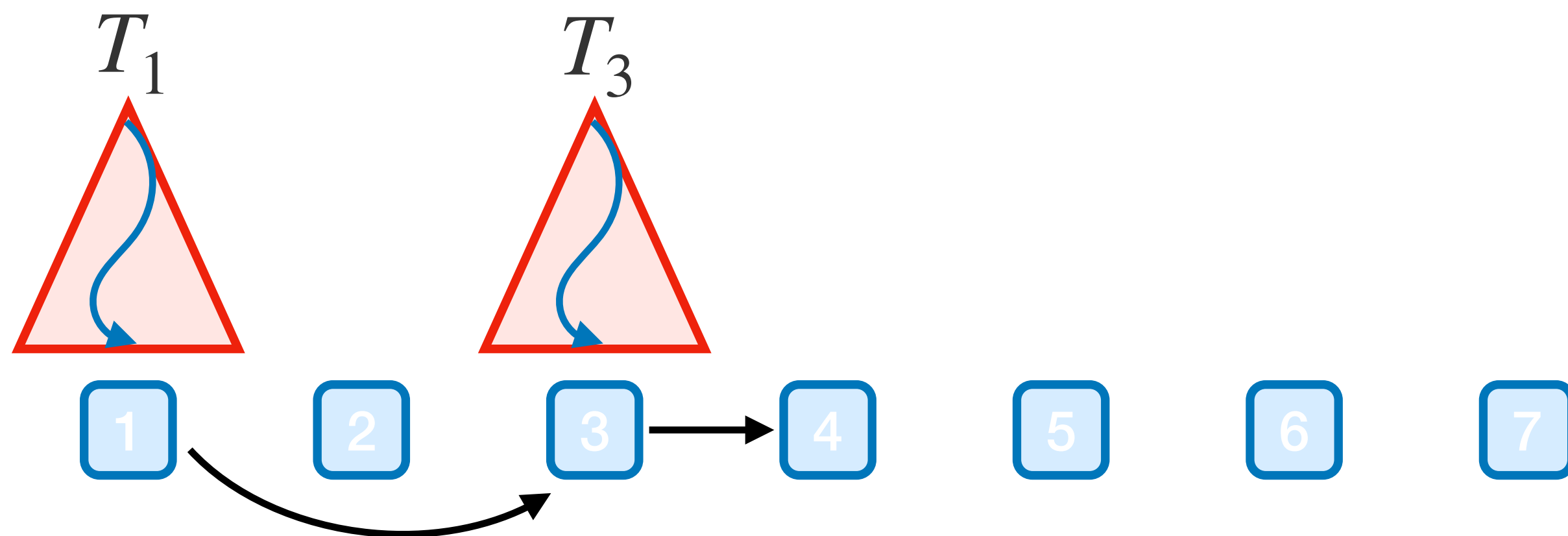
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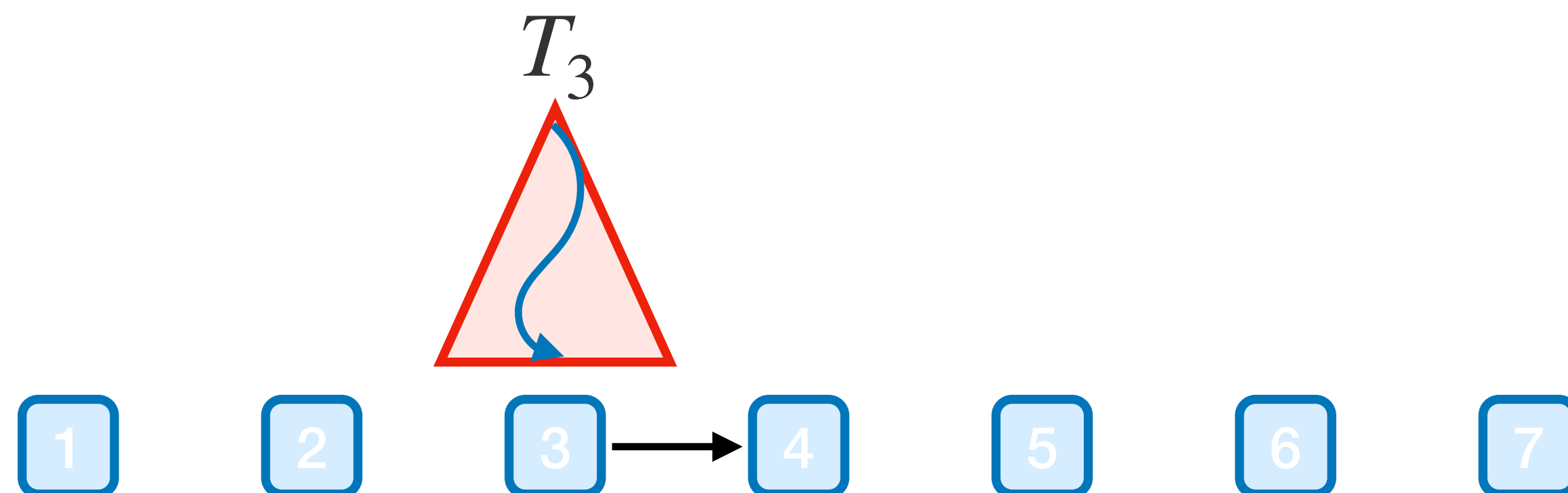
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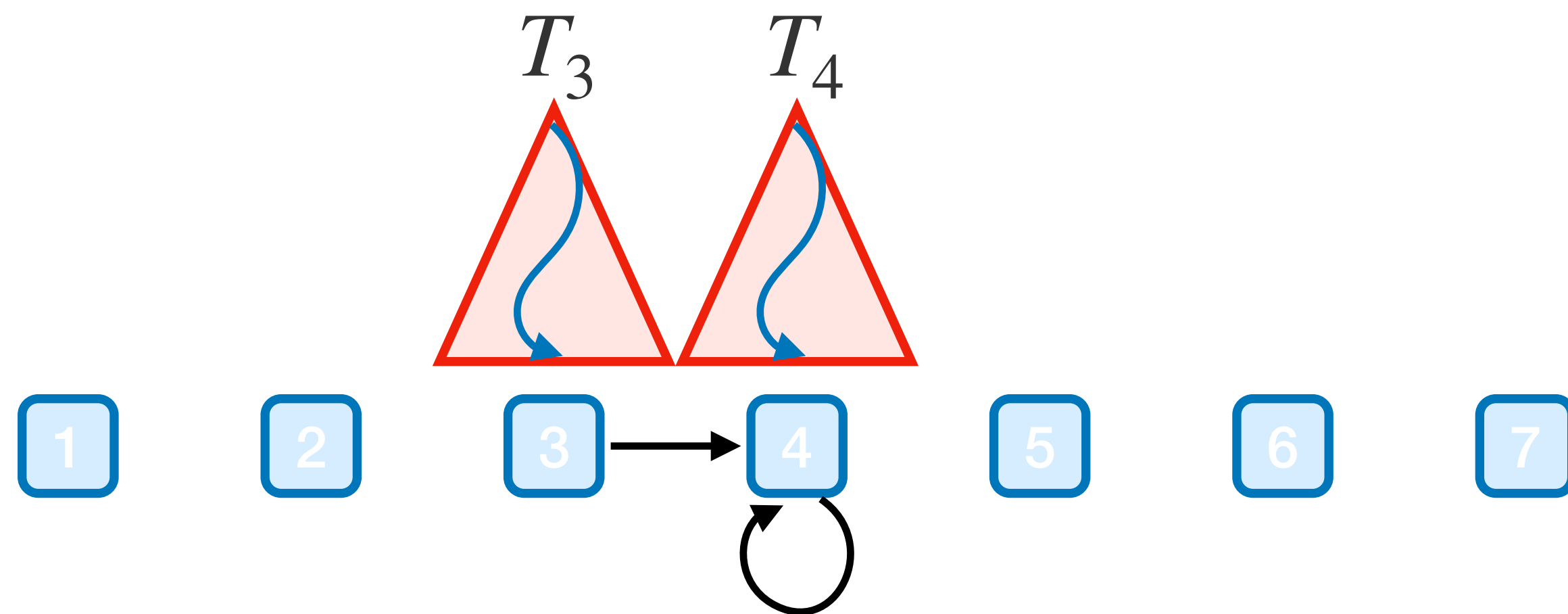
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Memory

T_3

T_4

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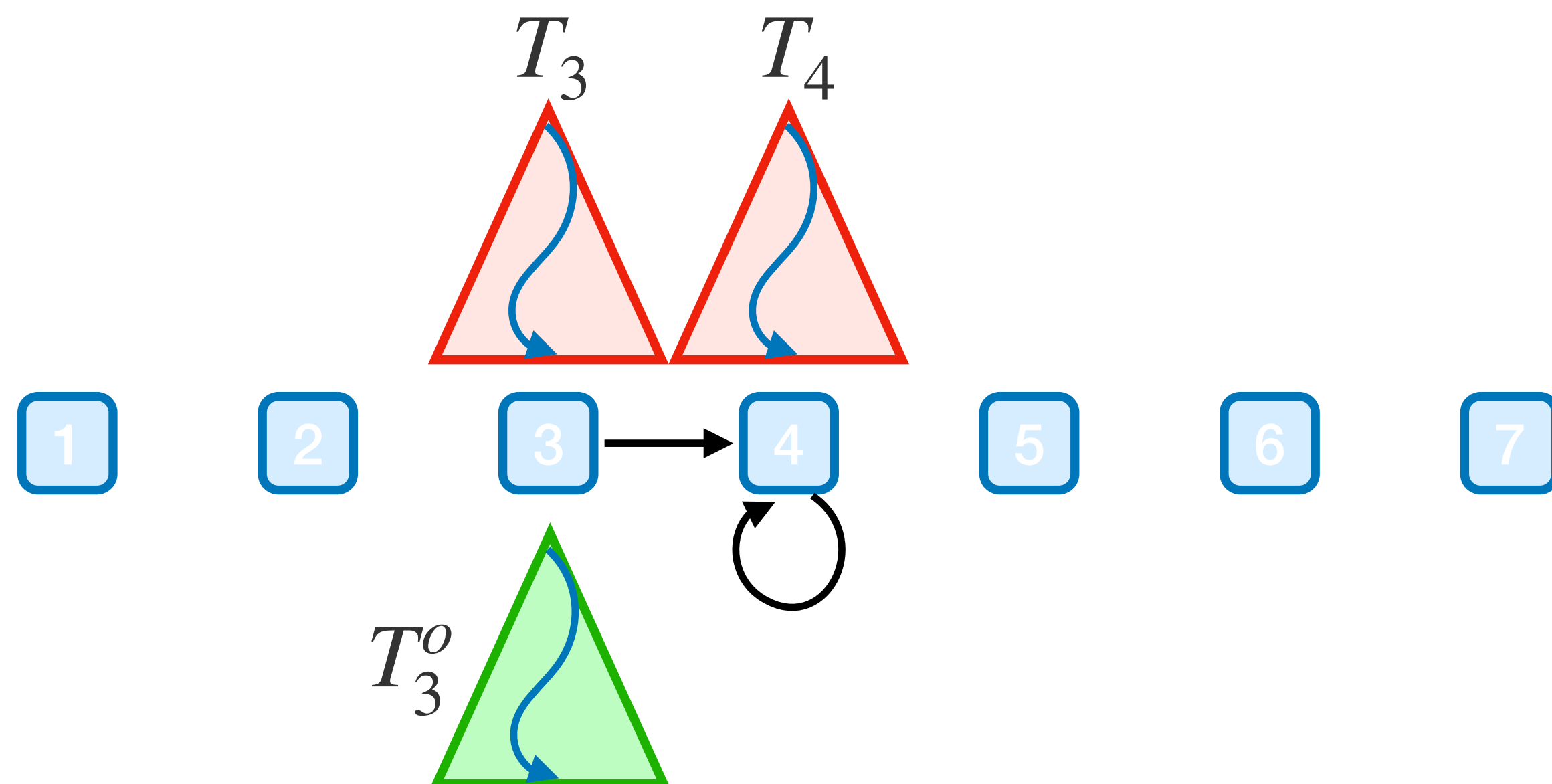
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Memory

T_3

T_4

T_3^0

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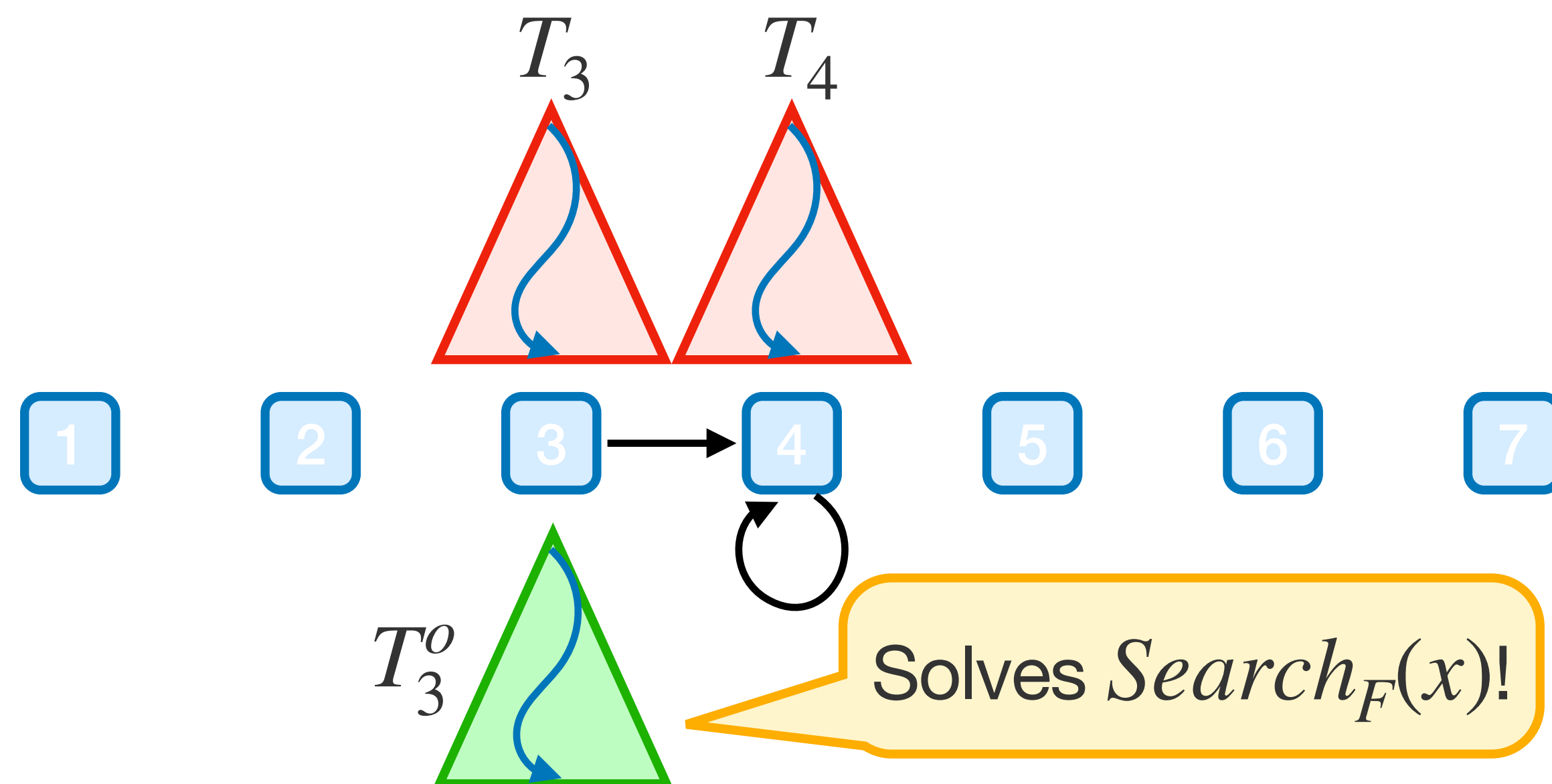
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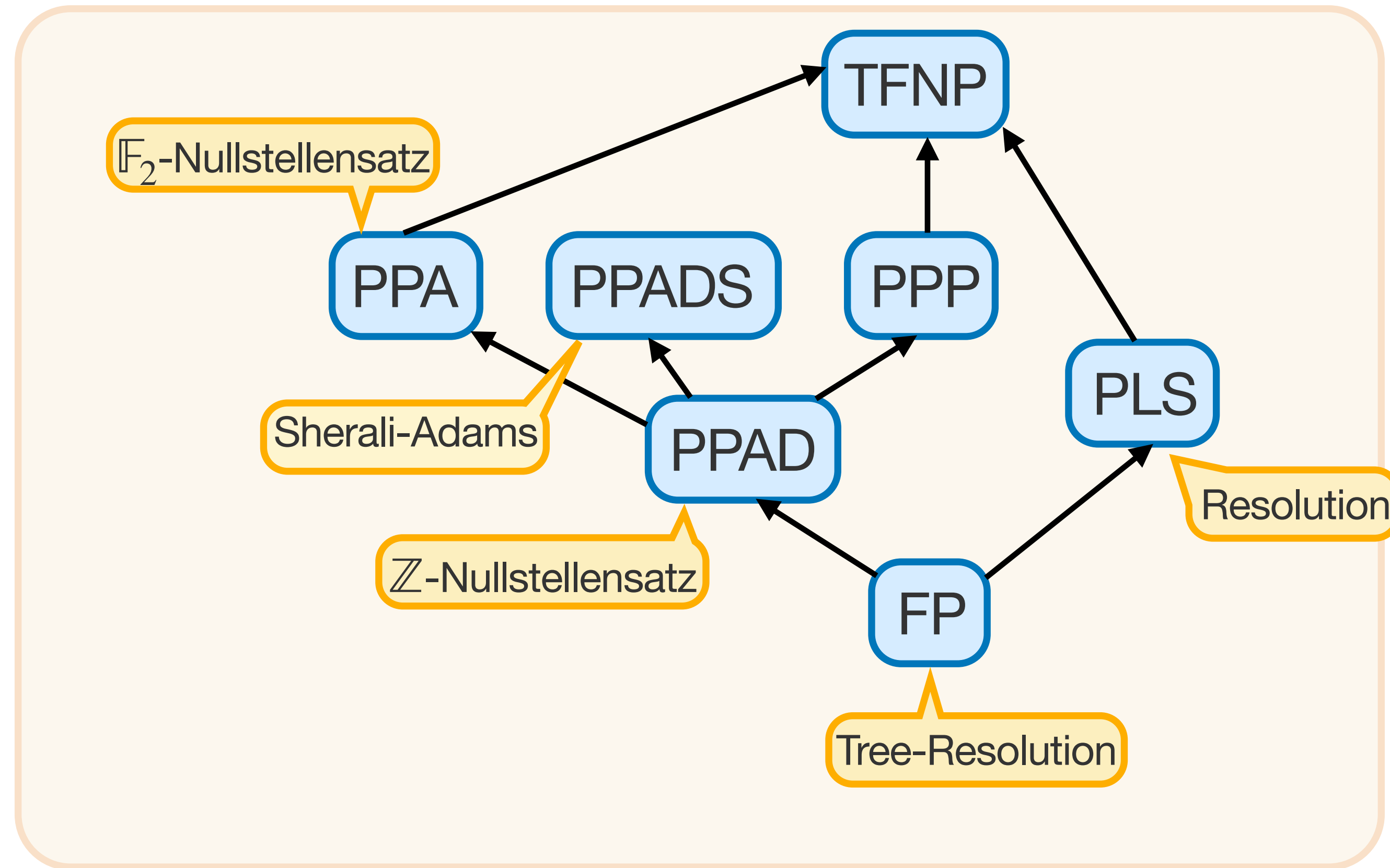
Memory

T_3

T_4

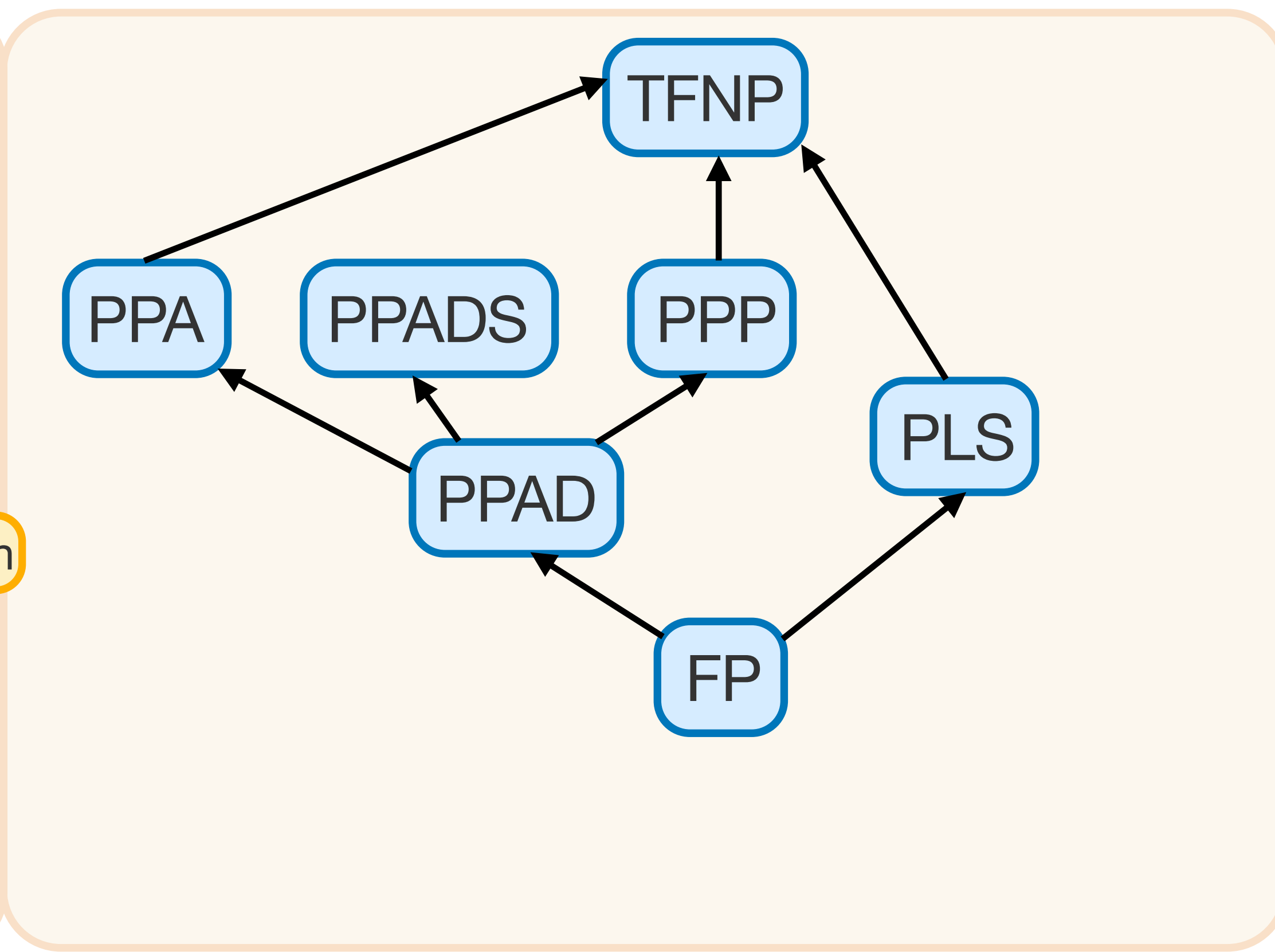
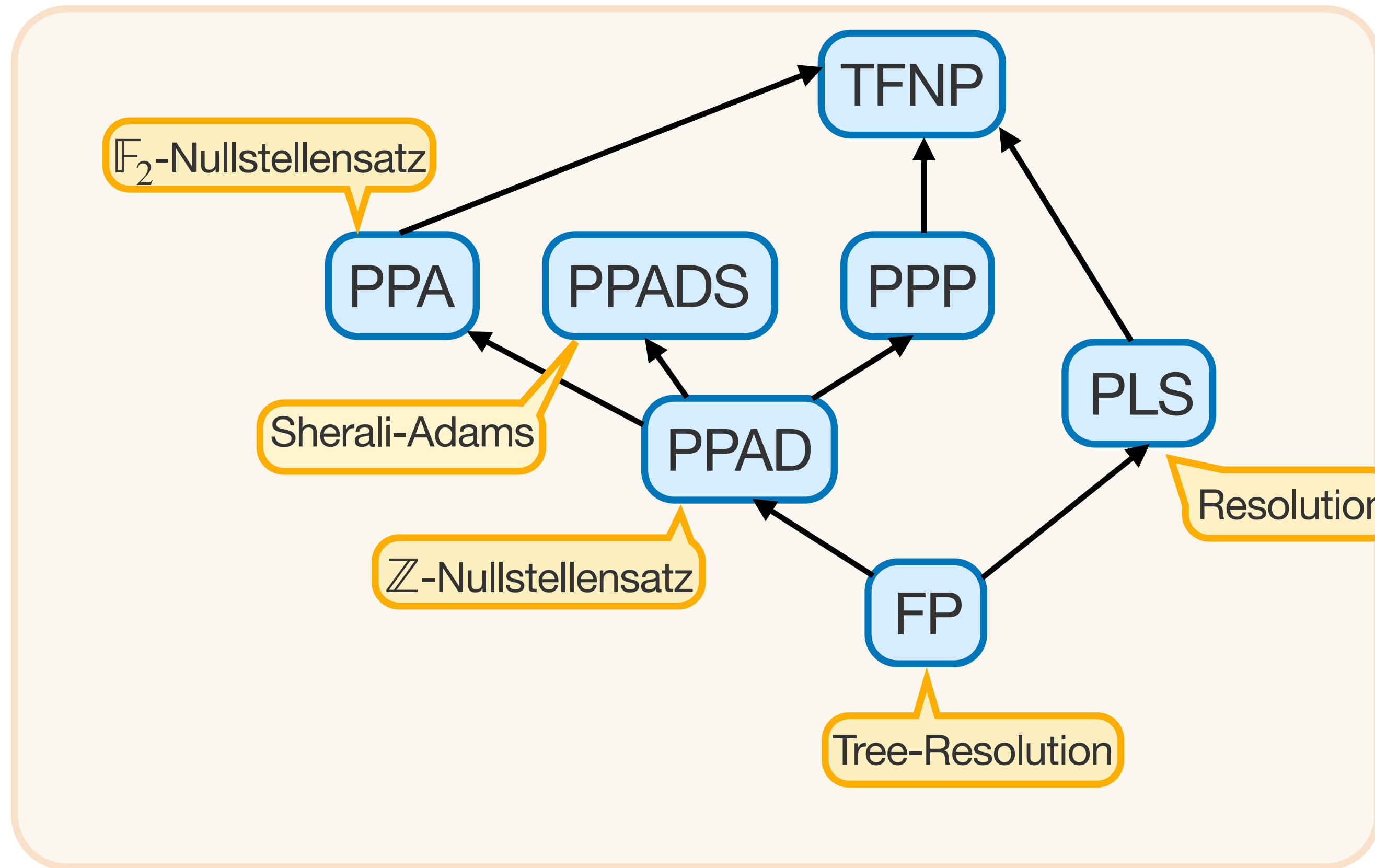
T_3^0

TFNP



Model of Computation: Decision Trees

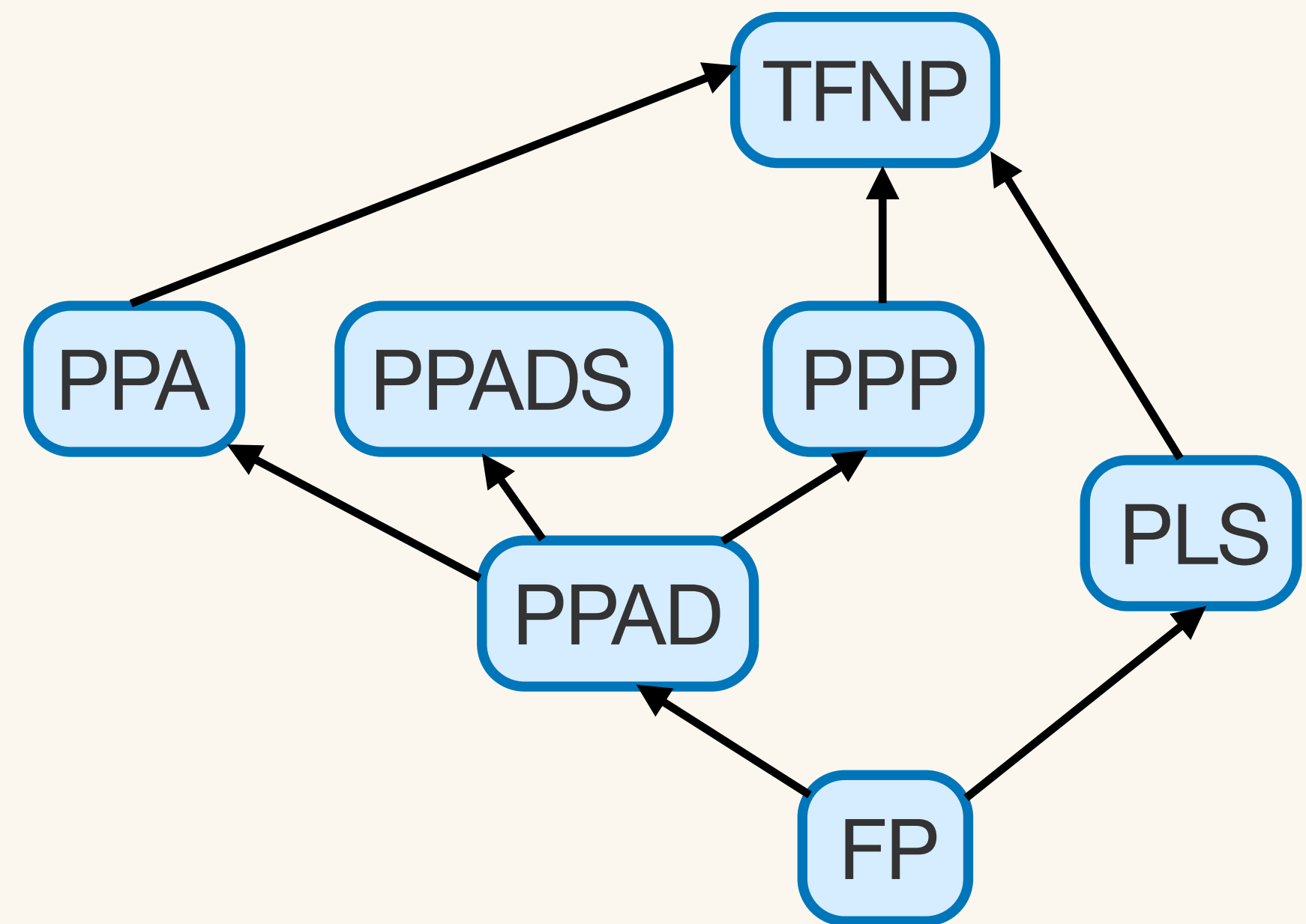
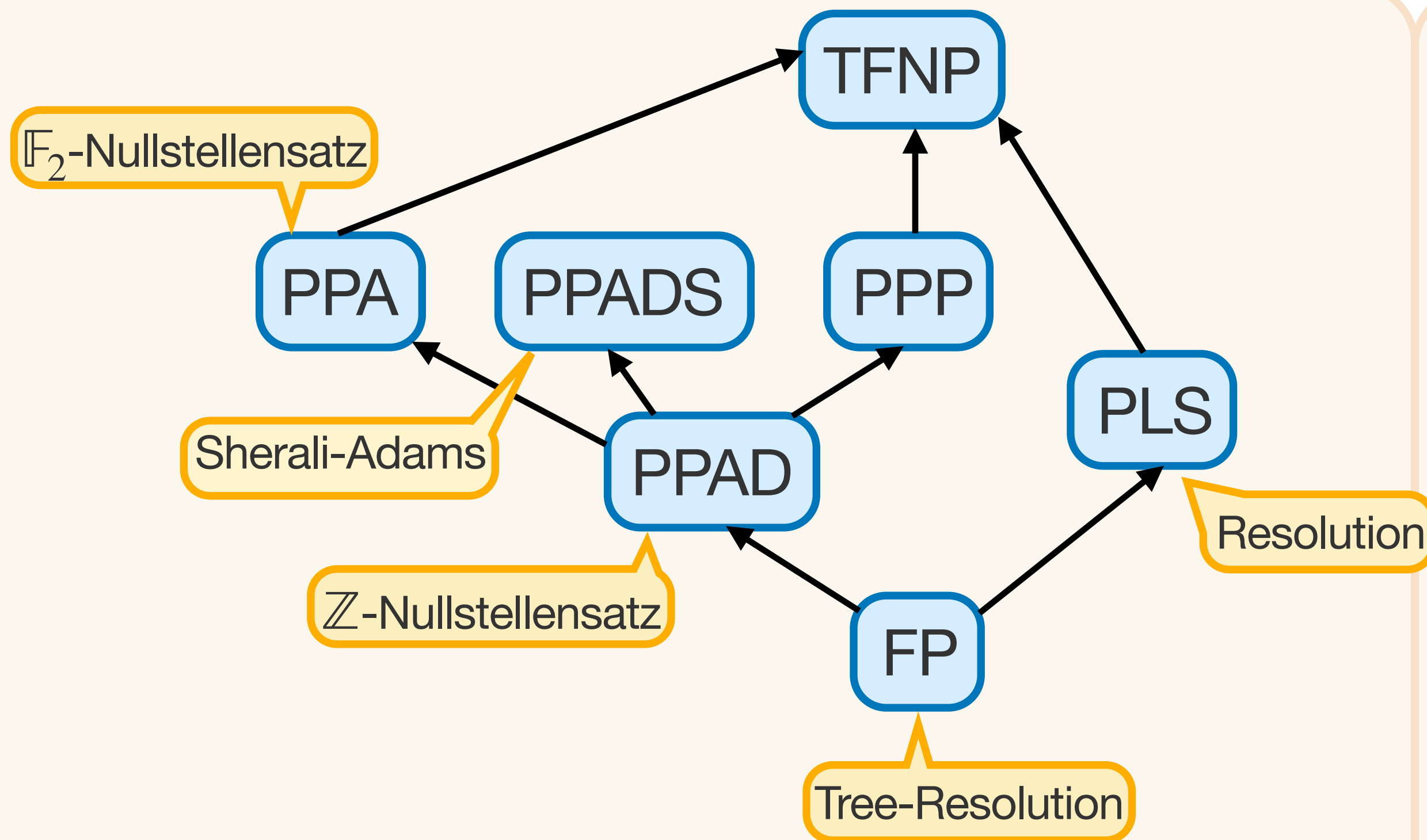
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Model of Computation: Decision Trees Communication Protocols

TFNP

[GKRS18] Certain circuit models are **equivalent** to communication TFNP classes!



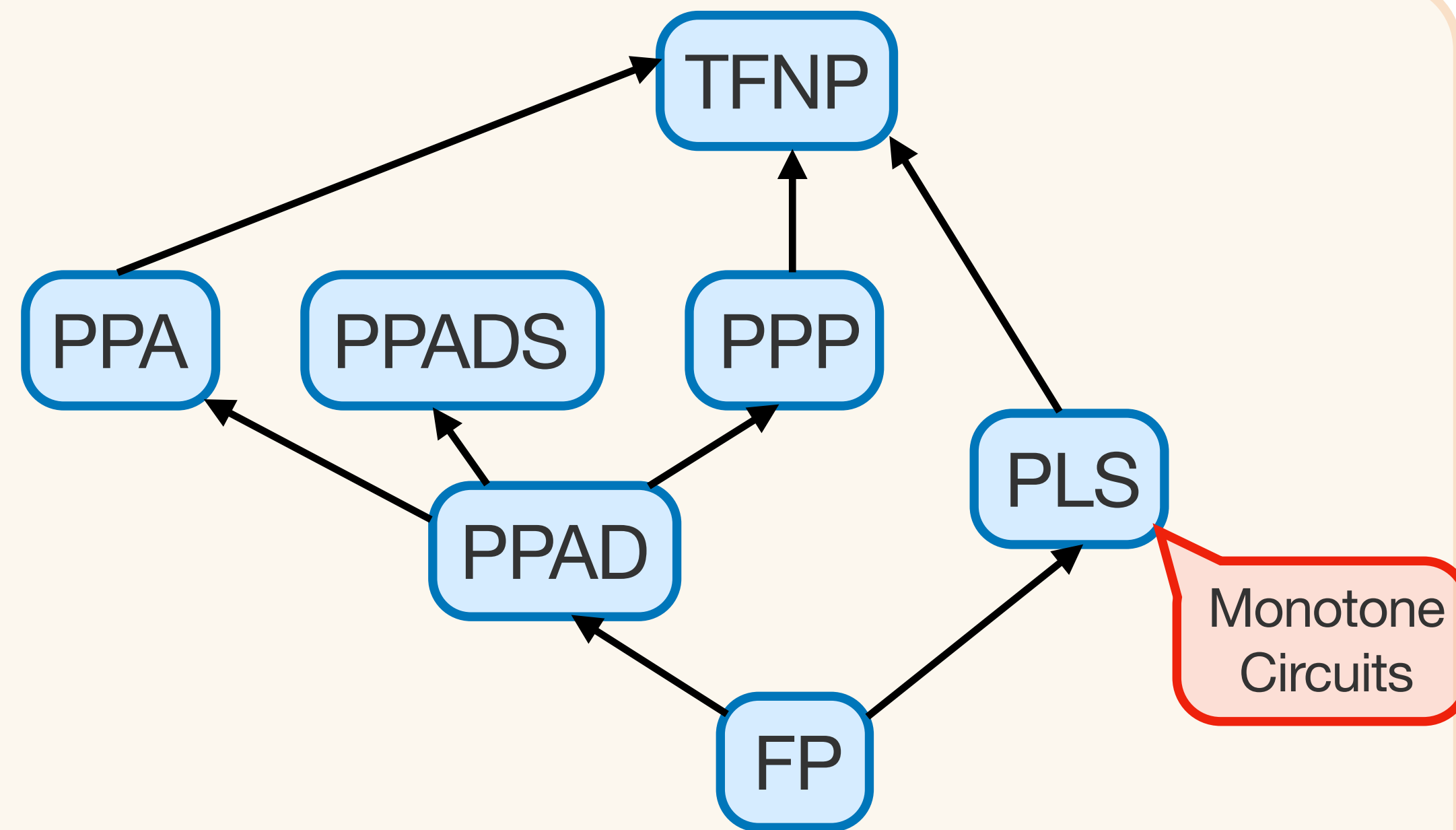
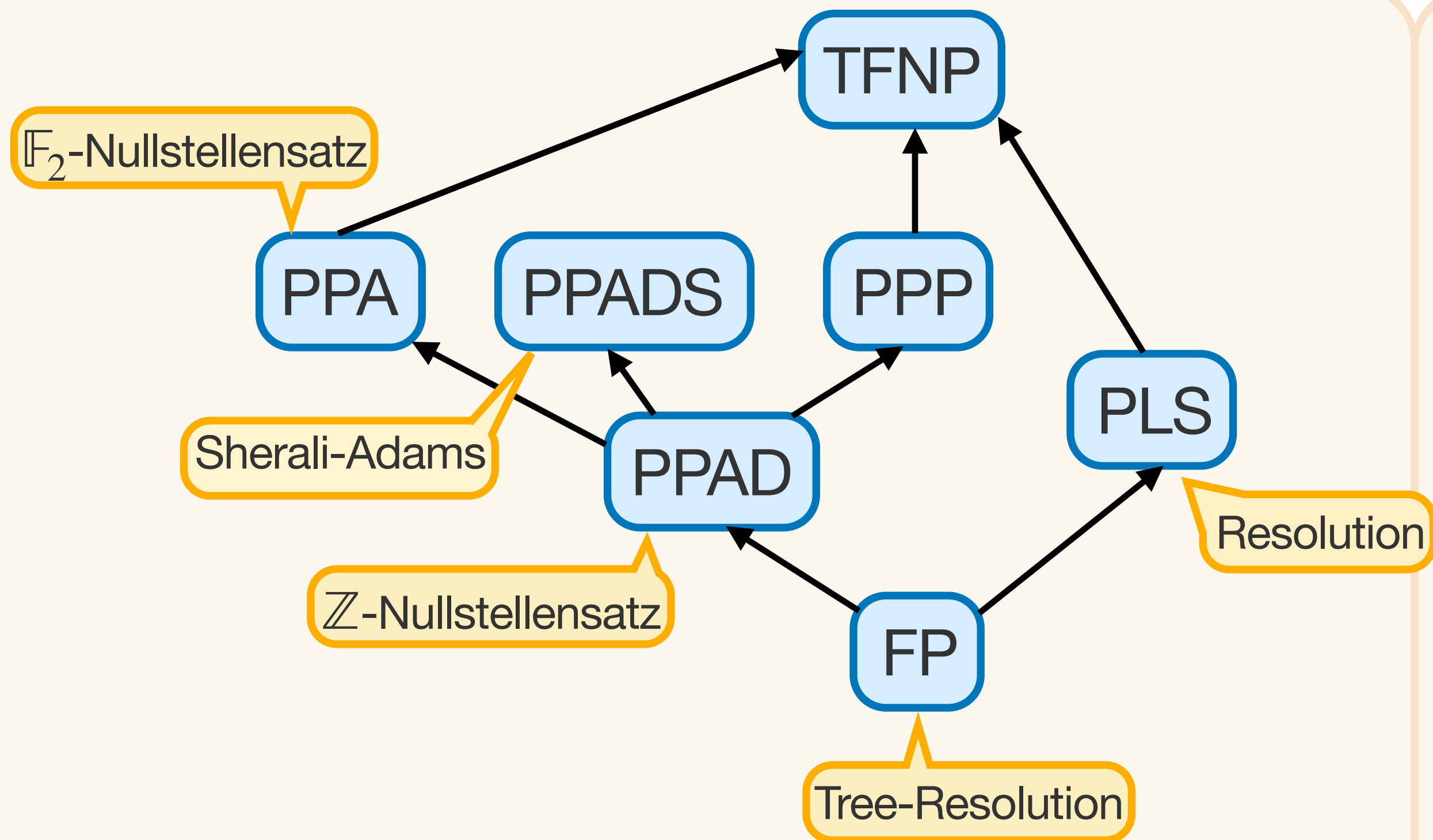
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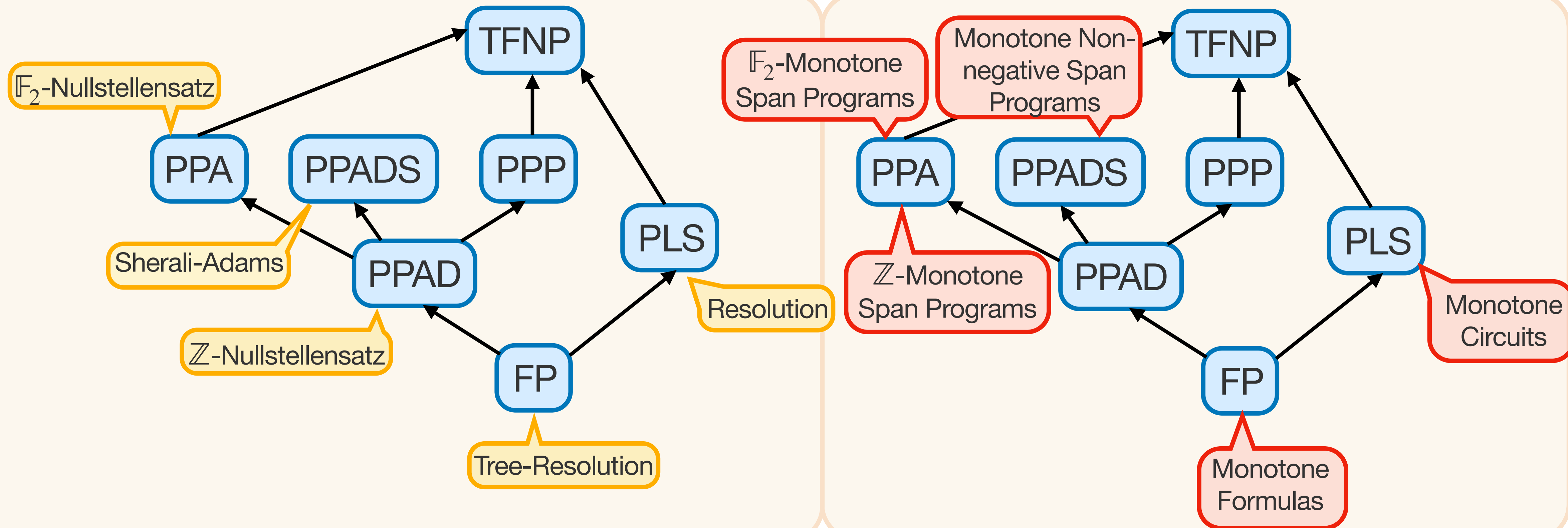
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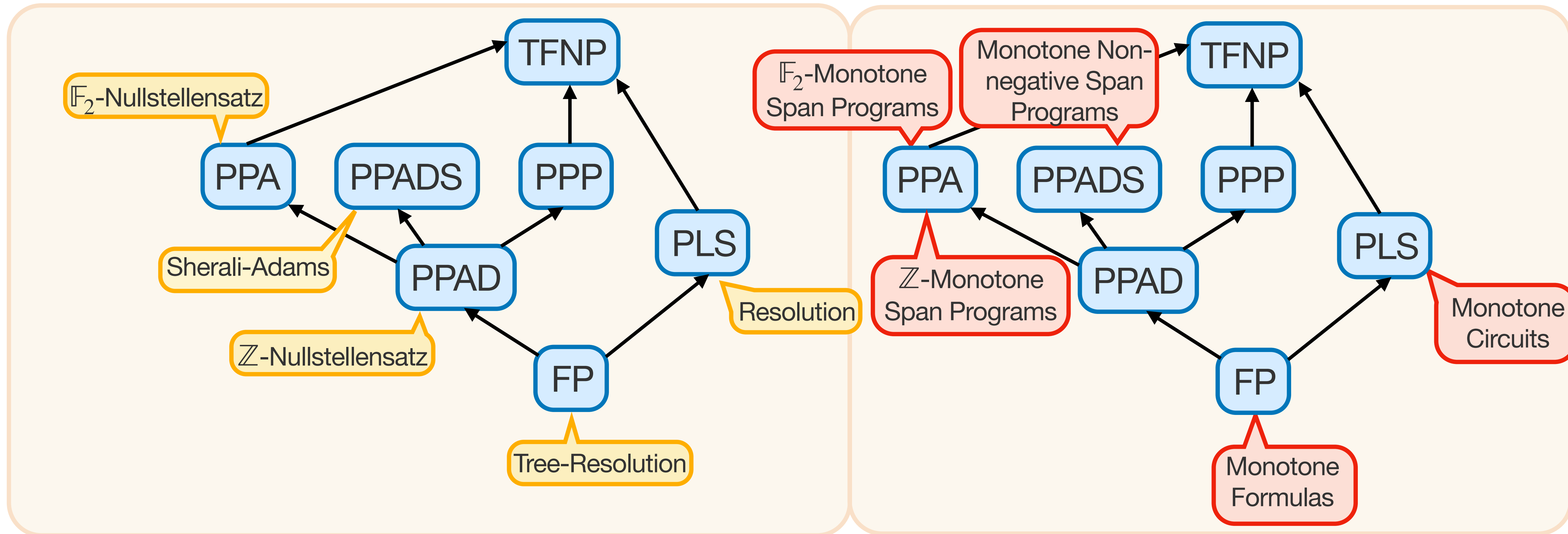
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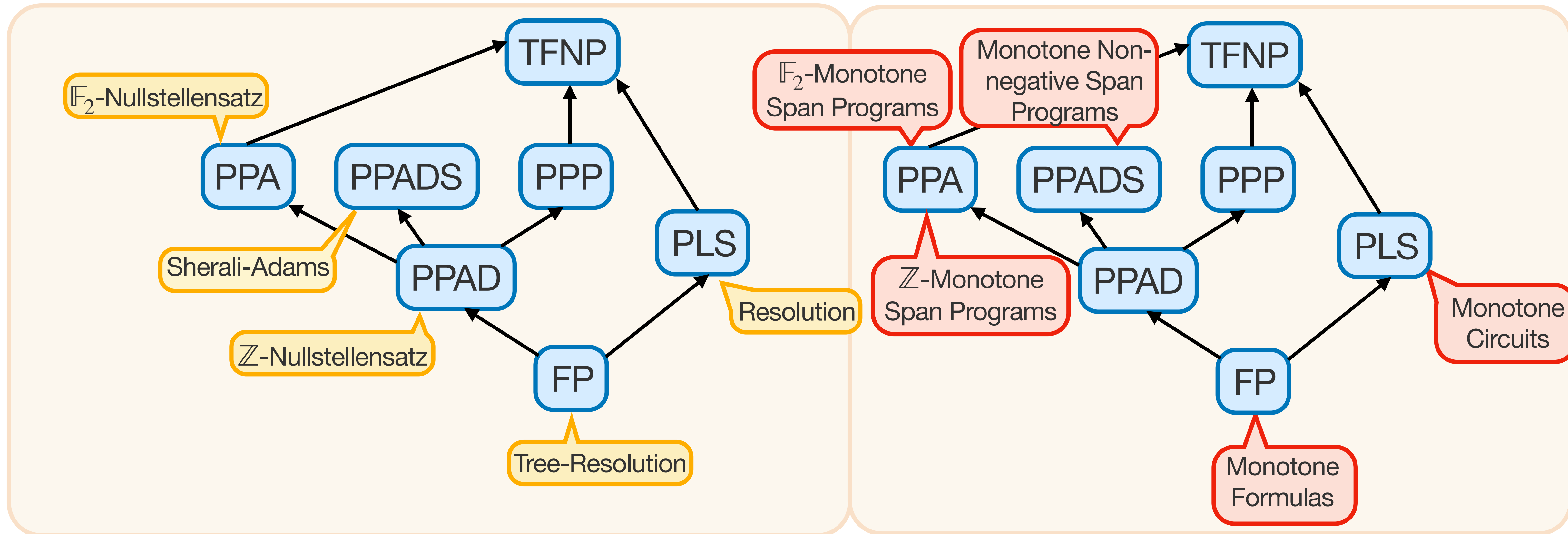


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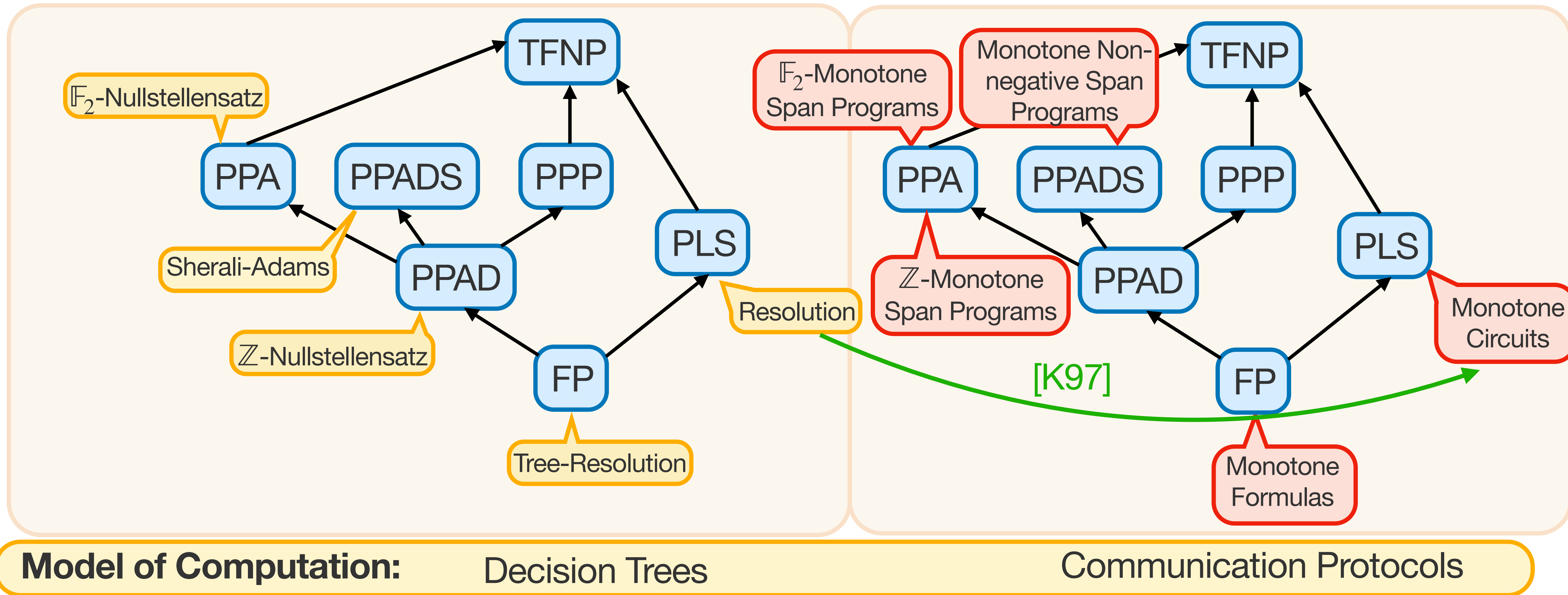


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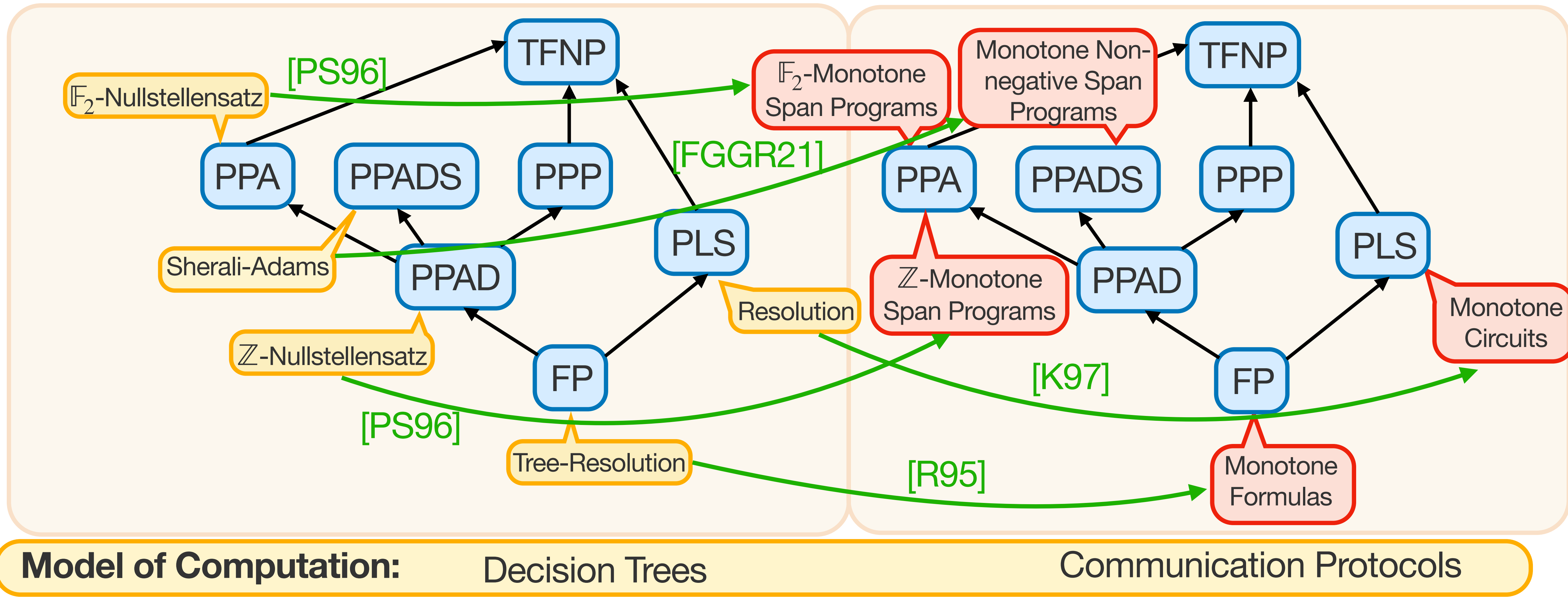
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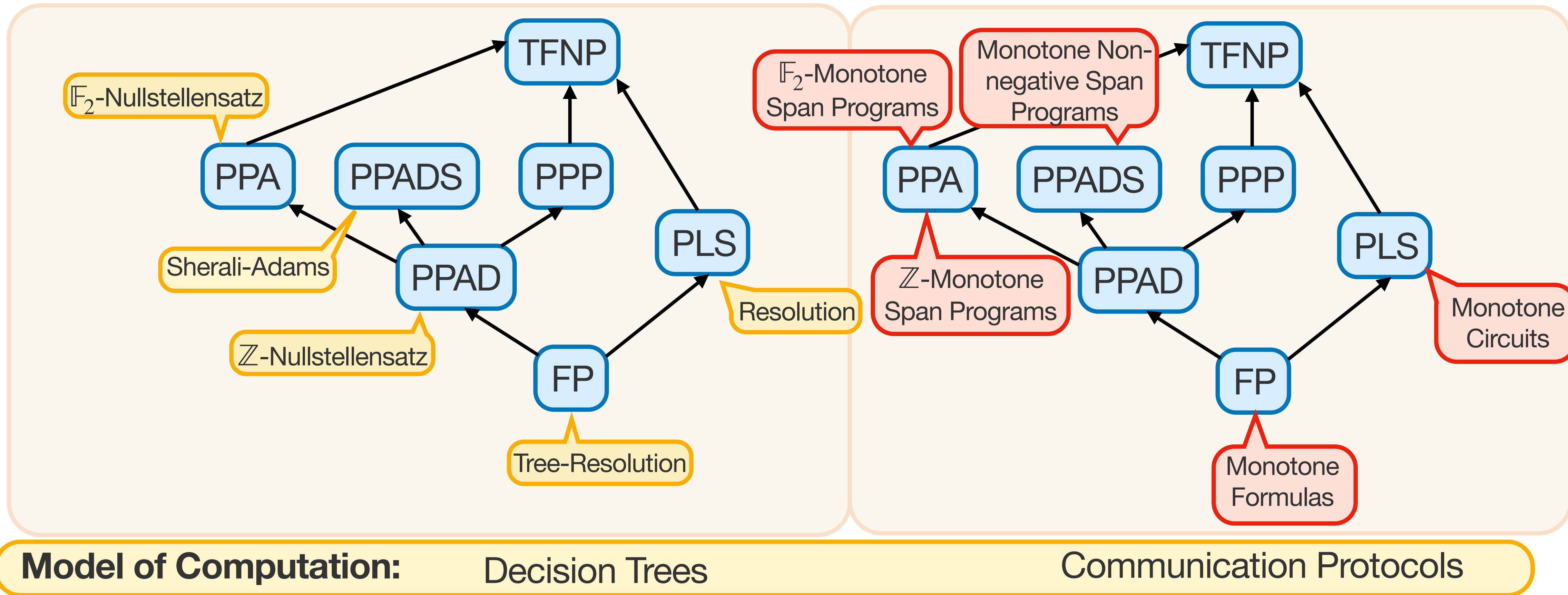
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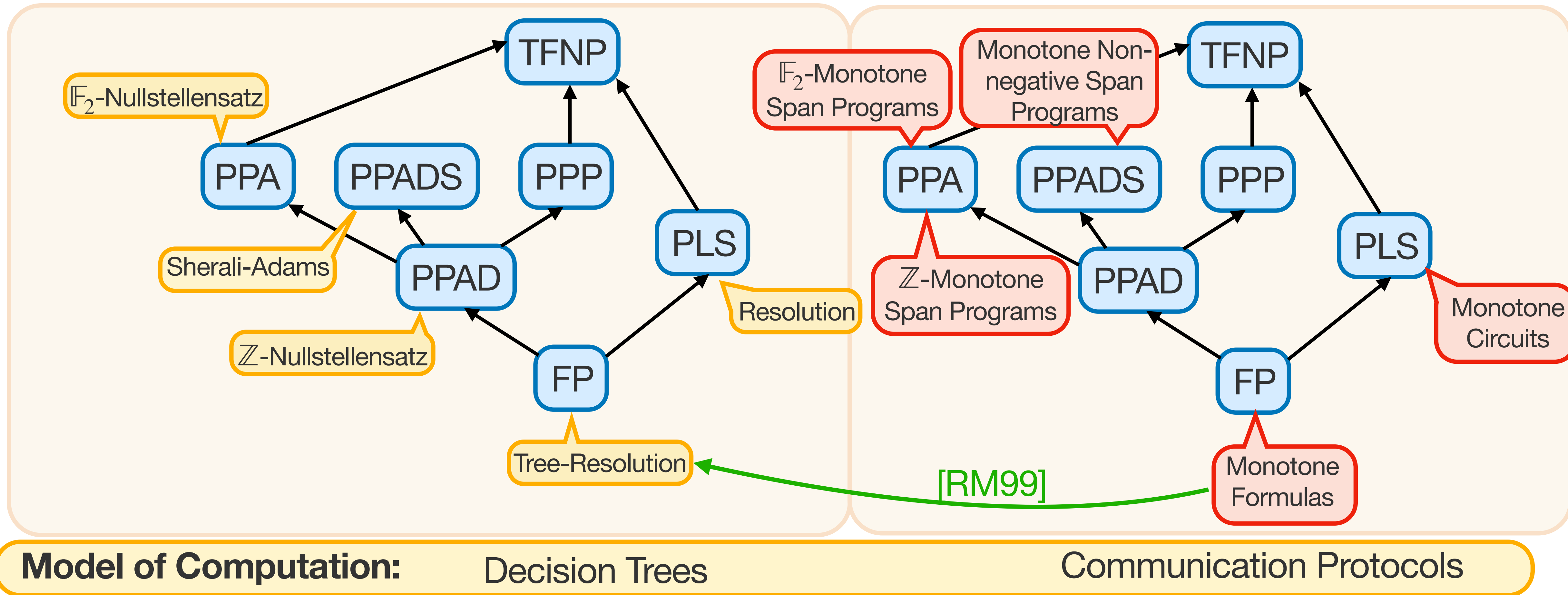
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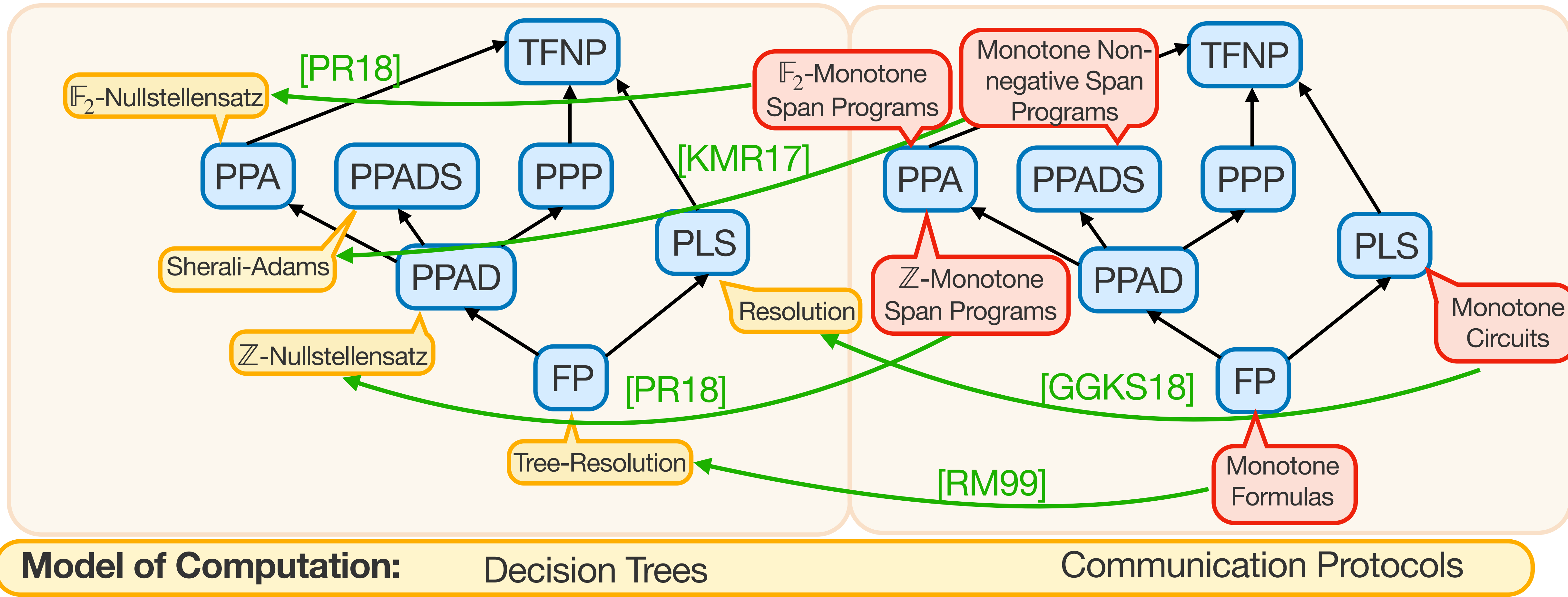
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Cook-Reckhow proof system — proofs are verifiable!

→ Just check that $(n', \{T_i\}, \{T_j^o\})$ describes a valid reduction!

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Standard proof systems satisfy this — e.g., Resolution, Sherali-Adams, Nullstellensatz...

Short Proofs of Soundness

Reflection principle for proof system P

$$Ref_{P,n,m,c} := Proof_P(F, \Pi) \wedge SAT(F, \alpha)$$

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Fix a standard encoding of SAT

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Short Proofs of Soundness

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→ Each generates a TFNP class as everything reducible to $Search_{Ref_P}$

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Π is low complexity \implies number of variables of Ref_P instance is not much more than that of F

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Corollary: A proof system admits a TFNP^{dt} characterization iff it is closed under decision tree reductions and has short proofs of a reflection principle about itself.

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A monotone function F such that

1. for any partial function g :

C **efficiently** computes $g \implies$ there is a string z such that $F \upharpoonright z(x) = g(x)$
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Q. Under what **conditions** does a monotone circuit model admit a TFNP characterization?

A. Iff the monotone circuit model C has a **universal family** of functions! (And closed under low-depth formula reductions).

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Open Problem

Q. A generic lifting theorem?

A circuit and proof system characterization of a TFNP class immediately implies an *interpolation theorem*. Does the same hold for *lifting theorems*?